



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 009. (A.P.)

Date: 03-07-2018

## CIRCULAR

All the III B.Tech (ME) students are hereby informed that A one day workshop on "Safety Awareness Programme" will be conducted on 11-07-2018. All interested students can enroll their names with Mr. B. Dharma Kumar, Asst. Professor, ME Dept on or before 07-07-2018. The details of topics and resource persons are available in brochure displayed in ME department notice board.

Venue : ME Department, Seminar hall.

  
HOD


  
PRINCIPAL

Circulate among III B.Tech students'

HODs are informed to circulate among concerned staff members

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Ch. Vinod Kumar

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Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTHAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 13-07-2018

## Program Report

**Name of theEvent :** Workshop on " SAFETY AWARENESS PROGRAMME".

**Date/Duration :** 11-07-2017 10:00 Am to 05:00 Pm.

**Resource Person :** Mr.K. Srinivasrao, Jayaram automobiles, Kakinada,

Ph. No.: 9440681047,

Mail Id :heromotorcorp.jayaram@gmail.com

**Name of Coordinator :** Mr. B . Dharma Kumar , Asst .Prof.

**Number of students attended :** 52

**Objective of the event :**

- Concentrates on general safety awareness.
- Safe working habits and safe operating procedures.
- Different hazards and machinery.

**Topics covered :**

- Workshop at employer & employee responsibility.
- Safety signs.
- Preventing accidents.

**Outcome of the event :**

The student will be able to

- Learn about the importance of safety signs and what the various types of signs.
- Learn how to prevent accidents in workshop and what should be done in case of emergency.

B. Dharma  
CO-ORDINATOR

  
Principal  
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VIDYUTNAGAR, KAKINADA

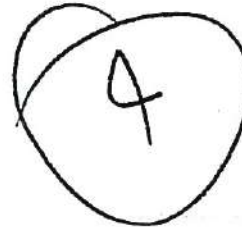
  
HOD

JAYARAM HERO

 Hero

11/07/2018

TO,  
THE PRINCIPAL,  
  
KAKINADA.



RESPECTED SIR

WE ARE AUTHORISED DEALER FOR HERO MOTOCORP LTD, TWO WHEELERS IN  
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WE HAVE TRAINED SAFETY ADVISOR TO EDUCATE THE STUDENTS ABOUT ROAD SAFETY WHICH  
IS VERY VERY IMPORTANT NOW A DAYS

SO WE REQUEST YOU TO GIVE US PERMISSION TO CONDUCT \* SAFETY AWARENESS  
PROGRAMME \* IN YOUR COLLEGE FOR YOUR STUDENTS

THANKING YOU

JAYARAM HERO

*K. Sundar Rao*

KAKINADA

  
Principal  
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VIDYUTNAGAR KAKINADA

**Jayaram Automobiles - Authorised Dealer, Hero MotoCorp Ltd.**

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hmcl.ap.kakinada.jayaramauto@gmail.com jayaram.kakinada@heromotocorp.biz

3-1 (Hexo)

Workshop on Safety awareness programme

Hallticket Number	Name
166K1A0301	ANISETTI NAVEEN DAYA SAGAR
166K1A0302	ANASURI VENKATA SANDEEP
166K1A0303	APPANA NAGA VENKATA MANIKANTA KISHORE
166K1A0304	BATTIVALASA GURU SAI CHARAN
166K1A0305	BATTINA SIDDHARTHAKUMAR
166K1A0306	BEJAWADA SIVAKALYAN
166K1A0307	BOGULA VEERA VENKATA DURGA LOKESHYADAV
166K1A0308	CHATRA MADHUBABU
166K1A0309	CHINTHALAPUDI RAMA KRISHNA
166K1A0310	CHUNDRU SRIRAM BHARAT
166K1A0311	DASARI RAJU
166K1A0312	DEVU RAVI TEJA
166K1A0313	DUPPADA JAGADEESH CHANDRA
166K1A0314	ERUGULA SURESH
166K1A0315	GANDROTHULA SRIRAM
166K1A0316	GODUGU RATNAKUMAR
166K1A0317	GUNIMANIKALA SRIKANTH
166K1A0318	HANUMANTHAVAJJULA RAGHAVENDRA KOUSHIK
166K1A0319	ILLINGI RAJU
166K1A0320	JANGAM RAMESH
166K1A0321	KORUKONDA EARNSH SHINE PAUL
166K1A0324	KANDA RAMESH
166K1A0325	KALADI RAMBABU
166K1A0326	KOLLA MANJUNADH
166K1A0327	KOMALLAPALLI SATYA SRINIVAS
166K1A0328	KOMMOJU SIVARAMA KRISHNA
166K1A0329	KOPPULA VEERA VENKATA SATYA SAI PRASAD
166K1A0330	KOTA VARAPRASAD
166K1A0332	MEDIBOINA VEERA VENKATA RAMANA
166K1A0333	MANDA VENKATA RAMA PADMANABHAM
166K1A0334	MOHAMMAD TAJUDDIN BASHA
166K1A0335	MOHAMMAD SHAH KAREEMUDDIN
166K1A0336	NAKKA SUBHASH CHANDRA
166K1A0337	PIDIMI SAIBABA
166K1A0338	PRAMIDALA VEERA VENKATA KISHORE
166K1A0339	PALEPU DURGA PRASAD
166K1A0340	PANDI ANAND
166K1A0341	POLAM MANIKANTA
166K1A0342	RYALI MOHANA SUMEETH
166K1A0343	SANA SWAMI NAIDU
166K1A0344	SANDINA PRUDHVI
166K1A0345	SANGANI RAJKUMAR
166K1A0346	TALLATAM VENKATASIVA RAM TEJA
166K1A0347	VASIPILLI AKHIL
166K1A0348	VULLUR SAI SOWMYA SREE
166K1A0349	VOLETI VENKATA RAMYA
166K1A0350	VEMPADA DURGA PRASAD
166K1A0351	VITHANALA CHAITANYA CHARAN RAJU
166K1A0352	VOLETI KAMESWARA RAO

52

  
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IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 25-07-2018

## CIRCULAR

All the II B.Tech (ME) students are hereby informed that **A two days workshop on "Thermodynamics"** will be conducted on 02-08-2018 & 03-08-2018 . All interested students can enroll their names with Mrs. R. Vijaya lakshmi, Asst. Professor, ME Dept on or before 29-07-2018. The details of topics and resource persons are available in brochure displayed in ME department notice board.

Venue : ME Department, Seminar hall.

  
HOD

  
PRINCIPAL

Circulate among II B.Tech students

HODs are informed to circulate among concerned staff members

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VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 06-08-2018

## Program Report

**Name of theEvent :** Two days workshop on OVERVIEW ON THERMODYNAMICS.

**Date/Duration :** 02-08-2018,03-08-2018|10:00 Am to 05:00 Pm.

**Resource Person :** Mr.V.Sivaprasad, Coaching center, Hyderabad,

Ph. No.:9515718191,

Mail Id :sivakkd26@gmail.com

**Name of Coordinator :** Mrs. R. Vijayalakshmi , Asst .Prof, ME Dept.

**Number of students attended :** 72

**Objective of the event :**

- To provide basic knowledge of work and heat transfer.
- To explore the present trends in thermodynamics.
- To give students to concepts of entropy.

**Topics covered :**

- Work and heat transfer.
- First law of thermodynamics applied to (i) closed system (ii) open system.
- Concept of entropy.

**Outcome of the event :**

The student will be able to

- Describe the present trends in second law of thermodynamics.
- Student will understand and apply knowledge on properties of steam, (raukine cycle-use of steam tables).

  
CO-ORDINATOR

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

  
HOD

### ABOUT THE COLLEGE

Ideal Institute of Technology was established under the aegis of Governing Body of ideal college of Arts and sciences during academic year 2009-2010. It is being running under the able guidance of Dr.P. Krishna Rao garu Chairman of ideal Institute of Technology and Pragati Engineering College. The Education Society is the outcome of honored philanthropists, educationists, intellectuals and elite persons of Kakinada such as Sri P.V.N.Raju, Dr.N.S.R.Sastry, Dr.P.Chiranjeevini Kumari. Ideal Institutions are of its own kind in rendering service to society and value based education. During early seventies Kakinada used to have only two colleges. Student community used to suffer a set-back to upgrade their education even to the level of intermediate. The Ideal Junior college flourished into Degree college and P.G. course like MBA, MCA, MA, M.Sc. And Engineering College. Ideal Institute of Technology is one and only institute which is located in the midst of Kakinada.

### ABOUT MECHANICAL DEPARTMENT

The department is strengthened with one Doctorate, nine Post Graduates as Out of which two staff members have industrial experience in reputed organizations. Machine Design, Piping & Inspection. Not only have the teaching staff, the technicians also had tremendous technical experience. The department is equipped with ISO certified laboratory equipment and sophisticated teaching aids like day light projector, e-Class room etc.

### THEME OF THE LECTURE

This lecture will provide a platform for academicians, Researchers, Scientists and industry personnel from industry to come together and share the latest developments in broad areas of Mechanical Engineering.


The concept of heat arises from that particular sensation of warmth and coldness which is immediately experienced on touching a body. This direct sensation, however, furnishes no quantitative scientific measure of a body's state with regards to heat; it yields only quantitative results, which vary according to external circumstances.

Although the freezing of liquids and melting of crystals are fundamental for many areas of the sciences, even simple properties like the temperature-pressure relation along melting line cannot be predicted to day. However we can present a theory in which properties of coexisting crystal and liquid phases at a single thermodynamic state point provide basis for calculating the pressure, density and entropy.

### RESOURCE PERSON

1. Sri V Siva Prasad,  
B.Tech, M.Tech (IIT MADRAS)  
Faculty ACE coaching center, Hyderabad

Mobile No-

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

## AN OVER VIEW ON THERMODYNAMICS

2, 3<sup>rd</sup> August 2018



2

Organized by

Department of Mechanical Engineering

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&

Vidyut Nagar, Kakinada, E.G.Dt.,  
Andhra Pradesh, India- 533 003  
Ph:

Fax: 08852-252232

Website: [www.idealtech.edu.in](http://www.idealtech.edu.in)

Email: [principal@idealtech.edu.in](mailto:principal@idealtech.edu.in)

NATIONAL SEMINAR  
ON  
RECENT TRENDS IN MECHANICAL  
ENGINEERING

2, 3<sup>rd</sup> August 2018

REGISTRATION FORM

(Make copies if desired)

Name : \_\_\_\_\_

Date of Birth : \_\_\_\_\_

Designation : \_\_\_\_\_

Department : \_\_\_\_\_

Organization : \_\_\_\_\_

Address for Communication:  
\_\_\_\_\_  
\_\_\_\_\_

Phone : \_\_\_\_\_

E-mail : \_\_\_\_\_

DD No : \_\_\_\_\_

Date: \_\_\_\_\_

Signature of the  
Applicant

**ORGANISING COMMITTEE**

**Chief Patron**

Dr. Paruchuri Krishna Rao  
*Chairman*

**Patrons**

Dr.P. Chirajeevini Kumari  
*Secretary and Correspondent*  
Sri P.Kiran  
*Treasurer*

**Coordinator'**

Dr. T. Srikanth  
*Principal & Professor, Dept. of ME*

**Co-Coordinator**

Mrs.K.Aruna  
*Head, Dept. of ME*

**Advisory Committee**

Dr.G.Krishnam Raju, *Director*  
Dr.Ch.V.V.Satyanarayana Murthy  
Dr. Srihari Varma  
Mr. Bh.Ayyappa Kumar  
Mr. Ch.Vinod Kumar  
Mr. M.S.R.S. Prasad

**Organizing Committee**

Mr.M.Deceepya  
Mr. N. Raja  
Mrs.R. Vijaya Lakshmi  
Mrs.J L Prathyusha  
Mr.B.Dharma Kumar  
Mr.K.S.Rambabu  
Mr.N.Krishnakanth

**ATTENDEES**

This seminar is intended for Academicians,  
Researchers, Industry Personnel and PG  
Students

REGISTRATION FEE: *NIL*

**IMPORTANT DATES:**

Submission of Application : 27-07-18  
Intimation of Selection : 28-07-18  
Confirmation by Participant : 30-07-18

**MODE OF PAYMENT**

Participants are requested to send their filled  
registration form along with Demand Draft drawn in  
favor of "Ideal Institute of Technology," payable at  
Kakinada. Spot payment is allowed, but registration  
must be confirmed earlier.

**MAILING ADDRESS**

The Coordinator,  
Department of ME  
Vidyut Nagar, Kakinada, East Godavari District,  
Andhra Pradesh, India-533003  
[principal@idealtech.edu.in](mailto:principal@idealtech.edu.in)

*For any further clarifications please feel free to  
contact*

Mrs.K. Aruna  
Head of the Department  
Department of ME  
Coordinator - +91 9949305868

2-1 (TD)  
A two days workshop on Non Destructive Test

Hallticket Number	Name
176K1A0301	AKKAPALLI ABHISHEK GOUD
176K1A0302	BODDU GIREESH BHAVANI SANKAR
176K1A0303	BODDU KRISHNA SATYA SAI SUNIL
176K1A0304	BOLLU NAGA KUCHELU
176K1A0305	BONTHU RAJESH
176K1A0306	CHALAMALASETTY SAI KRISHNA
176K1A0307	CHODABATHULA VEERABABU
176K1A0308	DOPPA SAI TEJA APUROOP
176K1A0309	GINJALA VEERA SIVA SHANKAR
176K1A0310	GURTHURTHI MANIKANTA ESWAR
176K1A0311	IREDDY VINAY
176K1A0312	IJJAPU NAGENDRA
176K1A0313	ILLA PARVATHIDEVI
176K1A0314	JAKKALA AKHILESH HIMANTH
176K1A0315	KALLA HITESH RAM
176K1A0316	KARRI NAGADEEPIKA
176K1A0317	KASILANKA SIVA CHAKRADHAR
176K1A0318	KETHA BHAVANI PRASAD
176K1A0319	KOLIMALLA GANGADHAR
176K1A0320	KOLLABATHULA AKHIL
176K1A0321	KOLLU BHARAT VIJAY
176K1A0322	KURASALA PRADEEP
176K1A0323	MADDULA JASWANTH RATNAKAR
176K1A0324	ANUSURI SANDEEP SWAMY
176K1A0325	MALAKALA PAVAN KALYAN
176K1A0326	MALIPEDDI MUTHYA LOKESH
176K1A0327	MATTAPARTHI VENKATA PRADEEP
176K1A0328	MEDISETTI TEJA VENKATA MANIKUMAR
176K1A0329	MEESALA VENKATESH
176K1A0330	MOKA PRADEEP
176K1A0331	NAKKA NAGA LEELA VENKATA AJAY
176K1A0332	PABBINEEDI DINESH
176K1A0333	PILLI NAGASAI
176K1A0334	PILLI SHIVARAJU
176K1A0335	PAPPU DEVI SATYA RAJA VIGNESH
176K1A0336	POTHU APARNA
176K1A0337	RAPAKA SAKHI
176K1A0338	POTNURI UMA MAHESWARA RAO
176K1A0339	REDNAM VEERENDRA LAKSHMAN SIVA SRINIVAS
176K1A0340	SATYADI WILLIAM CHARLEY
176K1A0341	SANA SRIRAM
176K1A0342	SEELAMANTHULA VIJAY
176K1A0343	SEELI SANDEEP SAGAR
176K1A0344	SEERAM KARUN KUMAR
176K1A0345	SHAIK HYDER
176K1A0346	SIDDIREDDI SRI SATYA SAI PRASANTH
176K1A0347	VASIREDDY MANIKANTA
176K1A0348	UMMIDI GANGADHAR
176K1A0349	UMMIDI JOSEPH

  
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176K1A0350	VANGALA LAKSHMANACHARYULU
176K1A0351	VARA ROSE JESSY ANAND
176K1A0352	YENDAMURI LAKSHMI SAI RAJ VAMSI
176K1A0353	VASA SRIHARSHA
176K1A0354	KOTIKALAPUDI CHARAN KUMAR
186K5A0301	ADIREDDY RAJESH
186K5A0302	CHELIKANI MANIKIRAN
186K5A0303	GIDU'URI HEMANTHSAISRIHARI
186K5A0304	GUBBALA APUROOP
186K5A0305	KALIDINDI PRASUNA
186K5A0306	KARRI TEJA
186K5A0307	KATTA JANAKI RAMAIAH
186K5A0308	KUDELLI NAVEEN KUMAR
186K5A0309	MALIREDDY RAMU
186K5A0310	MALLIPUDI ANIL KUMAR
186K5A0311	MOGALATURTHI VEERABABU
186K5A0312	NEELAM TABITHA PRIYANKA
186K5A0313	PULI MAHALAKSHMI
186K5A0314	SIRIKI JAGADEESHBABU
186K5A0315	PATCHIPALA VENKATESWARARAO
186K5A0316	GUBBALA SAITEJA
186K5A0317	MANTHA NAGA DURGA PRASANTH
186K5A0318	SALADI BHANU SAI AVINASH

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# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

4

Date: 30-11-2018

## CIRCULAR

All the II B.Tech (CSE) students are hereby informed that A One day workshop on "Computer Organization" will be conducted on 07-12-2018. All interested students can enroll their names with Mrs. R. V. V. G. Lakshmi, Asst. Professor, CSE Dept on or before 11-12-2018. The details of topics and resource persons are available in brochure displayed in CSE department notice board.

Venue : CSE Department, E-Class Room.

  
HOD

  
PRINCIPAL

Circulate among II B.Tech students

HODs are informed to circulate among concerned staff members

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Ch. Venkata Kumar







  
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VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 11-12-2018

## Program Report

**Name of the Event :** A one day workshop on COMPUTER ORGANIZATION.

**Date / Duration :** 07-12-2018 | 10:00 AM to 05:00 PM.

**Resource Person :** Dr. M. Radhika Mani, Associated Prof, CSE, Pragathi Engineering college

Ph.No. : 94404 84800

Mail Id : radhika\_madireddy@gmail.com

**Name of Coordinator :** Mrs.R.V.V.G.Lakshmi, Asst. Professor , CSE Dept.

**Number of students attended :** 52

**Objective of the event :**

To understand the Structure function and characteristics of Computer System. To understand design of various functional units and components of computer.

**Topics covered :**

1. Basic Computer Instructions.
2. Instruction Design and Format.
3. Computer Arithmetic.

**Outcome of the event :**

- To understand the Structure, function and characteristics of Computer System.
- Design various functional unit and components of computer.

  
CO-ORDINATOR

  
HOD

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

## Learning Goals

1. To Learn that Memory System for computers uses a hierarchy of Memories appropriately organized to Provide applications cost effective overall memory with high capacity and fast access.

2. To Identify that RAM uses DRAMs which are low cost, high capacity solid state memory. Their Access time is around 50ns. To get a Faster Access, it is combined with a small SRAM memory called cache memory. Cache is smaller but faster than DRAM main memory. The Management of main memory is critical to the computer system.

3. To Analyze how Programs usually follow a pattern of Memory access in which Instructions stored in a small range of addresses are frequently referenced. Instruction accesses and Data Accesses are clustered in two distinct range of addresses. This is called Locality of Reference. This fact is used to keep frequently referenced instructions and data in small, fast memories called caches. Normally there is a Data cache and an Instruction Cache.

4. To Learn a commonly used method in the design of memory systems called Virtual Memory. A virtual memory provides a user with a large logical memory address space to write programs even though the available physical memory (DRAM) may be small. This is achieved by Combining DRAM and a magnetic memory in such a way that the memory cycle time of the combined system is closer to DRAM cycle time while a large storage space provided by a magnetic disk.

5. To learn how computers use both cache and virtual memory effectively for memory management. Hardware and Operating System cooperate to achieve this.

6. To learn Various methods of connecting I/O Devices to the CPU-memory system of a computer and also various challenges in I/O organization arises due to the fact that there is a large variety of I/O devices which vary in their speeds and data formatting.

7. To learn various methods used to transfer data to/from I/O Devices to the main memory. They are program controlled data transfer, Interrupt Controlled data transfer and DMA based data transfer.

DAY 1	Memory and Input/Output Organization
-------	--------------------------------------

### Program Committee

#### Chief Patron:

Dr. P. Krishna Rao

#### Chairman

#### Patron:

Dr. P. Chiranjeevi Kumari

#### Secretary & Correspondent

#### Principal:

Dr. J. Srikanth

#### Convener:

Mr. M.S.R.S. Prasad, Assoc. Prof.

#### HoD, CSE

#### Coordinators:

Smt. S. Surya Sri, Asst. Prof.

Smt. R.V.V.G. Lakshmi, Asst. Prof.

Smt. S. Indira Spandana, Asst. Prof.

Mr. R.S.V.V. Prasad Rao, Asst. Prof.

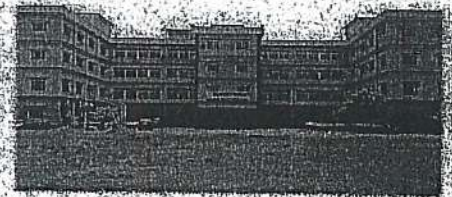
Mrs. N.D. Deepthi Priya, Asst. Prof.

Mr. M.K. Pran Kumar, Asst. Prof.

Smt. M. Jayasree, Asst. Prof.

Smt. S. Jyothirmayee, Asst. Prof.

Ms. P. Sri Sawjanya, Asst. Prof.



## WORKSHOP ON COMPUTER ORGANIZATION

07<sup>th</sup> December 2018

Organized by  
Department of CSE

*(Signature)*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR, KAKINADA

## About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

The Education Society is the outcome of honoured philanthropists, educationists, intellectuals and elite of Kakinada such as (Late) P. V. N. Raju, Dr. N. S. R. Sastry, Dr. P. Chitanjevi Kumar. Ideal institutions have started from school and blossomed into group of colleges. Ideal Institutions are of its own kind in rendering service to society and value-based education.

During early seventies Kakinada used to have only two colleges. Student community used to suffer a setback to upgrade their education even to the level of intermediate.

The ideal junior college had been started to cater to the needs of student community. In due course the jr. college flourished into Degree college and P.G courses like M.B.A., M.C.A., M.A., M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P. Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job-potentials of the students.

Ideal institute of technology is one and only institute which is located in the midst of Kakinada amid mango groves in the lap of Nature.

In this present era of knowledge and technology driven economy, globalization and liberalization, every

employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure.

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

We seek to develop in each member of the Ideal community the ability and passion to work wisely, creatively and effectively for the betterment of mankind.

The Institute extends moral and placement support too. Value based education, mentoring and character building, job oriented short term courses, guest speakers from the industry, visits to industry houses, Entrepreneurship Development Programmes are our main stay. Students live in a unique environment that promotes culture, sports, social contributions, and above all the self-governance.

The institute is always abuzz with new ideas and is an exciting place to be. If you happen to be dreamer.

The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Computer Organization

Computer Organization refers to the operational units and their interconnections that realize the Architectural Specifications. Organizational Attributes include those hardware details transparent to the programmer, such as control signals, interfaces between the computer and peripherals, Types and Capacity of Memory, Register Structure and Instruction set. It Answers the Question "How Does a Computer Work?" basically from Programmer's Point of View. Thus with the knowledge of Computer Organization, a Computer Designer refines these Component Specifications and Implements them using Hardware, Software and Firmware elements.

## About Workshop

The motivation behind this workshop is to give Hands-on learning experience to the students in learning basic parameters in Designing Memory systems for the Computer systems and to learn several Techniques that are used in Practical Architectures to enhance the I/O system Performance.

# A one day workshop on Computer Organization

IDEAL INSTITUTE OF TECHNOLOGY

II.B.TECH BRANCH : CSE BATCH:2017--2021

S.NO	REG.NO	STUDENT NAME
1	176K1A0501	ADABALA LIKHITA CHANDANA
2	176K1A0502	ADARI SUVARNA LAKSHMI
3	176K1A0503	ALLURU SUBHA MAHESWARI
4	176K1A0504	ANUSURI SRIKRISHNA
5	176K1A0505	BURRE SRI SAI LOKESH
6	176K1A0506	BAVARISSETTY PRAVALLIKA DEVI
7	176K1A0508	CHANDRAMALLA SUNIL KUMAR
8	176K1A0509	CHINTAPALLI BHAVANI ANUSHA
9	176K1A0510	DADALA JAYAKAR
10	176K1A0511	DARA LAKSHMI NARAYANI VANGMAYI
11	176K1A0512	DEVU VEERA VENKATA SATYANARAYANA SWAMY
12	176K1A0513	DRONAMRAJU KRISHNA CHAITANYA
13	176K1A0514	DUDDI VARAPRASAD
14	176K1A0516	GUNNADA LAKSHMI LAVANYA
15	176K1A0517	GUTALA MANIKYA PADMA SAHITHI
16	176K1A0518	GUNDU SAI SAMHITHA
17	176K1A0521	IMANDI PRUDVI KUMAR
18	176K1A0524	KARNASULA APARNA
19	176K1A0525	KASI MADHURI
20	176K1A0526	MAGAPU RAGHURAM
21	176K1A0527	KOPPISETTI VINAY KUMAR
22	176K1A0528	MEESALA BHARGAVA SRIRAJ
23	176K1A0529	MAILIPILLI NAVYA RANI
24	176K1A0533	NAMADI MELIMA
25	176K1A0535	PABBINEEDI GANGA BHAVANI
26	176K1A0536	DODDIPATLA GANGA SINDUJA
27	176K1A0537	PETTELA SWETHA
28	176K1A0539	PALIKA SURYA SRI
29	176K1A0540	PASUPULETI SREEVENI POOJITHA
30	176K1A0542	POSIMSETTI DIVYA SREE
31	176K1A0543	SWARNA NAGA SIRISHA
32	176K1A0544	SAGI SIVANI
33	176K1A0545	SOLLETI SRI HARSHA PRIYA
34	176K1A0547	SABBINA SURYASARANYA
35	176K1A0548	SHAIK BASHEER
36	176K1A0550	TIRUMALASETTI SAI MEGHANA
37	176K1A0551	URIVITI SAI LAKSHMI CHELSEA
38	176K1A0553	VADDI GANGOTRI
39	176K1A0557	KADIYALA SURESH
40	176K1A0558	VILAPARTHI NAGA VEERA SESA SWAROOP
41	176K1A0559	GOLLAPALLI DINESH RAJA
42	17S01A0511	MUDUNURU HIMA SAHITHI
43	186K5A0501	AAKULA ALEKYA
44	186K5A0502	GORUSETTI MURALI
45	186K5A0503	GUBBALA SARITHA
46	186K5A0504	JAGU VEERABHARGAVI
47	186K5A0506	KANDREGULA PRAVEEN KUMAR
48	186K5A0507	MEDISETTI RATNA SURYA DEVI
49	186K5A0510	PENKE HEMA VENKATA DURGA LAKSHMI
50	186K5A0511	UDATHA JYOTHI
51	186K5A0512	VITHANALA KEERTHI KALYANI
52	186K5A0513	KALADI PRASANNA KUMAR

Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

5

Date: 03-12-2018

## CIRCULAR

All the II B.Tech (CSE) students are hereby informed that A Two days workshop on "Software Engineering" will be conducted on 11-12-2018 & 12-12-2018. All interested students can enroll their names with Mrs. S. Jyothirmayee, Asst. Professor, CSE Dept on or before 07-12-2018. The details of topics and resource persons are available in brochure displayed in CSE department notice board.

Venue : CSE Department, E-Class Room.

  
HOD

  
1.8/3/12/18  
PRINCIPAL

Circulate among II B.Tech students

HODs are informed to circulate among concerned staff members


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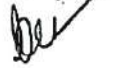
AO 

HOD - CE 

HOD - EEE Ch. Vinod Kumar

HOD - ME 

HOD - ECE 

HOD - H&BS 

  
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IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 14-12-2018

## Program Report

**Name of the Event :** A one day workshop on SOFTWARE ENGINEERING.

**Date / Duration :** 11-12-2018 to 12-12-2018 | 10:00 AM to 05:00 PM.

**Resource Person :** Dr. P. Vamsi Krishna Raja, prof in CSE dept.

Director, Innovations & incubations,

Swarnandhra college of engineering & Technology(Autonomous),Narsapuram.

Ph.No. : 63008 12123

Mail Id : [drpvkrajju@swarnandhra.ac.in](mailto:drpvkrajju@swarnandhra.ac.in)

**Name of Coordinator :** Mrs. S. Jyothirmayee, Asst. Professor , CSE Dept.

**Number of students attended :** 52

**Objective of the event :**


1. Reliability- an attribute of software quality.
2. A tool or methodology to assist software developers in reaching the goals.


**Topics covered :**

- Databases
- Programming tools.
- Libraries.

**Outcome of the event :**

An ability to identify formulate and solve complex engineering problems by applying principle of Engineering, Science and Mathematics.

  
CO-ORDINATOR

  
Principal  
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VIDYUTNAGAR KAKINADA

  
HOD

programmers, as managers, etc. Therefore most of the issues in Software Engineering are concerned with interactions among people, rather than with the production of software.

### About Workshop

The main motivation of the workshop is to give hands on experience to the faculty on software engineering and its data models .How to make decisions about systems and testing techniques.

### Learning Goals

- a) An ability to apply knowledge of mathematics, science, and engineering.
- b) An ability to design and conduct experiments, as well as to analyze and interpret data.
- c) An ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability.
- d) An ability to function on multi-disciplinary teams.
- e) An ability to identify, formulate, and solve engineering problems.
- f) An understanding of professional and ethical responsibility.
- g) An ability to communicate effectively.
- h) The broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context.
- i) A recognition of the need for, and an ability to engage in, life-long learning.
- j) A knowledge of contemporary issues.
- k) An ability to use the techniques, skills, and modern engineering tools necessary for engineering practice.

DAY 1	Software engineering goals Objectives Models and uses of all system models
DAY 2	White box testing Black box testing Levels of testing and all other testing techniques.

### Program Committee

#### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

#### Patrons:

Dr. P. Chiranjeevini Kumari  
Secretary & Correspondent

#### Principal:

Dr. T. Srikanth

#### Convenor:

Mr. M.S.R.S.Prasad, Assoc. Prof.  
HoD, CSE

#### Coordinators:

Smt. S. Surya Sri, Asst. Prof.  
Smt. R.V.V.G.Lakshmi, Asst. Prof.  
Smt. S. Indira Spandana, Asst. Prof.  
Mr. R.S.V.V.Prasad Rao Asst. Prof.  
Ms. N.D.Deepti Priya, Asst. Prof.  
Mr. M.Kiran Kumar, Asst. Prof.  
Smt. M. Jayasree, Asst. Prof.  
Smt. S.Jyothirmayee, Asst. Prof.  
Ms. P.Sai Sowjanya, Asst. Prof.



## WORKSHOP ON SOFTWARE ENGINEERING

11<sup>th</sup> Dec 2018 & 12<sup>th</sup> Dec 2018

Organized by  
Department of CSE

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IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTHAGAR KAKINADA

## About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

The Education Society is the outcome of honoured philanthropists, educationists, intellectuals and elite of Kakinada such as (Late) P. V. N. Raju, Dr. N. S. R. Sastry, Dr. P. Chiranjeevini Kumari. Ideal institutions have started from school and blossomed into group of colleges. Ideal Institutions are of its own kind in rendering service to society and value-based education.

During early seventies Kakinada used to have only two colleges. Student community used to suffer a setback to upgrade their education even to the level of intermediate.

The ideal junior college had been started to cater to the needs of student community. In due course the jr college flourished into Degree college and P.G courses like M.B.A, M.C.A, M.A, M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job potentials of the students.

Ideal institute of technology is one and only institute which is located in the midst of Kakinada amid mango groves in the lap of Nature.

In this present era of knowledge and technology driven economy, globalization and liberalization, every

employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

We seek to develop in each member of the Ideal community the ability and passion to work wisely, creatively and effectively for the betterment of mankind.

The Institute extends moral and placement support too. Value based education, mentoring and character building, job oriented short term courses, guest speakers from the industry, visits to industry houses, Entrepreneurship Development Programmes are our main stay. Students live in a unique environment that promotes culture, sports, society contributions, and above all the self-governance.

The institute is always abuzz with new ideas and is an exciting place to be, if you happen to be dreamer.

The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Software Engineering


Software Engineering is the sub discipline of Computer Science that attempts to apply engineering principles to the creation, operation, modification and maintenance of the software components of various systems. As with much of Computer Science, the subject of Software Engineering is at an very early stage in its development. It is much more of an art than a science, and at present has little in common with classical engineering.

Software Engineering may well be concerned with the application of well-understood scientific methods to the construction, operation, modification and maintenance of software. Today, however, Software Engineering is concerned with finding ways in which to produce working software for predictable costs in predictable time. When the problems involved are very simple or when only one person is involved, implementing software to meet their own needs, there isn't much to be said, and we are a long way from having any scientific principles for the production of software. Therefore, the major focus of software engineering today is on well-tested heuristics for the production of software to solve complex problems when many people are

# A oneday workshop on Software Enginnee

IDEAL INSTITUTE OF TECHNOLOGY::KAKINADA  
II B.TECH CSE STUDENTS

S.NO	ROLL NO	NAME
1	176K1A0501	ADABALA LIKHITA CHANDANA
2	176K1A0502	ADARI SUVARNA LAKSHMI
3	176K1A0503	ALLURU SUBHA MAHESWARI
4	176K1A0504	ANUSURI SRIKRISHNA
5	176K1A0506	BAVARISETTY PRAVALLIKA DEVI
6	176K1A0507	BOMMISETTI SAI VENKATA SOWJANYA
7	176K1A0509	CHINTAPALLI BHAVANI ANUSHA
8	176K1A0510	DADALA JAYAKAR
9	176K1A0511	DARA LAKSHMI NARAYANI VANGMAYI
10	176K1A0512	DEVU VEERA VENKATA SATYANARAYANA SWAMY
11	176K1A0514	DUDDI VARAPRASAD
12	176K1A0516	GUNNADA LAKSHMI LAVANYA
13	176K1A0517	GUTALA MANIKYA PADMA SAHITHI
14	176K1A0518	GUNDU SAI SAMHITHA
15	176K1A0520	GARIMELLA VENKATA SATYA SAI ARUNASRI
16	176K1A0521	IMANDI PRUDVI KUMAR
17	176K1A0522	KADALI SATYA ISHYANTH
18	176K1A0525	KASI MADHURI
19	176K1A0526	MAGAPU RAGHURAM
20	176K1A0527	KOPPISETTI VINAY KUMAR
21	176K1A0528	MEESALA BHARGAVA SRIRAJ
22	176K1A0529	MAILIPILLI NAVYA RANI
23	176K1A0532	MERUGU DIVYA NAGA SATYABHAVANI
24	176K1A0533	NAMADI MELIMA
25	176K1A0535	PABBINEEDI GANGA BHAVANI
26	176K1A0536	DODDIPATLA GANGA SINDUJA
27	176K1A0537	PETTELA SWETHA
28	176K1A0538	PASUPULETI VISHAL
29	176K1A0540	PASUPULETI SREEVENI POOJITHA
30	176K1A0541	PITANI HEMA PRIYANKA
31	176K1A0542	POSIMSETTI DIVYA SREE
32	176K1A0543	SWARNA NAGA SIRISHA

  
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33	176K1A0545	SOLLETI SRI HARSHA PRIYA
34	176K1A0547	SABBINA SURYASARANYA
35	176K1A0548	SHAIK BASHEER
36	176K1A0549	TAMMANA ABHIVARDHAN
37	176K1A0550	TIRUMALASETTI SAI MEGHANA
38	176K1A0551	URIVITI SAI LAKSHMI CHELSEA
39	176K1A0553	VADDI GANGOTRI
40	176K1A0554	VANAJANGI LIKHITHA
41	176K1A0555	VANKA RESHMA NEELIMA
42	176K1A0559	GOLLAPALLI DINESH RAJA
43	17S01A0511	MUDUNURU HIMA SAHITHI
44	186K5A0501	AAKULA ALEKYA
45	186K5A0502	GORUSETTI MURALI
46	186K5A0503	GUBBALA SARITHA
47	186K5A0504	JAGU VEERABHARGAVI
48	186K5A0505	KAMIREDDY ANUSHA
49	186K5A0506	KANDREGULA PRAVEEN KUMAR
50	186K5A0509	PAMPANABOINA N V MADHAVI LATHA
51	186K5A0510	PENKE HEMA VENKATA DURGA LAKSHMI
52	186K5A0511	UDATHA JYOTHI

  
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MIDYUNAGAR KAKINADA



Dr. P. Vamsi Krishna Raja is giving lecture on Software Engineering.

*T.S.R.*

Principal

IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR, KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 05-12-2018

## CIRCULAR

All the III B.Tech (CE) students are hereby informed that A one day workshop on "Sustainable Green Construction and Assessment" will be conducted on 12-12-2018. All interested students can enroll their names with Mrs. R. Gayatridevi, Asst. Professor, CE Dept on or before 08-12-2018. The details of topics and resource persons are available in brochure displayed in CE department notice board.

Venue : CE Department, Seminar hall.

  
HOD


  
1.8/5/12/18  
PRINCIPAL


Circulate among III B.Tech students


HODs are informed to circulate among concerned staff members

Copy to:

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HOD - ME 

HOD - ECE 

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# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 15-12-2018

## Program Report

**Name of the Event :** A one day workshop on SUSTAINABLE GREEN CONSTRUCTION AND ASSESSMENT .

**Date / Duration :** 12-12-2018 | 10:00 Am to 05:00 Pm.

**Resource Person :** Mr.A.Srinivas

Ph.No. : 9701405241

Mail Id : sreenee.a@gmail.com

**Name of Coordinator :** Mrs. R. Gayatri Devi, Asst. Professor , CE Dept.

**Number of students attended :** 51

**Objective of the event :**

- To introduce the concept of sustainable construction of different attributes.
- Principles of sustainable construction.
- Recent advancements in constructions.
- Purpose of sustainability assessments.

**Topics covered :**

- Principles of sustainable construction.
- Attributes of sustainable constructions.
- Case studies.
- Report information gathering for decision making during different phases of construction, design and use of a building.

**Outcome of the event :**

The student will be able to

- Understand the concepts of green building.
- Green buildings recent advancement and case studies
- Know the different systems employed for the design , construction.

  
CO-ORDINATOR

  
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build and maintain homes to the utilities needed to power them, we take a lot from the earth in order to live day to day.

The use of energy-saving technologies in green buildings should run through the entire process of building design, construction and use, enabling green energy-saving technologies to maximize their effectiveness in construction. Realize the sustainable development of green building, reduce energy consumption, reduce people's interference with the natural environment, suitable for people living in "green" building.

While sustainable buildings are undeniably still at a nascent stage in the country, India ranks second, just after the US, in terms of the number of green technology projects and built-up area.

In the age of global warming and the resulting climate change problems, man-made intervention has caused a tremendous impact on the environment and the ecosystem, leading to a major resource crisis across the planet. Understandably, the construction industry is important to support the growing populations' living aspirations, but certainly not at the expense of over-exploiting Mother Nature.

It may be news to the uninformed that the construction industry, together with the materials industries that support it, is one of the major global consumers of natural resources, both physical and biological

Today Civil Engineering has extended its applications both in a diversified and integrated way. By adopting the basic concepts and latest technology

many of the complications in an engineering application are considerably reduced. One of such is sustainable green construction and assessment. The main aim of this Seminar is to share the Knowledge between the experts, Faculty members and Students.

DAY 1	Green Construction
-------	--------------------

#### Program Committee

##### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

##### Patrons:

Dr. P. Chiranjeevini Kumari  
Secretary & Correspondent

##### Principal:

Dr. T. Srikanth

##### Convenor:

Mr. S. Nazeer Ahamed, Asst. Prof.,  
HoD, Civil

##### Coordinators:

Mr. M Venkata Rao, Asst. Prof.

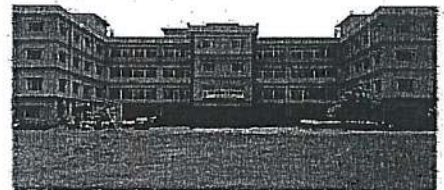
Mr. R Kiran Kumar, Asst. Prof.

Ms. K Sumavalli. Prof.

Ms. R Gayathri Devi, Asst. Prof.

Ms. K Manjusha, Asst. Prof.

Ms. K Sulekya, Asst. Prof.



## SUSTAINABLE GREEN CONSTRUCTION AND ASSESSMENT IN CIVIL ENGINEERING

12<sup>th</sup> December 2018

Organized by  
Department of CIVIL

Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTHAGAR KAKINADA

## About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

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### ABOUT THE WORKSHOP:

Buildings across the globe are adopting sustainable operational and constructional strategies. A green building is aimed at minimizing the use of water, optimized energy efficiency, low waste generation, conservation of natural resources and thereby offering the occupants with a healthy environment to live and work in

As our planet is faced with more and more problems related to finite resource depletion, pollution, and climate change, we're forced to examine everything in our daily lives that makes an impact on the earth.

The buildings we live and work in are a huge part of this — both the most basic means of shelter for our survival as well as the source for our greatest consumption of resources. From the materials used to

Workshop on Sustainable Green Construction & Assessment  
 IDEAL INSTITUTE OF TECHNOLOGY

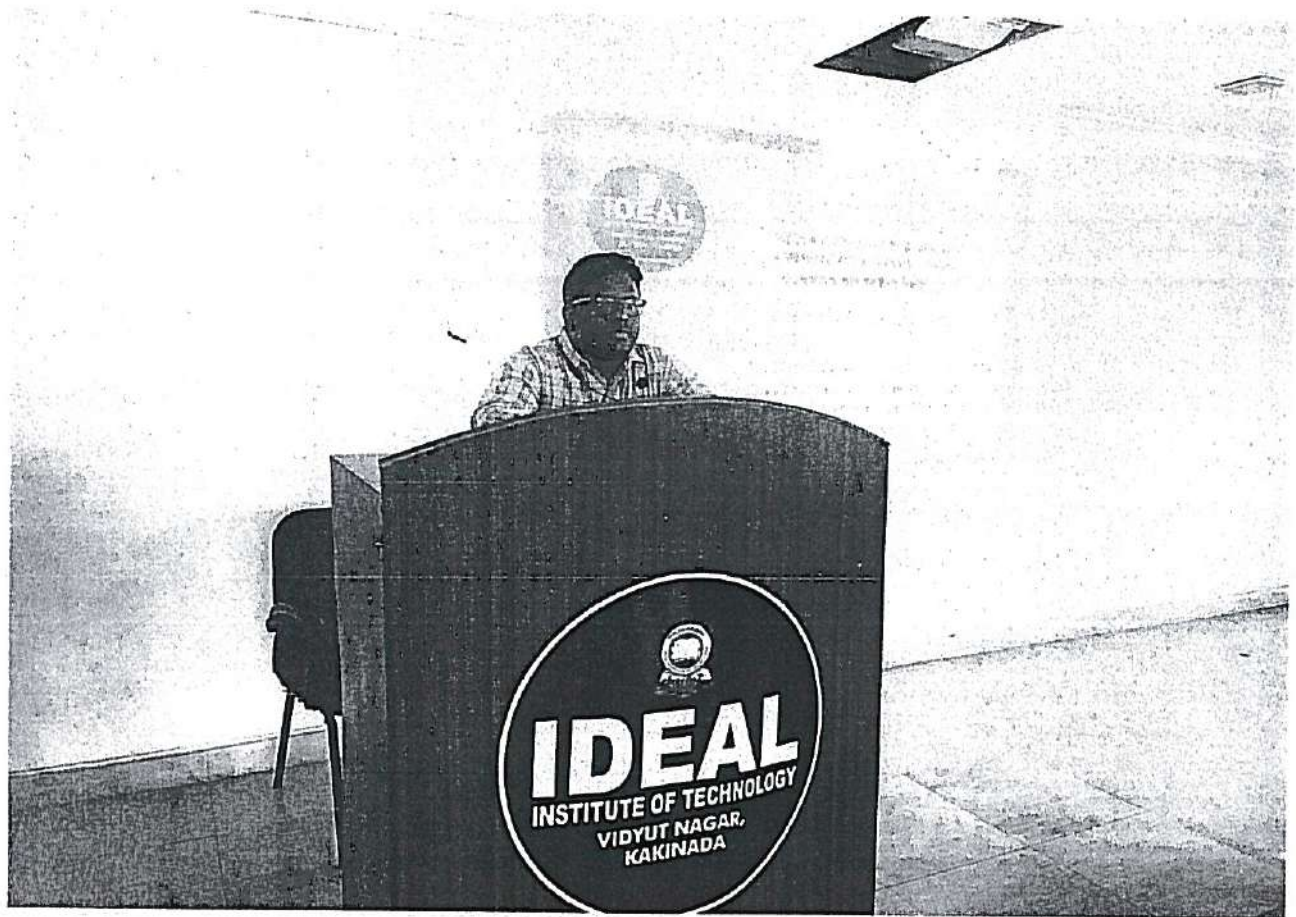
III.B.TECH II SEM BRANCH:CIVIL BATCH:2016-2020 A.Y:2018-19 REG:R16

S.NO	REG.NO	STUDENT NAME
1	166K1A0101	ADABALA NAGA GANESH
2	166K1A0102	BORRA RAJKUMAR
3	166K1A0103	BODHIREDDY VINAYKUMAR
4	166K1A0104	BYDIPUDI YAMUNA
5	166K1A0105	DEVA SRIDEVI KAMAKSHI
6	166K1A0107	GODAVARTHI S V SREE KRISHNA SASANK
7	166K1A0108	GUNTAMUKKALA VEERANDRA
8	166K1A0109	GUTURI VEERA CHARAN
9	166K1A0111	KALIGINEEDI RAVI DURGA
10	166K1A0112	KARANAM DURGA BABU
11	166K1A0113	L SRI SURYA NAGA MALLESWARI BHAVANI
12	166K1A0114	LINGAMPALLI DEVI
13	166K1A0117	MALAKALA BABI SATISH KUMAR
14	166K1A0118	NALAM ANNAPURNA
15	166K1A0119	NEDUNURI SWETHA
16	166K1A0120	NOOKALA SUSMITHA RAM
17	166K1A0121	SEERAPU HEMANTH KRISHNA KOUSHIK
18	166K1A0122	SHAIK HUMEERA
19	166K1A0123	TAMARAPU ROCHANA
20	166K1A0124	VYDADI VEERA MANIKANTA
21	166K1A0126	YERUBANDI SATTIYYA NAIDU
22	166K1A0127	SIRIVELA SRI VIDHYADHAR
23	176K5A0101	ADURUPAKA DURGARAO
24	176K5A0102	ANKAMREDDI KUSARAJU
25	176K5A0103	ANUSURI SAI VAMSI
26	176K5A0104	ATLURI VISHNU VARDHAN
27	176K5A0105	AVALA SATYA MURALIKRISHNA VAMSI YADAV
28	176K5A0106	BIRUDA ANUDEEP SAGAR
29	176K5A0107	BOGAVARAPU SAI SATYA VENI
30	176K5A0108	DANDU MOSEYRAJU
31	176K5A0109	GOPI AJAY
32	176K5A0110	GUBBALA SAI PADMA
33	176K5A0111	GUBBALA SHEELA RANI
34	176K5A0112	GUNUPURU RAVIKUMAR
35	176K5A0113	GURRALA MANIKANTA
36	176K5A0114	KATTAMURI SHIVARAM
37	176K5A0115	KETHA BUJJI
38	176K5A0116	KILARI ARUNA PRIYA
39	176K5A0117	KOPPISETTI AKHILA DEVI
40	176K5A0118	MERUGU SAMSONU
41	176K5A0119	PAMPANA HARSHINI
42	176K5A0120	PATTA TEJASWI SURYA PADMA SREE
43	176K5A0121	PILLI VEERA BHASKARA RAO
44	176K5A0122	POLEPALLI LAKSHMI PAVANI
45	176K5A0123	SIDDHANTHAM NAGA BALA SAI KUMAR
46	176K5A0124	VASAMSETTI DURGABHAVANI
47	176K5A0125	V V VENKATA SAI DIVYA SIRISHA MOUNIKA
48	176K5A0126	YARRA VENKATA VINAY SAI NARAYANA DATTA
49	176K5A0127	YARRABATHULA GANGA VEERENDRA

Principal  
 IDEAL INSTITUTE OF TECHNOLOGY  
 VIDYUTNAGAR KAKINADA

50	176K5A0128	KAKARAPALLI HITENDRA
51	176K5A0129	GUMMALA SATYA SIRISHA

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



Mr. A. Srinivas giving lecture on  
Sustainable green construction and  
Assesment En Civil Engg

*T. S. S.*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 07-12-2018

## CIRCULAR

All the III B.Tech (EEE) students are hereby informed that A one day workshop on "Super Conductivity & Engineering Applications" will be conducted on 14-12-2018. All interested students can enroll their names with Mr. A. Ramesh, Asst. Professor, EEE Dept on or before 11-12-2018. The details of topics and resource persons are available in brochure displayed in EEE department notice board.

Venue : EEE Department, Seminar hall.

*Ch. Vinod Kumar*  
HOD

*T. S. R.*  
7/12/18  
PRINCIPAL

Circulate among III B.Tech students

HODs are informed to circulate among concerned staff members

Copy to:

AO *CS*

HOD - CE *AD*

HOD - ME *R. V. S.*

HOD - ECE *A. Ramesh*

HOD - CSE *H. S. R.*

HOD - H&BS *CS*

*T. S. R.*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 17-12-2018

## Program Report

**Name of the Event :** A one day workshop on SUPER CONDUCTIVITY and ENGINEERING APPLICATIONS.

**Date / Duration :** 14-12-2018 | 10:00 Am to 05:00 Pm.

**Resource Person :** Dr. S. Srikanth, HOD-EEE, BVC Engineering College

Ph.No. : 93465 47224

Mail Id : [srikanth@gmail.com](mailto:srikanth@gmail.com)

**Name of Coordinator :** Mr. A. Ramesh, Asst. Professor , EEE Dept.

**Number of students attended :** 36

**Objective of the event :**

Super Conductivity is discuss classical aspects of super conductors followed by study of their Thermodynamic and Magnetic properties and Electrodynamic response.

**Topics covered :**

1. High Temperature super conductors.
2. Phasor measurement units.

**Outcome of the event :**

The meaning of the newly define terms and symbols. Distinguish between perfect conduction and perfect Diamagnetism.

*A. Ramesh*  
CO-ORDINATOR

*Ch. Vinod Kumar*  
HOD

*T. S. J.*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR

transportation, industrial equipment, medicine, electronics, communications, and defense/space.

Following the discussion of applications is a section on the lessons for HTS that can be gleaned from nearly 80 years' experience with LTS. The seminar concludes with a discussion of the significance of a higher critical transition temperature (TC) in the context of the broader requirements that must be met by any viable commercial technology.

Superconductors exhibit unique features other than their ability to perfectly conduct current. For example, many expel magnetic fields during the transition to the superconducting state. This is due to the Meissner effect by which superconducting materials set up electric currents near their surface at  $T_c$ , therefore canceling the fields within the material itself. A stationary magnet on a superconductor demonstrates this effect: as the superconductor cools through its critical temperature, the expulsion of magnetic flux from the conductor causes the magnet to levitate above the material.

Below a certain "critical" temperature, materials undergo transition into the superconducting state, characterized by two basic properties: firstly, they offer no resistance to the passage of electrical current. When resistance falls to zero, a current can circulate inside the material without any dissipation of energy. Secondly, provided they are sufficiently weak, external magnetic fields will not penetrate the superconductor, but remain at its surface. This field expulsion phenomenon is known as the Meissner effect, after the physicist who first observed it in 1933.

The discovery of these "High Temperature Superconductors" sparked vast interest, and an entire industry dedicated to the research and commercial development of these materials and their applications has emerged. Today, an enormous range of devices are

under development for both low and high temperature superconductors.

International competition is strong in these materials, and current efforts involve many facets of the electronics, communications, power, medical technology, transportation, military, and materials processing industries.

DAY 1	Super Conductivity and Engineering Applications
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#### Program Committee

##### Chief Patrons:

**Dr. P. Krishna Rao**  
Chairman

##### Patrons:

**Dr. P. Chiranjeevini Kumari**  
Secretary & Correspondent

##### Principal:

**Dr. T. Srikanth**

##### Convenor:

**Mr. Ch. Vinod Kumar, Assoc. Prof.,**  
HoD, EEE

##### Coordinators:

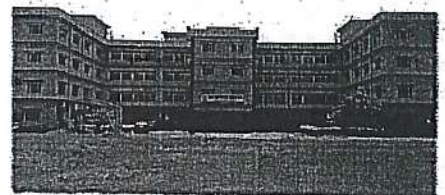
**Mr. K. Venkateswara Rao, Asst. Prof.**

**Mr. G. Ravi Kumar, Asst. Prof.**

**Mr. D. Madhu, Asst. Prof.**

**Mr. M. Jagadeesh, Asst. Prof.**

**Mr. A. Ramesh, Asst. Prof.**



## WORKSHOP ON SUPER CONDUCTIVITY & ENGINEERING APPLICATIONS

14<sup>th</sup> December 2018

Organized by  
Department of EEE

Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR, KAKINADA

## About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

The Education Society is the outcome of honoured philanthropists, educationists, intellectuals and elite of Kakinada such as (Late) P. V. N. Raju, Dr. N. S. R. Sastry, Dr. P. Chiranjeevini Kumari. Ideal institutions have started from school and blossomed into group of colleges. Ideal Institutions are of its own kind in rendering service to society and value-based education.

During early seventies Kakinada used to have only two colleges. Student community used to suffer a setback to upgrade their education even to the level of intermediate.

The ideal junior college had been started to cater to the needs of student community. In due course the jr college flourished into Degree college and P.G courses like M.B.A, M.C.A, M.A, M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job potentials of the students.

Ideal institute of technology is one and only institute which is located in the midst of Kakinada amid mango groves in the lap of Nature.

In this present era of knowledge and technology driven economy, globalization and liberalization, every

employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

We seek to develop in each member of the Ideal community the ability and passion to work wisely, creatively and effectively for the betterment of mankind.

The Institute extends moral and placement support too. Value based education, mentoring and character building, job oriented short term courses, guest speakers from the industry, visits to industry houses, Entrepreneurship Development Programmes are our main stay. Students live in a unique environment that promotes culture, sports, society contributions, and above all the self-governance.

The institute is always abuzz with new ideas and is an exciting place to be, if you happen to be a dreamer.

The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Super Conductivity and Engineering Applications

A short account is given of the characteristic properties of superconductors and the reasons for the importance of superconductivity in science and technology. The occurrence of the phenomenon in metals, alloys and chemical compounds is briefly discussed, with particular reference to those materials having the highest critical temperatures. The major part of the seminar is concerned with the applications of superconductivity in engineering, and attention is particularly devoted to materials for magnet construction, the various uses of superconducting magnets, and the other applications of superconductors such as power transmission cables, and transformers. Some forecasts are made regarding the probable developments in superconductivity in the next few years.

The purpose of this seminar is to assess the significance of high-temperature superconductors (HTS) and to forecast the timing of potential markets. Accordingly, it examines the major present and potential applications of superconductors in seven different sectors: high energy physics, electric power,

A one day workshop on Super Conductivity

IDEAL INSTITUTE OF TECHNOLOGY :: KAKINADA

& Engg

II. B.TECH: EEE-STUDENTS FEE DETAILS FOR THE ACADEMIC YEAR 2018-19

Application

S. NO.	ADMISSION NO.	NAME OF THE STUDENT
1	176K1A0201	ANUSURI RAJESWARI
2	176K1A0202	BOTTA SAI DURGA KARTHIK
3	176K1A0203	CHITNEEDI V L S D M PRIYANKA
4	176K1A0204	CHOKKA JAI VENKATA PRASAD
5	176K1A0205	DENDULURI VEERA GANGESWARARAO
6	176K1A0206	DUVVANA VINAY KUMAR
7	176K1A0207	KORLAPATI JAGADEESH
8	176K1A0208	KADALI CHAITANYA PAWAN KALYAN
9	176K1A0209	MEDISETTY VEERA VENKATA SRI DHANUSH
10	176K1A0210	MOHAMMAD AYESHA BEGUM
11	176K1A0211	OLETI NAGARJUNA
12	176K1A0212	PEBBILI VARALAKSHMI
13	176K1A0213	PILLI SIRISHA
14	176K1A0214	MENDU VEERA VENKAT
15	176K1A0215	TANNERU MANOJ
16	186K5A0201	BHESETTI VENKATARAMANA
17	186K5A0202	BIRUDA VIJAYAKUMAR
18	186K5A0203	BOJJA RUCHITA
19	186K5A0204	PITHANI MAHESH
20	186K5A0205	JALUMURU SIMHACHALAM
21	186K5A0206	KAMADI KIRAN TEJA
22	186K5A0207	KANDEPALLI ROHIT
23	186K5A0208	MASABATTULA SUNEEL
24	186K5A0209	PALEPU RAJESH
25	186K5A0210	SARIKOPPULA SAI
26	186K5A0211	SIVALANKA HIMAVARSHA
27	186K5A0212	TADI MANOJ KUMAR
28	186K5A0213	TATAPUDI BHANU
29	186K5A0214	TEKUMUDI DHANUKAR
30	186K5A0215	ANUSURI RAJA SURENDRA
31	186K5A0216	ADAPA VEERABABU
32	186K5A0217	RAVULA VEERABABU
33	186K5A0218	MAGAPU SAI PAVAN KALYAN
34	186K5A0219	VEMAGIRI SRINIVAS
35	186K5A0220	GANDHAM RAJEETH KUMAR
36	166K5A0231	SHEIK SALMAN

T-8  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
PROJYUT NAGAR KAKINADA

36

2-



**ONE DAY WORKSHOP**  
on  
**"SUPER CONDUCTIVITY &  
ENGINEERING APPLICATIONS"**  
Date:14-12-2018

**RESOURCE PERSON NAME:**

**Dr. S.SRI KANTH**  
Professor & HoD- EEE Dept,  
BVC Engineering College,  
Odalarevu, Amalapuram.

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 28-12-2018

## CIRCULAR

All the III B.Tech (CE) students are hereby informed that A two days workshop on "Total Station Theodolite" will be conducted on 05-01-2019 & 06-01-2019 . All interested students can enroll their names with Mr. R. Kiran Kumar, Asst. Professor, CE Dept on or before 02-01-2019. The details of topics and resource persons are available in brochure displayed in CE department notice board.

Venue : CE Department, Seminar hall.

  
HOD

  
PRINCIPAL

Circulate among III B.Tech students

HODs are informed to circulate among concerned staff members

Copy to:

AO



HOD - EEE

 Ch. Vinod Kumar

HOD - ME



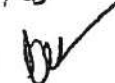
HOD - ECE



HOD - CSE



HOD - H&BS



  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 10-01-2019

## Program Report

**Name of the Event :** A two days workshop on Total station theodolite.

**Date / Duration :** 05-01-2019 to 06-01-2019 | 10:00 AM to 05:00 PM.

**Resource Person :** Mr. D. Venkateswarlu, Prof. & H.O.D. Civil Dept., GIET Rajahmundry,

Ph. No.: 94402 48032,

Mail Id : [venkateswarlu@gmail.com](mailto:venkateswarlu@gmail.com)

**Name of Coordinator :** Mr. R. Kiran Kumar, Asst. Professor , CE Dept.

**Number of students attended :** 56

**Objective of the event :**

- To introduce the basics of total station and theodolite
- Various methods of linear and angular measurements by using total station and theodolite
- To provide methods for calculating areas and volumes

**Topics covered :**

- Principles, adjustments in theodolite and total station
- Measurement of vertical and horizontal angles
- Real time use in construction industry

**Outcome of the event :**

The student will be able to

- Use theodolite and total station
- Perform different methods of surveying
- Compute various data required for surveying

*R.K. Kumar*  
CO-ORDINATOR

*T.S.*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR, KAKINADA

*ee*  
HOD

### ABOUT THE COLLEGE

Ideal Institute of Technology was established under the aegis of Governing Body of ideal college of Arts and sciences during academic year 2009-2010. It is being running under the able guidance of Dr.P. Krishna Rao garu Chairman of ideal Institute of Technology and Pragati Engineering College. The Education Society is the outcome of honored philanthropists, educationists, intellectuals and elite persons of Kakinada such as Sri P.V.N.raju, Dr.N.S.R.Sastry, Dr.P.Chiranjeevini Kumari. Ideal Institutions are of its own kind in rendering service to society and value based education. During early seventies Kakinada used to have only two colleges. Student community used to suffer a set-back to upgrade their education even to the level of intermediate. The Ideal Junior college flourished into Degree college and P.G. course like MBA, MCA, MA, M.Sc. And Engineering College. Ideal Institute of Technology is one and only institute which is located in the midst of Kakinada.

### ABOUT CIVIL DEPARTMENT

The department is strengthened with one Doctorate, nine Post Graduates as Out of which two staff members have industrial experience in reputed organizations. Structural Design, Geotechnical analysis, Surveying and many other. Not only have the teaching staff, the technicians also had tremendous technical experience. The department is equipped with ISO certified laboratory equipment and sophisticated teaching aids like day light projector, e-Class room etc.

### THEME OF THE LECTURE

This lecture will provide a platform for academicians, Researchers, Scientists and industry personnel from industry, to come together and share the latest developments in broad areas of Total Station.

A total station (TS) or total station theodolite (TST) is an electronic/optical instrument used for surveying and building construction. It is an electronic transit theodolite integrated with electronic distance measurement (EDM) to measure both vertical and horizontal angles and the slope distance from the instrument to a particular point, and an on-board computer to collect data and perform triangulation calculations.

This eliminates the need for an assistant staff member as the operator holds the retro reflector and controls the total station from the observed point. These motorized total stations can also be used in automated setups known as Automated Motorized Total Station (AMTS). In this workshop hands on sessions also included for real time experience.

### RESOURCE PERSON

1. Mr. V.V. SATYANARAYANAMURTHY.  
Engineering Professionals,  
Rajahmundry

### TWO DAY WORKSHOP ON TOTAL STATION 5<sup>th</sup> & 6<sup>th</sup> DECEMBER 2019



Organized by

Department of CIVIL Engineering

### IDEAL INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi;  
Affiliated to JNTUK, Kakinada,

&


Vidyut Nagar, Kakinada, E.G.Dt.,  
Andhra Pradesh, India- 533 003

Ph:

Fax: 08852-252232

Website: [www.idealtech.edu.in](http://www.idealtech.edu.in)

Email: [principal@idealtech.edu.in](mailto:principal@idealtech.edu.in)

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

TWO DAY WORKSHOP ON  
ON  
TOTAL STATION  
5<sup>th</sup> & 6<sup>th</sup> DECEMBER 2019  
**REGISTRATION FORM**

(Make copies if desired)

Name : \_\_\_\_\_

Date of Birth : \_\_\_\_\_

Designation : \_\_\_\_\_

Department : \_\_\_\_\_

Organization : \_\_\_\_\_

Address for Communication:  
\_\_\_\_\_  
\_\_\_\_\_

Phone : \_\_\_\_\_

E-mail : \_\_\_\_\_

DD No : \_\_\_\_\_

Date: \_\_\_\_\_

Signature of the  
Applicant

**ORGANISING COMMITTEE**

**Chief Patron**

Dr. Paruchuri Krishna Rao  
*Chairman*

**Patrons**

Dr.P. Chirajeevini Kumari  
*Secretary and Correspondent*  
Sri P.Kiran  
*Treasurer*

**Coordinator**

Dr. T. Srikanth  
*Principal & Professor, Dept. of ME*

**Co-Coordinator**

Ms.K.Sumavalli  
*Head, Dept. of CE*

**Advisory Committee**

Dr.G.Krishnam Raju, *Director*  
Dr.Ch.V.V.Satyanarayana Murthy  
Dr. Srihari Varma  
Mr. Bh.Ayyappa Kumar  
Mr. Ch.Vinod Kumar  
Mr. M.S.R.S. Prasad

**Organizing Committee**

Mr.S.Nazeer Ahmed  
Mr.R.Kirankumar  
Mr.Y.Sriram  
Ms.B.Monica  
Ms.M.Geeta sri

**ATTENDEES**

This seminar is intended for Academicians,  
Researchers, Industry Personnel and PG  
Students

**REGISTRATION FEE:** NIL

**IMPORTANT DATES:**

Submission of Application : 10-11-19  
Intimation of Selection : 03-12-19  
Confirmation by Participant : 04-12-19

**MAILING ADDRESS**

The Coordinator,  
Department of CE  
Vidyut Nagar, Kakinada, East Godavari District,  
Andhra Pradesh, India-533003  
[principal@idealtch.edu.in](mailto:principal@idealtch.edu.in)

*For any further clarifications please feel free to  
contact*

Ms.K. Sumavalli  
Head of the Department  
Department of CE  
Coordinator - +91 7995539549

A two days workshop on Total Station  
Theodolite

IDEAL INSTITUTE OF TECHNOLOGY		
II.B.TECH II SEM BRANCH: CIVIL BATCH: 2017-21 REG: R16		
S.NO	REG NO.	STUDENT NAME
1	176K1A0101	AYILA SRINIVASA ARJUN
2	176K1A0102	AVALA VEERA SAI KUMAR
3	176K1A0103	ALLIPILLI LAKSHMAN RAO
4	176K1A0104	ATLA VEERA TEJA
5	176K1A0105	BARLA MONISH VARAHALA YADAV
6	176K1A0106	BHARATAM MANOZNA SRUTISHA
7	176K1A0111	DADALA SUJATHA
8	176K1A0112	DARA PUSHPA VALLI
9	176K1A0114	GANDIKOTA AKHIL
10	176K1A0115	GEESALA VEERA SAI VARA PRASAD
11	176K1A0116	GONTHUNA SRINIVASU
12	176K1A0117	GUBBALA RUPADEVI
13	176K1A0118	GUNTURI LIKHITHA BALA TRIPURA SUNDARI
14	176K1A0119	ILLA RAMA DURGA PRASAD
15	176K1A0120	JOSYULA SRI RAM
16	176K1A0121	KONADA SATYA PAVANI
17	176K1A0123	KADA MOHAN KUMAR
18	176K1A0126	KARNEEDI SATYA SURYA RAMA DEVI
19	176K1A0127	KOTA AJAY
20	176K1A0129	LAKKIMSETTI MANEESHA
21	176K1A0131	MUDRAGADA RAJA HARSHA
22	176K1A0133	MAJI SWATHI
23	176K1A0134	MANCHIMSETTI ANIL KUMAR
24	176K1A0135	MOHAMMED ABDUL SAMDANI BASHA
25	176K1A0136	MOHAMMAD SAJID
26	176K1A0137	MOKANA ANITHA
27	176K1A0138	NAMA VEERA VENKATA SATYA SHARAN
28	176K1A0139	NAWAZ KHAN
29	176K1A0140	OLETI RATNABABU
30	176K1A0141	PALIKA PRIYA
31	176K1A0142	PASILA ANAND
32	176K1A0143	PENTA VEERA VENKATA SAI GOWTAM
33	176K1A0144	PENTAPATI SAI RAM
34	176K1A0145	REDDI RANI
35	176K1A0146	RUDRI SRI ANANYA SHIVANI
36	176K1A0147	SANGANI MOHANA KRISHNA
37	176K1A0148	TALABATHULA LAKSHMI SIRISHA
38	176K1A0149	TELLAKULA VEERABABU
39	176K1A0150	GOMPA APPALARAJU
40	176K1A0151	VEERAMACHANENI JAYA VARDHAN
41	176K1A0152	PENKEY JYOTHI SUREKHA
42	176K1A0153	YANDAMURI SUNITHA
43	176K1A0154	ELLE SHAKINA
44	186K5A0102	ATHILI DATTESWAR SAI
45	186K5A0104	GRANDHI VENKATA NAGA SESHU MANI KUMAR
46	186K5A0105	KAREDLA VIJAY KUMAR
47	186K5A0107	KORUVADA NOMI RAMA DEVI
48	186K5A0108	MEDISETTI GANESH
49	186K5A0109	NANDIPATI PAVANKUMAR
50	186K5A0111	VARRI RAVI TEJA
51	186K5A0112	VELIVELLA SAI RAJESWARI
52	186K5A0113	CHITIKIREDDY ANAND
53	186K5A0114	KADA ANIL KUMAR
54	186K5A0115	SURISETTI SAI SURYA TEJA
55	186K5A0116	DAKI RANADEEP KUMAR
56	186K5A0118	ELURI LAKSHMINARAYANA

T-8L  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYANAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA-533 003. (A.P.)

Date: 29-12-2018

## CIRCULAR

All the III B.Tech (EEE) students are hereby informed that A one day workshop on "Power Systems Stability & Facts to Improve it" will be conducted on 06-01-2019.

All interested students can enroll their names with Mr. M. Jagadeesh, Asst. Professor, EEE Dept on or before 03-01-2019. The details of topics and resource persons are available in brochure displayed in EEE department notice board.

Venue : EEE Department, Seminar hall.

*Ch. Vinod Kumar*  
HOD

*M. Jagadeesh*  
29/12/18  
PRINCIPAL

Circulate among III B.Tech students

HODs are informed to circulate among concerned staff members

Copy to:

AO *[Signature]*

HOD - CE *[Signature]*

HOD - ME *[Signature]*

HOD - ECE *[Signature]*

HOD - CSE *[Signature]*

HOD - H&BS *[Signature]*

*[Signature]*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 09-01-2019

## Program Report

**Name of the Event :** A one day workshop on POWER SYSTEMS STABILITY AND FACTS TO IMPROVE IT.

**Date / Duration :** 06-01-2019 | 10:00 Am to 05:00 Pm.

**Resource Person :** Dr. G. Naresh, Prof & Dean Administration, Pragati Engineering College

Ph.No. : 98498 98440

Mail Id : [naresh.elec@gmail.com](mailto:naresh.elec@gmail.com)

**Name of Coordinator :** Mr. M.Jagadeesh, Asst. Professor , EEE Dept.

**Number of students attended :** 35

**Objective of the event :**

power transfer capability and controllability is enhanced by using FACTS devices. New development in Power Electronics and control have extended the application range of FACTS.

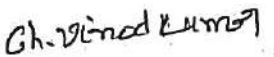
**Topics covered :**

Steady state stability transient stability dynamic stability applications of FACTS.

**Outcome of the event :**

Modern technology Power Systems stability and facts to improve it. Different connections are used (Series and Shunt Compensation).

  
CO-ORDINATOR

  
HOD

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

disturbances or faulty flow of power. Having understood these terminologies related to power system stability let us now look into the different types of stability.

The synchronous stability of a power system can be of several types depending upon the nature of disturbance, and for the purpose of successful analysis it can be classified into the following 3 types as shown below:

1. Steady state stability.
2. Transient stability.
3. Dynamic stability.

In recent years, power demand has increased substantially while the expansion of power generation and transmission has been severely limited due to limited resources and environmental restrictions. As a consequence, some transmission lines are heavily loaded and the system stability becomes a power transfer-limiting factor. Flexible AC transmission systems (FACTS) controllers have been mainly used for solving various power system steady state control problems. However, recent studies reveal that FACTS controllers could be employed to enhance power system stability in addition to their main function of power flow control.

The seminar explains about an increasing interest in this subject for the last two decades, where the enhancement of system stability using FACTS controllers has been extensively investigated. This seminar presents a comprehensive review on the research and developments in the power system stability enhancement using FACTS damping controllers. Several technical issues related to FACTS installations have been

highlighted and performance comparison of different FACTS controllers has been discussed. In addition, some of the utility experience, real-world installations, and semiconductor technology development will be reviewed and summarized. Applications of FACTS to other power system studies will be discussed.

DAY 1	Power Systems Stability & FACTS to improve it
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#### Program Committee

##### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

##### Patrons:

Dr. P. Chiranjeevi Kumari  
Secretary & Correspondent

##### Principal:

Dr. T. Srikanth

##### Convenor:

Mr. Ch. Vinod Kumar, Assoc. Prof.,  
HoD, EEE

##### Coordinators:

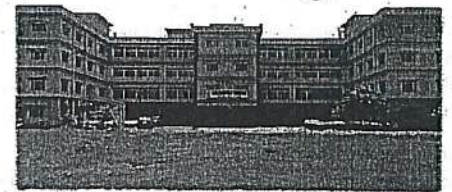
Mr. K. Venkateswara Rao, Asst. Prof.

Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.



## WORKSHOP ON POWER SYSTEMS STABILITY & FACTS TO IMPROVE IT

06<sup>th</sup> January 2019

Organized by  
Department of EEE

Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR, KAKINADA

## About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

The Education Society is the outcome of honoured philanthropists, educationists, intellectuals and elite of Kakinada such as (Late) P. V. N. Raju, Dr. N. S. R. Sastry, Dr. P. Chiranjeevini Kumari. Ideal institutions have started from school and blossomed into group of colleges. Ideal Institutions are of its own kind in rendering service to society and value-based education.

During early seventies Kakinada used to have only two colleges. Student community used to suffer a setback to upgrade their education even to the level of intermediate.

The ideal junior college had been started to cater to the needs of student community. In due course the jr college flourished into Degree college and P.G courses like M.B.A, M.C.A, M.A, M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job potentials of the students.

Ideal institute of technology is one and only institute which is located in the midst of Kakinada amid mango groves in the lap of Nature.

In this present era of knowledge and technology driven economy, globalization and liberalization, every

employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

We seek to develop in each member of the Ideal community the ability and passion to work wisely, creatively and effectively for the betterment of mankind.

The Institute extends moral and placement support too. Value based education, mentoring and character building, job oriented short term courses, guest speakers from the industry, visits to industry houses, Entrepreneurship Development Programmes are our main stay. Students live in a unique environment that promotes culture, sports, society contributions, and above all the self-governance.

The institute is always abuzz with new ideas and is an exciting place to be, if you happen to be dreamer.

The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Power Systems Stability & FACTS to improve it

Power system stability is the ability of an electric power system, for a given initial operating condition, to regain a state of operating equilibrium after being subjected to a physical disturbance, with most system variables bounded so that practically the entire system remains intact.

For stable operation it is important for the bus to be well synchronized with the generators over the entire duration of transmission, and for this reason the power system stability is also referred to as synchronous stability and is defined as the ability of the system to return to synchronism after having undergone some disturbance due to switching on and off of load or due to line transience.

To understand stability well another factor that is to be taken into consideration is the stability limit of the system. The stability limit defines the maximum power permissible to flow through a particular point or a part of the system during which it is subjected to line

A oneday workshop on Power systems Stability & Facts to improve it

IDEAL INSTITUTE OF TECHNOLOGY::KAKINADA

IV B.Tech - II Sem Branch: EEE Batch: 2015-19 A.Y: 2018-19 R13

SNO	REG NO	NAME OF THE STUDENT
1	156K1A0201	BANDARU VENKATESH
2	156K1A0202	BAVISETTI S J LAKSHMI PRASANNA
3	156K1A0203	PALIVELA PRATYUSHA
4	156K1A0204	PENKE MOHAN SRI SATYA KUMAR
5	156K1A0205	YELUDUTI MURALI
6	166K5A0201	ABBANA V N BHAVANI DATTA
7	166K5A0202	BASWA MANIKANTA
8	166K5A0203	BAVISETTI DURGA THATHAJI
9	166K5A0204	BYREDDY PRASADU
10	166K5A0205	CHITEKENA VINAY
11	166K5A0206	DANGETI DEVI PRASAD
12	166K5A0207	DOODHI VATHSALYA KUMAR
13	166K5A0208	ESAKONA VEERAGANI KUMAR
14	166K5A0209	G VV S SIVA GANGA
15	166K5A0210	GOLI VEERENDRA
16	166K5A0211	GURAJAPU ARAVIND SWAMY
17	166K5A0212	GUTHULA NITYANANDA BABU
18	166K5A0213	KADIYALA ADITYA
19	166K5A0214	KALIBOINA SIVA
20	166K5A0215	KAMINI MANIKANTA
21	166K5A0216	KARAY DEEPSAGAR
22	166K5A0218	KATAKAM CHARAN
23	166K5A0219	KOMARTHI BALA RAM
24	166K5A0220	KUKKULURI GANGADHAR
25	166K5A0221	MAHADASU SRUTHI
26	166K5A0222	MATTEY KARTHIK
27	166K5A0224	NOWGANTLA BALARAMAKRISHNA
28	166K5A0225	OLETI DHANAMJAYA
29	166K5A0226	PAPPU RAMESH BABU
30	166K5A0227	PILLI SAI HEMANTH KUMAR
31	166K5A0228	SATHI EBENEZER
32	166K5A0230	SHAIK GOUSE MOHIDDIN
33	166K5A0232	VANAPALLI DORABABU
34	166K5A0233	VEEDHI RAJA KUMARI
35	166K5A0235	VEDULA SUNDAR DEEPAK

T-8  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



ONE DAY WORKSHOP  
on  
“POWER SYSTEMS STABILITY & FACTS  
TO IMPROVE IT”

Date:06-01-2019

**RESOURCE PERSON NAME:**

**Dr. G.NARESH**

Professor in EEE & Dean Administration,  
Pragati Engineering College,  
Surampalem.

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

2019 - 13

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# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 04-01-2019

## CIRCULAR

All the II B.Tech (ECE) students are hereby informed that A Two days workshop on "Electronics Circuit Analysis" will be conducted on 07-01-2019 & 08-01-2019 . All interested students can enroll their names with Mrs. P. V. Ratnam, Asst. Professor, ECE Dept on or before 06-01-2019. The details of topics and resource persons are available in brochure displayed in ECE department notice board.

Venue : ECE Department, E- Class room.

HOD

1.8/4/19  
PRINCIPAL

Circulate among II B.Tech students

HODs are informed to circulate among concerned staff members

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HOD - EEE Ch. Vinod Kumar

HOD - ME

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IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR K.



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 11-01-2019

## Program Report

**Name of the Event :** A one day workshop on ELECTRONICS CIRCUITS ANALYSIS.

**Date / Duration :** 07-01-2019 & 08-01-2019 | 10:00 Am to 05:00 Pm.

**Resource Person :** Dr. V. Sailaja, Professor, Pragathi Engineering College

Ph.No. : 94914 44434

Mail Id : [sailaja.v@pragathi.ac.in](mailto:sailaja.v@pragathi.ac.in)

**Name of Coordinator :** Mrs. P. V. Ratnam , Asst. Professor , ECE Dept.

**Number of students attended :** 40

**Objective of the event :**

- To provide a methodical approach to solving .
- To understand the concept of graphical solution to electrical networks.
- To analyze various types of filters and attenuators.

**Topics covered :**

- Resistive elements & networks.
- Circuit method including KCL, KVL & the Node method.
- Independent & dependent sources.
- Linearity superposition, Thevenin & Norton methods.
- Combinational gates, MOSFET Switches

**Outcome of the event :** Upon completion of the students use basic electrical DC concepts & theorems to Analyze circuits. Build and simulate electrical DC circuits and perform measurement with electronic test equipments.

*P.V. Ratnam*  
CO-ORDINATOR

*Abhinav*  
HOD

*T.S.K*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

## Analog Circuits

A circuit diagram representing an analog circuit, in this case a simple amplifier

Analog electronic circuits are those in which current or voltage may vary continuously with time to correspond to the information being represented. Analog circuitry is constructed from two fundamental building blocks: series and parallel circuits.

In a series circuit, the same current passes through a series of components. A string of Christmas lights is a good example of a series circuit: if one goes out, they all do.

In a parallel circuit, all the components are connected to the same voltage, and the current divides between the various components according to their resistance.

## Digital Circuits

In digital electronic circuits, electric signals take on discrete values, to represent logical and numeric values. These values represent the information that is being processed. In the vast majority of cases, binary encoding is used: one voltage (typically the more positive value) represents a binary '1' and another voltage (usually a value near the ground potential, 0V) represents a binary '0'. Digital circuits make extensive use of transistors, interconnected to create logic gates that provide the functions of Boolean logic: AND, NAND, OR, NOR, XOR and combinations thereof. Transistors interconnected so as to provide positive feedback are used as latches and flip flops, circuits that have two or more metastable states, and remain in one of these states until changed by an external input. Digital circuits therefore can provide logic and memory, enabling them to perform arbitrary computational functions. (Memory based on flip-flops is known as static random-access memory (SRAM). Memory based on the storage of

charge in a capacitor, dynamic random-access memory (DRAM) is also widely used.)

The design process for digital circuits is fundamentally different from the process for analog circuits. Each logic gate regenerates the binary signal, so the designer need not account for distortion, gain control, offset voltages, and other concerns faced in an analog design. As a consequence, extremely complex digital circuits, with billions of logic elements integrated on a single silicon chip, can be fabricated at low cost.

DAY 1	Analog Circuits
DAY 2	Digital Circuits

### Program Committee

#### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

#### Patrons:

Dr. P. Chiranjeevi Kumari  
Secretary & Correspondent

#### Principal:

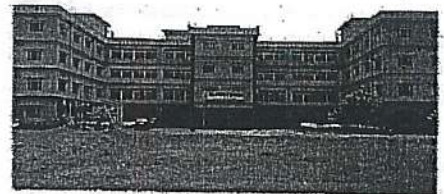
Dr. T. Srikanth

#### Convenor:

Mr. Bh. V.S.R.K. Ayyappa Kumar,  
Assoc. Prof., HoD, ECE

#### Coordinators:

Mr. B.V. Kalyan Ram, Asst. Prof.  
Smt. D. Vijaya Lakshmi, Asst. Prof.  
Mr. Ch. V. Prakash, Asst. Prof.  
Smt. P. V. Ratnam, Asst. Prof.  
Mr. V. Krishnan, Asst. Prof.  
Mr. P. S.J. Sree Ram, Asst. Prof.  
Ms. N. TejaSri, Asst. Prof.



## WORKSHOP ON ELECTRONIC CIRCUIT ANALYSIS

07<sup>th</sup> & 08<sup>th</sup> January 2019

Organized by  
Department of ECE

## About the College

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A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Electronic Circuit Analysis

An electronic circuit is composed of individual electronic components, such as resistors, transistors, capacitors, inductors and diodes, connected by conductive wires or traces through which electric current can flow. To be referred to as electronic, rather than electrical, generally at least one active component must be present. The combination of components and wires allows various simple and complex operations to be performed: signals can be amplified, computations can be performed, and data can be moved from one place to another.

Circuits can be constructed of discrete components connected by individual pieces of wire, but today it is much more common to create interconnections by photolithographic techniques on a laminated substrate (a printed circuit board or PCB) and solder the components to these interconnections to create a finished circuit. In an integrated circuit or IC, the components and interconnections are formed on the same substrate, typically a semiconductor such as silicon or (less commonly) gallium arsenide.

S.NO	REG.NO	STUDENT NAME
1	166K1A0401	AKULA BHARGAVI PRIYANKA
2	166K1A0402	ANAKAPALLI SATYA INDHRA DEVI
3	166K1A0403	AKULA HARIPRIYA
4	166K1A0404	ALAPANA LAKSHMI HARI
5	166K1A0405	ANASURI BHANU PRAKASH
6	166K1A0406	ANGARA LAKSHMI MEGHANA
7	166K1A0407	ANISSETTI KASI ANNAPURNA
8	166K1A0408	AVANIGADDA RAGA NAVYA
9	166K1A0409	DOOLAM NEEHARIKA
10	166K1A0410	BADDI VANI
11	166K1A0411	BANDARU LAVANYA AKHILA
12	166K1A0412	CHUNDRU SRINIVAS
13	166K1A0413	DAMMU ATCHAMMA
14	166K1A0414	DALEY KAILASH
15	166K1A0415	DEVISETTI VENKATASUSHMASARANYA
16	166K1A0416	DEVU VENKATESH
17	166K1A0417	GARAPATI GOWTHAMSAGAR
18	166K1A0418	GOLAMARI VENKATA SIVA RANGA REDDY
19	166K1A0419	GOTTETI DURGA PRASAD
20	166K1A0420	GUDIWADA SURENDRA BABU
21	166K1A0421	GURRALA HARITHA SAI DURGA
22	166K1A0422	KAMISETTI PALLAVI SRI
23	166K1A0423	KARE SANDHYA RANI
24	166K1A0424	KARRI SARIKA RANI
25	166K1A0425	KUCHERLAPATI GEETHA SRI
26	166K1A0426	KURINI KALYAN
27	166K1A0427	LANKA PRASANTH KUMAR
28	166K1A0428	MUCHERLA S V P P APPAJI
29	166K1A0429	MALLADI KAMAKSHI
30	166K1A0430	MALLADI PRAMEELA DEVI
31	166K1A0431	MANTHI PRANEETHA
32	166K1A0432	MERUVA KUNDAN KUSHALKUMAR
33	166K1A0433	MUGGULLA MANI TEJA
34	166K1A0434	MUPPIDI SURESH REDDY
35	166K1A0435	NIMMAGANTI JEEVANSATYA
36	166K1A0436	OLETI TIRUMALA SHIVA VEERA REDDY
37	166K1A0437	PUSARLA ADI SIVA SAIRAM
38	166K1A0438	SESHU SAI ALEKHYA PUPPALA
39	166K1A0439	PENKE ISWARYA
40	166K1A0440	PENKE SUVARNA TEJA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 10-01-2019

## CIRCULAR

All the II B.Tech (CSE) students are hereby informed that A Two days workshop on "Distributed System" will be conducted on 24-01-2019 & 25-01-2019. All interested students can enroll their names with Mrs. M. Jayasree, Asst. Professor, CSE Dept on or before 18-01-2019. The details of topics and resource persons are available in brochure displayed in CSE department notice board.

Venue : CSE Department, Seminar Hall.

  
HOD

  
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Circulate among II B.Tech students

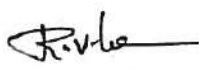
HODs are informed to circulate among concerned staff members

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# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 30-01-2019

## Program Report

**Name of the Event :** A two days workshop on DISTRIBUTED SYSTEM.

**Date / Duration :** 24-01-2019 & 25-01-2019 | 10:00 AM to 05:00 PM.

**Resource Person :** Dr. P. Vamsi Krishna Raju, prof in CSE dept.

Director, Innovations & incubations,

Swarnandhra college of engineering & Technology(Autonomous),Narsapuram.

Ph.No. : 63008 12123

Mail Id : [drpvkraj@swarnandhra.ac.in](mailto:drpvkraj@swarnandhra.ac.in)

**Name of Coordinator :** Mrs. M. Jaysree, Asst. Professor , CSE Dept.

**Number of students attended :** 53

**Objective of the event :**

- Make it easy for users to access remote resources and share them with other resources.
- Enable users to access Services and run application.

**Topics covered :**

1. Concepts of distributed System.
2. Layers of Systems and Introduction of all the systems.

**Outcome of the event :**

1. To analyse the current popular distributes systems.
2. To know about shared memory techniques.

CO-ORDINATOR

Principal

IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

HOD

## About Workshop

The main motivation of the workshop is to give hands on experience to the students on distributed systems and its concepts. What is a distributed system? It's one of those things that's hard to define without first defining many other things. Here is a "cascading" definition of a distributed system.

## Learning Goals

To be truly reliable, a distributed system must have the following characteristics:

- **Fault-Tolerant:** It can recover from component failures without performing incorrect actions.
- **Highly Available:** It can restore operations, permitting it to resume providing services even when some components have failed.
- **Recoverable:** Failed components can restart themselves and rejoin the system, after the cause of failure has been repaired.
- **Consistent:** The system can coordinate actions by multiple components often in the presence of concurrency and failure. This underlies the ability of a distributed system to act like a non-distributed system.
- **Scalable:** It can operate correctly even as some aspect of the system is scaled to a larger size. For example, we might increase the size of the network on which the system is running. This increases the frequency of network outages and could degrade a "non-scalable" system. Similarly, we might increase the number of users or servers, or overall load on the system. In a scalable system, this should not have a significant effect.
- **Predictable Performance:** The ability to provide desired responsiveness in a timely manner.
- **Secure:** The system authenticates access to data and services

	Concepts of distributed systems
DAY 1	Layers of systems and introduction of all the systems
DAY 2	Operating systems Java concepts and techniques.

## Program Committee

### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

### Patrons:

Dr. P. Chiranjeevini Kumari  
Secretary & Correspondent

### Principal:

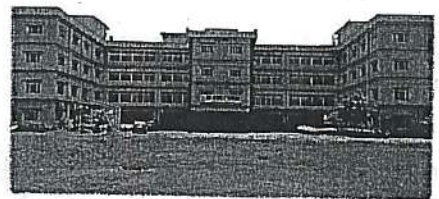
Dr. T. Srikanth

### Convenor:

Mr. M.S.R.S.Prasad, Assoc. Prof.  
HoD, CSE

### Coordinators:

Smt. S. Surya Sri, Asst. Prof.  
Smt. R.V.V.G.Lakshmi, Asst. Prof.  
Smt. S. Indira Spandana, Asst. Prof.  
Mr. R.S.Y.V.Prasad Rao Asst. Prof.  
Ms. N.D.Deepti Priya, Asst. Prof.  
Mr. M.Kiran Kumar, Asst. Prof.  
Smt. M. Jayasree, Asst. Prof.  
Smt. S.Jyothirmayee, Asst. Prof.  
Ms. P.Sai Sowjanya, Asst. Prof.



## WORKSHOP ON DISTRIBUTED SYSTEMS

24<sup>th</sup> Jan 2019 & 25<sup>th</sup> Jan 2019

Organized by  
Department of CSE

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IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

## About the College

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The Education Society is the outcome of honoured philanthropists, educationists, intellectuals and elite of Kakinada such as (Late) P. V. N. Raju, Dr. N. S. R. Sastry, Dr. P. Chiranjeevini Kumari. Ideal institutions have started from school and blossomed into group of colleges. Ideal Institutions are of its own kind in rendering service to society and value-based education.

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The ideal junior college had been started to cater to the needs of student community. In due course the jr college flourished into Degree college and P.G courses like M.B.A, M.C.A, M.A, M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job potentials of the students.

Ideal institute of technology is one and only institute which is located in the midst of Kakinada amid mango groves in the lap of Nature.

In this present era of knowledge and technology driven economy, globalization and liberalization, every

employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

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The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Distributed Systems

A distributed system is an application that executes a collection of protocols to coordinate the actions of multiple processes on a network, such that all components cooperate together to perform a single or small set of related tasks.

Why build a distributed system? There are lots of advantages including the ability to connect remote users with remote resources in an open and scalable way. When we say *open*, we mean each component is continually open to interaction with other components. When we say *scalable*, we mean the system can easily be altered to accommodate changes in the number of users, resources and computing entities.

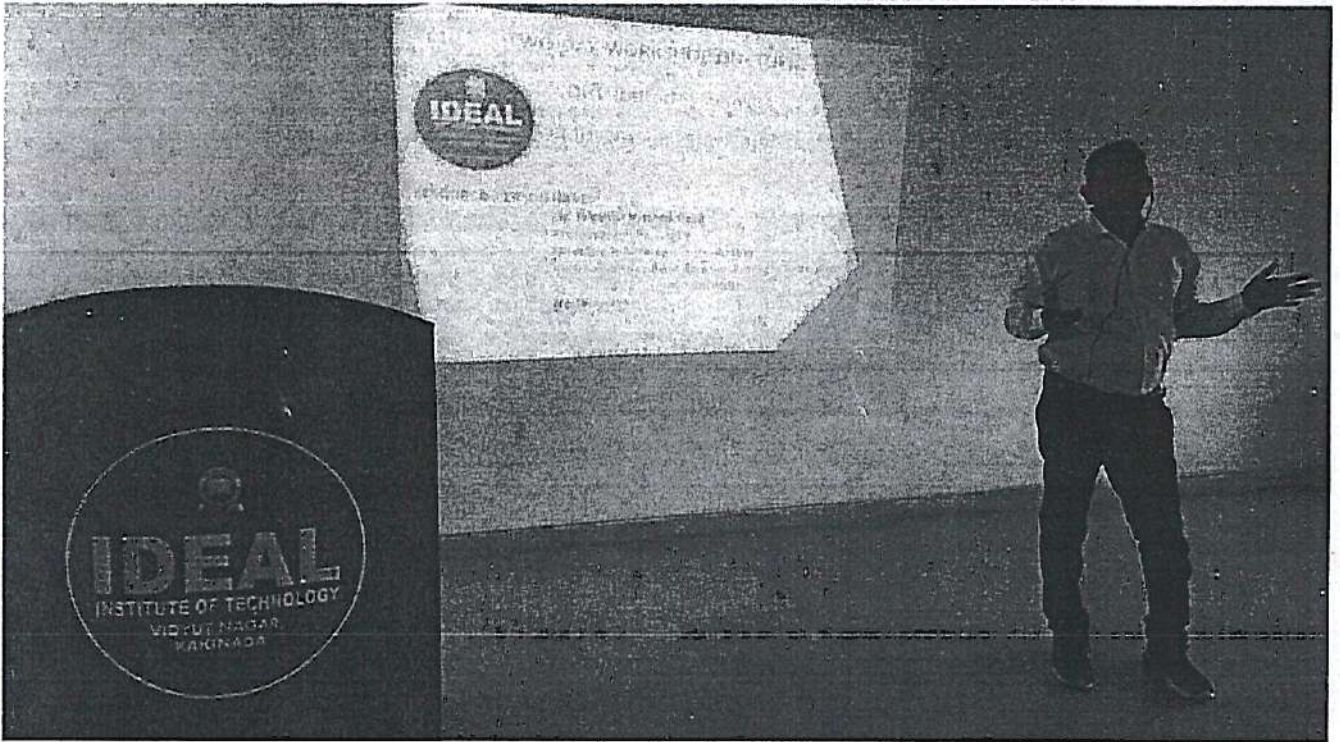
Thus, a distributed system can be much larger and more powerful given the combined capabilities of the distributed components, than combinations of stand-alone systems. But it's not easy - for a distributed system to be useful, it must be reliable. This is a difficult goal to achieve because of the complexity of the interactions between simultaneously running components.

IDEAL INSTITUTE OF TECHNOLOGY  
IV.B.TECH BRANCH: CSE

workshop on  
distributed systems

S.NO	REG.NO	STUDENT NAME
1	166K1A0501	BURRA VIGNESWARI DEVI PRIYANKA
2	166K1A0502	BADDILI KAVYA
3	166K1A0503	BATHINA SIVA YOGI PRASAD
4	166K1A0505	BHIMAMOJULA SAI RAM PAVAN
5	166K1A0507	CHEKURI LAKSHMI AISHWARYA
6	166K1A0508	CHELLE RESHMA
7	166K1A0509	CHOLLANGI JEEVANA SANDHYA PRIYA
8	166K1A0510	DASARI L SAGAR VEERA RAGHAVA ANUDEEP
9	166K1A0511	DANGETI VINITHA ARJUN RAO
10	166K1A0512	DEVADA DURGA DEVI
11	166K1A0513	DODDI SAI VASANTHA
12	166K1A0515	GADIRAJU SRIDEVI
13	166K1A0516	GOLAJAPU APARNA
14	166K1A0519	IJJAPUREDDY GAYATRI PADMA SRI VARNIKA
15	166K1A0520	JOGA S S BALA POLAYYA KRISHNA CHAKRADHAR
16	166K1A0521	KOPPISETTI LALITA
17	166K1A0522	KARRI RUPA SRAVANTHI
18	166K1A0523	SRI SAI KRITI KALIGOTLA
19	166K1A0526	KADIAM BHAVYASRI
20	166K1A0528	KANTHAMSETTI SATYA VENKATA RAJESWARI
21	166K1A0529	KARRI SUMASRI
22	166K1A0531	KHAIRUNNISA RUKHSAAR
23	166K1A0533	KOTAMSETTI RAMYA VENKATA SOWJANYA
24	166K1A0535	KUMMAMURU LAKSHMI SAILAJA
25	166K1A0536	KUPPA SRINUVASU
26	166K1A0537	MANTHENA SATYA SAI LAKSHMI SEETHA DEVI
27	166K1A0538	MYDAVOLU SATYASAI SRAVANI
28	166K1A0540	MARRIVADA SAI MOUNIKA
29	166K1A0541	MOHAMMAD TASLEEMA NASREEN
30	166K1A0542	N VENKATA GANGASWAMYAJAYCHAKRADHARARAO
31	166K1A0543	NAKKA SANDHYARANI
32	166K1A0544	NAKKA SIVANANDAM
33	166K1A0545	PALACHARLA SURYATEJA
34	166K1A0547	PATNALA VEERA VENKATA PADMAVATHI
35	166K1A0548	PERURI VEERA VENKATA SIVA SURESH
36	166K1A0550	POOSARLA PALLAVI SADANA SRIKALA
37	166K1A0551	SAGIRAJU VINEESHA
38	166K1A0552	SHAIK RUBEENA
39	166K1A0553	SHAIK THANEESHA
40	166K1A0554	TADI VENKATA BINDU SRAVYA
41	166K1A0555	TALAPOLU AMRUTHA
42	166K1A0556	TEENA SATYA SWETHA NAGA RANI
43	166K1A0557	TUMMALAPALLI SRI SINDHU
44	166K1A0560	SANTOSH PATRO
45	176K5A0501	AGANTI DIVYA
46	176K5A0502	AKUMARTHI SUNIL
47	176K5A0503	BALLA KAVYA SREE
48	176K5A0504	KAKARA SRILEKHA DEVI
49	176K5A0506	MOHAMMAD REHMAN
50	176K5A0507	MUMMIDI VEERA GOPI
51	176K5A0508	PEMMANABOIDI SASIKALA
52	176K5A0509	RAJANI JYOTHI PRASAD
53	176K5A0510	RAYUDU MOUNIKA

Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
MADHURAI KAKINADA



Dr. P. Vamsi Krishna Raja is giving lecture on Distributed Systems.

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 17-01-2019

## CIRCULAR

All the II B.Tech (CSE) students are hereby informed that A One day workshop on "Computer Networks" will be conducted on 22-01-2019. All interested students can enroll their names with Mr. M. Kiran, Asst. Professor, CSE Dept on or before 21-01-2019. The details of topics and resource persons are available in brochure displayed in CSE department notice board.

Venue : CSE Department, E-Class Room.

  
HOD

  
17/01/19  
PRINCIPAL

Circulate among II B.Tech students

HODs are informed to circulate among concerned staff members

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HOD - ME

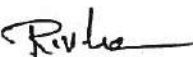
HOD - ECE

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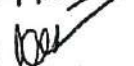




Ch. Venkatesh Kumar







  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUT NAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 24-01-2019

## Program Report

**Name of the Event :** A one day workshop on COMPUTER NETWORKS.

**Date / Duration :** 22-01-2019 | 10:00 AM to 05:00 PM.

**Resource Person :** Dr. M. Radhika Mani, Prof in CSE Dept,  
Pragathi Engineering College, Surempalem.

Ph.No. : 9440484800

Mail Id : hod\_cse@pragathi.ac.in

**Name of Coordinator :** Mr. M. Kiran, Asst. Professor , CSE Dept.

**Number of students attended :** 50

**Objective of the event :**

1. Resource Sharing.
2. Create all programs data and hardware accessible to anyone on network.

**Topics covered :**

1. Network management
2. Network Security
3. Telecom network.

**Outcome of the event :**

Explores architecture, components, functions, protocol and application of Internet and Computer networks.

CO-ORDINATOR

Principal

IDEAL INSTITUTE OF TECHNOLOGY  
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HOD

#### Components of Data Communication

1. **Message** : It is the information to be delivered.
2. **Sender** : Sender is the person who is sending the message.
3. **Receiver** : Receiver is the person to whom the message is to be delivered.
4. **Medium** : It is the medium through which message is to be sent for example modem.
5. **Protocol** : These are some set of rules which govern data communication.

#### About Workshop

The main motivation of the workshop is to give hands on experience to the students on computer networks. It is useful computer network security projects and also for simplest routing algorithms.

#### Learning Goals

The main goal of networking is "Resource sharing", and it is to make all programs, data and equipment available to anyone on the network without the regard to the physical location of the resource and the user.

A second goal is to provide high reliability by having alternative sources of supply. For example, all files could be replicated on two or three machines, so if one of them is unavailable, the other copies could be available.

Another closely related goal is to increase the systems performance as the work load increases by just adding more processors. With central mainframes, when the system is full, it must be replaced by a larger one, usually at great expense and with even greater disruption to the users.

Computer networks provide a powerful communication medium. A file that was updated or modified on a network can be seen by the other users on the network immediately.

DAY 1	CSMA and related problems
-------	---------------------------

#### Program Committee

##### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

##### Patrons:

Dr. P. Chiranjeevini Kumari  
Secretary & Correspondent

##### Principal:

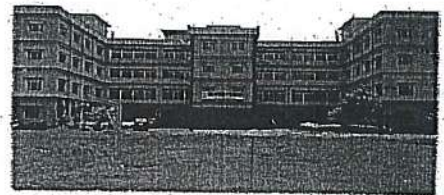
Dr. T. Srikanth

##### Convenor:

Mr. M.S.R.S.Prasad, Assoc. Prof.  
HoD, CSE

##### Coordinators:

Smt. S. Surya Sri, Asst. Prof.  
Smt. R.V.V.G.Lakshmi, Asst. Prof.  
Smt. S. Indira Spandana, Asst. Prof.  
Mr. R.S.V.V.Prasad Rao Asst. Prof.  
Ms. N.D.Deepti Priya, Asst. Prof.  
Mr. M.Kiran Kumar, Asst. Prof.  
Smt. M. Jayasree, Asst. Prof.  
Smt. S.Jyothirmayee, Asst. Prof.  
Ms. P.Sai Sowjanya, Asst. Prof.



## WORKSHOP ON COMPUTER NETWORKS

22<sup>nd</sup> January 2019

Organized by  
Department of CSE

*T.Sf*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUT NAGAR KAKINADA

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## Introduction to Computer Networks

Today the world scenario is changing. Data Communication and network have changed the way business and other daily affairs work. Now, they rely on computer networks and internetwork. A set of devices often mentioned as nodes connected by media link is called a Network. A node can be a device which is capable of sending or receiving data generated by other nodes on the network like a computer, printer etc. These links connecting the devices are called Communication channels.

Computer network is a telecommunication channel through which we can share our data. It is also called data network. The best example of computer network is Internet. Computer network does not mean a system with control unit and other systems as its slave. It is called a distributed system.

A network must be able to meet certain criteria, these are mentioned below:

1. Performance
2. Reliability
3. Scalability

S.NO	REG.NO	STUDENT NAME
1	186K1A0501	ADAPA HAMSIKA
2	186K1A0502	ADAPA SAIDURGA
3	186K1A0504	BATHINA NAGESWARA NIRDESH
4	186K1A0505	BATTU NITISH KUMAR
5	186K1A0506	BOKKA SRAVANI
6	186K1A0507	BONDADA LAKSHMI SRAVYA
7	186K1A0509	CHANDALA REVATHI
8	186K1A0511	CHODIPALLI DINESH BABU
9	186K1A0512	CHODISETTY AKSHAY
10	186K1A0513	DEYYALA MADHU
11	186K1A0517	GARAGA SATYA MOUNICA
12	186K1A0518	GARIMELLA SATYA SRINIVAS
13	186K1A0519	GUBBALA RAMYA
14	186K1A0520	KALADI SHEEBA
15	186K1A0522	KATTA SWETHA
16	186K1A0524	KONDRU SIVA PARVATHI
17	186K1A0525	KUSU ROHITHARAM
18	186K1A0526	MAGAPU HARI HARA KUMAR
19	186K1A0527	MAGAPU KEERTHANA
20	186K1A0529	MALLADI NAGA SRI DEVI
21	186K1A0530	MANDAPALLI ROSI
22	186K1A0531	MANTHAPUDI SRIKAMESWARI
23	186K1A0532	MYLAPALLI SUMANTH
24	186K1A0533	NADIMPALLI SAI NIKITHA
25	186K1A0534	NAGABATHULA POORNA PRAVEEN
26	186K1A0535	NAKKA RISHITA
27	186K1A0537	NARUKURTHI SWETHA
28	186K1A0538	NUNNA SURYA PRABHA
29	186K1A0539	PADALA VISHNU SINDHU
30	186K1A0540	PALIVELA VENKATA LOHITH
31	186K1A0541	PATTEM ALEKYA
32	186K1A0542	PENDYALA RAMYA PADMINI
33	186K1A0543	PENKE SAI LAKSHMI
34	186K1A0544	PUVVULA SAICHANDRA KEERTHANA
35	186K1A0545	REVURI SAI SUDHESHNA
36	186K1A0547	SALADI NAVYATHA
37	186K1A0548	SARILLA PRASANNA
38	186K1A0549	SATHI INDRANI
39	186K1A0550	SATHI VENKATA SAI KUMAR REDDY
40	186K1A0551	SHAIK SOFIA SAMREEN
41	186K1A0552	SINGAM BHANU
42	186K1A0554	THATAVARTHI BALANAGASAI SINDUJA
43	186K1A0556	V D NAGA VENKATA SAI SANTOSH
44	186K1A0557	VANAPALLI TARUN VEERA VENKAT SIVA
45	186K1A0558	YASARAPU VIJAYA LAKSHMI
46	186K1A0560	VARSHAPRAVALLIKA VALAVALA
47	196K5A0501	ACHANTA SATYANARAYANA
48	196K5A0504	GONDYALA SAI MURALIDHAR
49	196K5A0505	KAREDLA VIMALA KUMARI
50	196K5A0506	RAYUDU LIKHITHA MANASVI

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VIDYUTNAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

3

Date: 11-02-2019

## CIRCULAR

All the II B.Tech (ECE) students are hereby informed that A One day workshop on "Analog Communications" will be conducted on 18-02-2019. All interested students can enroll their names with Ms. N. Teja Sri, Asst. Professor, ECE Dept on or before 15-02-2019. The details of topics and resource persons are available in brochure displayed in ECE department notice board.

Venue : ECE Department, E- Class room.

  
HOD

  
11/2/19  
PRINCIPAL

Circulate among II B.Tech students

HODs are informed to circulate among concerned staff members

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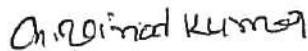
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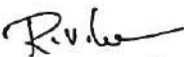
HOD - CE



HOD - EEE



HOD - ME



HOD - CSE



HOD - H&BS



  
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# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 21-02-2019

## Program Report

**Name of the Event :** A one day workshop on ANALOG COMMUNICATION .

**Date / Duration :** 18-02-2019 | 10:00 AM to 05:00 PM.

**Resource Person :** Mr. G. Rama Krishna, HOD, Aditya College of Engineering,

Ph.No. : 97012 93003

Mail Id : [hodece@acoe.edu.in](mailto:hodece@acoe.edu.in)

**Name of Coordinator :** Mrs. N. Tejasri, Asst. Professor , ECE Dept.

**Number of students attended :** 49

### **Objective of the event :**

- Familiarize with the fundamentals of analog communication.
- Familiarize with various techniques for analog modulator and demodulator.
- Familiarize with basic techniques for generating and demodulating various pulse modulated signals.

### **Topics covered :**

Modulation techniques like AM, FM, ANGLE Modulation, PWM, PPM.

### **Outcome of the event :**

- Design simple analog system for various modulation techniques.
- Analyze more characteristics of various analog modulation methods.
- Differentiate various analog modulation and demodulation schemes and their spectral characteristics.

N.T.Sri  
CO-ORDINATOR

*Alanna*  
HOD

*T.S.R.*  
Principal  
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changes in physical phenomena, such as sound, light, temperature, position, or pressure. The physical variable is converted to an analog signal by a transducer. For example, in sound recording, fluctuations in air pressure (that is to say, sound) strike the diaphragm of a microphone which induces corresponding fluctuations in the current produced by a coil in an electromagnetic microphone, or the voltage produced by a condenser microphone. The voltage or the current is said to be an "analog" of the sound.

An analog signal has a theoretically infinite resolution. In practice an analog signal is subject to electronic noise and distortion introduced by communication channels and signal processing operations, which can progressively degrade the signal-to-noise ratio (SNR). In contrast, digital signals have a finite resolution. In analog systems, it is difficult to detect when such degradation occurs. However, in digital systems, degradation can not only be detected but corrected as well.

Pulse-amplitude modulation (PAM), is a form of signal modulation where the message information is encoded in the amplitude of a series of signal pulse. It is an analog pulse modulation scheme in which the amplitudes of a train of carrier pulses are varied according to the sample value of the message signal. Demodulation is performed by detecting the amplitude level of the carrier at every single period.

There are two types of pulse amplitude modulation:

1. Single polarity PAM: In this a suitable fixed DC bias is added to the signal to ensure that all the pulses are positive.
2. Double polarity PAM: In this the pulses are both positive and negative.

Pulse-amplitude modulation is widely used in modulating signal transmission of digital data, with non-

baseband applications having been largely replaced by pulse-code modulation; and, more recently, by pulse-position modulation. In particular, all telephone modems faster than 300 bit/s use quadrature amplitude modulation (QAM). (QAM uses a two-dimensional constellation). The number of possible pulse amplitudes in analog PAM is theoretically infinite. Digital PAM reduces the number of pulse amplitudes to some power of two. For example, in 4-level PAM there are possible discrete pulse amplitudes; in 8-level PAM there are possible discrete pulse amplitudes; and in 16-level PAM there are possible discrete pulse amplitudes.

DAY 1	Pulse Amplitude Modulation
-------	----------------------------

#### Program Committee

##### Chief Patrons:

Dr. P. Krishna Rao

Chairman

##### Patrons:

Dr. P. Chiranjeevini Kumari

Secretary & Correspondent

##### Principal:

Dr. T. Srikanth

##### Convenor:

Mr. Bh.V.S.R.K.Ayyappa Kumar, Assoc.

Prof., HoD, ECE

##### Coordinators:

Mr. B.V. Kalyan Ram, Asst. Prof.

Smt. D. Vijaya Lakshmi, Asst. Prof.

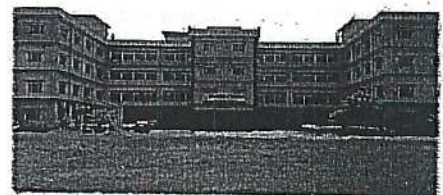
Mr. Ch. V. Prakash, Asst. Prof.

Smt. P. V. Ratnam, Asst. Prof.

Mr. V. Krishnan, Asst. Prof.

Mr. P. S.J. Sree Ram, Asst. Prof.

Ms. N. TejaSri, Asst. Prof.



WORKSHOP ON  
ANALOG COMMUNICATION

18<sup>th</sup> February 2019

Organized by  
Department of ECE

*T. S.*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

## About the College

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In this present era of knowledge and technology driven economy, globalization and liberalization, every

employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

We seek to develop in each member of the Ideal community the ability and passion to work wisely, creatively and effectively for the betterment of mankind.

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The institute is always abuzz with new ideas and is an exciting place to be, if you happen to be dreamer.

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A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

## Introduction to Analog Communication

An analog signal is any continuous signal for which the time varying feature (variable) of the signal is a representation of some other time varying quantity, i.e., *analogous* to another time varying signal. For example, in an analog audio signal, the instantaneous voltage of the signal varies continuously with the pressure of the sound waves. It differs from a digital signal, in which the continuous quantity is a representation of a sequence of discrete values which can only take on one of a finite number of values. The term analog signal usually refers to electrical signals; however, mechanical, pneumatic, hydraulic, human speech, and other systems may also convey or be considered analog signals.

An analog signal uses some property of the medium to convey the signal's information. For example, an aneroid barometer uses rotary position as the signal to convey pressure information. In an electrical signal, the voltage, current, or frequency of the signal may be varied to represent the information.

Any information may be conveyed by an analog signal, often such a signal is a measured response to

S.NO	REG.NO	STUDENT NAME
1	186K1A0401	ACHANTA KRISHNA LAKSHMI ANUSHA
2	186K1A0403	AJAY MOYYETI
3	186K1A0404	AKULA SAILAKSHMI SOWJANYA
4	186K1A0405	ANEES YASMEEN
5	186K1A0406	BATCHALA VYUHITHA
6	186K1A0410	BURADAKAVI AISHWARYA
7	186K1A0411	BHIMUNI V V B GANGADHARA SATYANARAYANA
8	186K1A0412	CHATRAGADDA PRISCILLA ANGILN
9	186K1A0413	CHELLE KAVYA SREE
10	186K1A0414	DOWRLA SARAH
11	186K1A0415	GANDEPALLI SAI POOJITHA
12	186K1A0416	GAVIREDDY TEJA
13	186K1A0418	GOLLAPALLI V V S N HIMAJA
14	186K1A0419	GUNTAMUKKALA MOUNIKA
15	186K1A0421	KADA JOSHNA GODA JAHNAVI
16	186K1A0422	KANDALA SRI SAI SWETHA
17	186K1A0423	KAPAVARAPU LAKSHMI NAGA SOWMYA
18	186K1A0424	KODURI MAHA LAKSHMI
19	186K1A0425	KOPPIREDDI VENKATESWARA RAO
20	186K1A0428	KURRE SUMANTH
21	186K1A0429	MADIREDDY SATYANARAYANAMMA
22	186K1A0430	MALLIMPUDI VINAYASRI
23	186K1A0431	MOHAMMAD ABDUL AZEEM
24	186K1A0433	MUNUKUTLA V V D S SRI RAM
25	186K1A0435	NEELAM GOWRI
26	186K1A0436	NEKKALA VINAY
27	186K1A0437	PAMPANA KARTHEEK
28	186K1A0438	PAVAN VARSHITH MANEPALLI
29	186K1A0439	PESALA TULASI LAKSHMANA KUMAR
30	186K1A0440	PILLI NAGA ANUSHA
31	186K1A0441	PINAPOTHU SEKHAR
32	186K1A0442	PITANI INDUVATHI
33	186K1A0443	PONNAGANTI AKHILA
34	186K1A0444	POTNURI CHAITANYA SAI VIKAS
35	186K1A0447	RASAMSETTI KARTHIK RAJA
36	186K1A0448	RAYUDU BHAGYA SRI
37	186K1A0449	RAYUDU DEVIKA
38	186K1A0450	REPAKA JOSHNA RATNAM
39	186K1A0451	SAMANTHAKURTHI SURYA LAKSHMI PRASANNA
40	186K1A0452	SINGULURI SAI SURYA ROOPA LAKSHMI
41	186K1A0453	SURUBHOTLA HARSHAVARDHAN
42	186K1A0454	TAIRA VIDYA NAVABAKTULA
43	186K1A0456	VEDURUPAKA ARUNA SRI
44	186K1A0458	VIDYA PENNADA
45	186K1A0459	YANAMADALA NAVYA DEEPIKA
46	196K5A0402	KARAGAPATI KUSUMA
47	196K5A0403	KORUKONDA LAKSHMI PRASANNA
48	196K5A0405	OLETI NAGA LAKSHMI
49	196K5A0406	PILLI SATYA SAI LAKSHMI

1-8

Principal



Mr. G. Rama Krishna is giving  
Lecture on Analog communications.

*i-sh*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTHAGAR KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 04-03-2019

## CIRCULAR

All the III B.Tech (EEE) students are hereby informed that **A one day workshop on "Power Electronics & Drives"** will be conducted on 11-03-2019. All interested students can enroll their names with Mr. G. Ravi Kumar, Asst. Professor, EEE Dept on or before 07-03-2019. The details of topics and resource persons are available in brochure displayed in EEE department notice board.

Venue : EEE Department, Seminar hall.

*Ch. Vinod Kumar*  
HOD

*T. S. J.*  
4/3/19  
PRINCIPAL

Circulate among III B.Tech students

HODs are informed to circulate among concerned staff members

Copy to:

AO *[Signature]*  
HOD - CE *[Signature]*  
HOD - ME *[Signature]*  
HOD - ECE *[Signature]*  
HOD - CSE *[Signature]*  
HOD - H&BS *[Signature]*

*T. S. J.*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR, KAKINADA



# IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)  
Vidyut Nagar, KAKINADA - 533 003. (A.P.)

Date: 14-03-2019

## Program Report

**Name of the Event :** A one day workshop on POWER ELECTRONICS AND DRIVES.

**Date / Duration :** 11-03-2019 | 10:00 Am to 05:00 Pm.

**Resource Person :** Dr. K. Satya Narayana, Prof & HOD-EEE, Pragati Engineering College

Ph.No. : 9000195104

Mail Id : [smkola@gmail.com](mailto:smkola@gmail.com)

**Name of Coordinator :** Mr. D. Madhhu, Asst. Professor , EEE Dept.

**Number of students attended :** 54

**Objective of the event :**

1. Speed control of AC and DC motors.
2. Latest converters based on Power Semi Conductor devices.

**Topics covered :**

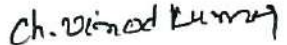
- Power electronics devices using motors.
- Synchronous and induction motors.

**Outcome of the event :**

- To control AC and DC loads using PWM technology.
- To describe the converter to topologies of various loads.



CO-ORDINATOR

  
HOD

  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR

you may develop an understanding that switching of power circuits is not only a logical operation, but also energy conversion. Every commutation in a power circuit will be accompanied with complex voltage and current transients.

1) Spurred on by technological progress and a growing concern about the efficient use of depleting energy resources, static power electronic converters employing advanced semi-conductor devices are currently used in an increasing number of applications. Such applications range from low power converters for miliwatt applications, to converters for high-voltage de transmission systems, handling hundreds of mega-watts.

2) Other classical applications include adjustable-speed ac and dc drives for use in industry, switch-mode power supplies, audio amplifiers, uninterruptible power supplies and welding machines. In the last decade, power electronics and electrical drives have deeply penetrated the renewable energy sector.

3) Today, power electronic converters provide vital functionality in a diversity of new technologies, including large wind turbine systems, fuel cells systems and photo-voltaic power generation. Further, motor drives comprising power electronic converters and advanced electric machines play a key role in the transportation sector, where new technologies are exploited in order to electrify many kinds of vehicle.

4) This specialisation combines state-of-the-art technologies with conventional technologies, for example power semiconductor devices, electronics, electro-magnetics, digital signal processors, control theory, EMC and energy technology.

5) - The themes for the study in Power Electronics and Drives provide you with in-depth understanding of the technologies and scientific disciplines involved in electric energy conversion by means of power electronic converters and electric machines.

DAY 1	Power Electronics & Drives
-------	----------------------------

#### Program Committee

##### Chief Patrons:

Dr. P. Krishna Rao  
Chairman

##### Patrons:

Dr. P. Chiranjeevini Kumari  
Secretary & Correspondent

##### Principal:

Dr. T. Srikanth

##### Convenor:

Mr. Ch. Vinod Kumar, Assoc. Prof.,  
HoD, EEE

##### Coordinators:

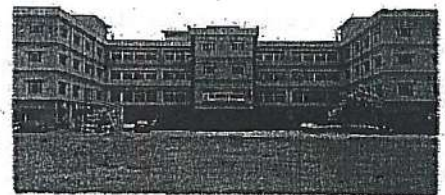
Mr. K. Venkateswara Rao, Asst. Prof.

Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.



## WORKSHOP ON POWER ELECTRONICS & DRIVES

11<sup>th</sup> March 2019

Organized by  
Department of EEE

  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA

## About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

The Education Society is the outcome of honoured philanthropists, educationists, intellectuals and elite of Kakinada such as (Late) P. V. N. Raju, Dr. N. S. R. Sastry, Dr. P. Chiranjeevini Kumari. Ideal institutions have started from school and blossomed into group of colleges. Ideal Institutions are of its own kind in rendering service to society and value-based education.

During early seventies Kakinada used to have only two colleges. Student community used to suffer a setback to upgrade their education even to the level of intermediate.

The ideal junior college had been started to cater to the needs of student community. In due course the jr college flourished into Degree college and P.G courses like M.B.A, M.C.A, M.A, M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job potentials of the students.

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## Introduction to Power Electronics & Drives

Power electronics and electric drives are two fields of technology, very closely connected. Power electronics devices are the most advanced and effective switches of high power electric circuits. Frequently, they are used as switches for the commutation of windings of electric machines. The system which includes an electric machine, a power converter, based on semiconductor switches, and several control devices is known as an electric drive. Today about 60 % of the electric energy produced will be converted and used by electric drives of technological machines.

The flexible control of speed and torque of electric machines is possible due to modern microprocessor control and advanced power electronics devices. Optimal motion control of technological machines is a very urgent problem, the solution which will lead to higher productivity of machines and essential energy saving.

This seminar is intended for students of universities of applied sciences and remote engineering colleges who study the courses of electronics. It is also useful for

A one day workshop on power Electronics & Drives

IDEAL INSTITUTE OF TECHNOLOGY :: KAKINADA  
III. B.TECH:: EEE-STUDENTS FEE DETAILS FOR THE ACADEMIC YEAR 2018-19

S. NO.	ADMISSION NO.	NAME OF THE STUDENT
1	166K1A0201	AMBALLA SARAN BABISH
	166K1A0202	B RAJESH
2	166K1A0203	BIRUDA SIVASAI MANIKANTA
3	166K1A0204	BOMMANA CHITTI BABU
4	166K1A0205	CHINTALA SATYA BHAVANA
5	166K1A0206	CHITNEEDI NAGA VEERA VENKATA SAI SUPRIYA
6	166K1A0207	DASARI KRISHNARJUNA
7	166K1A0208	GANDHAM ARUN SAI
8	166K1A0209	GANGIRI KALYAN
9	166K1A0210	GANGUMALLA USHA NAGA DEVI
10	166K1A0211	GANJI MEENAKSHI SRI SAI
11	166K1A0212	KOCHERLA NAGA VARAPRABHAKAR
12	166K1A0213	M NANDU VARMA
13	166K1A0214	MADIBOYINI MANIKANTA
14	166K1A0215	N VEERA BABU
15	166K1A0216	P SIVA GANGADHAR
16	166K1A0217	PINDIPROLI BHANU LAHARI
17	166K1A0218	VELUGUBANTLA VENKAT
18	166K1A0219	YARRA SAI GIRISH
19	176K5A0201	AMALADASU RAJA MAHESH
20	176K5A0202	ANUSURI RAJU
21	176K5A0203	APPABATHULA SUBHASH
22	176K5A0204	CHEKKA AJAY
23	176K5A0205	GANTA VENKATA RAMA MANIKANTA
24	176K5A0206	GUDIMUDI MANOJ
25	176K5A0207	JOGI AJAY KUMAR
26	176K5A0208	KOMTHAM RAJA
27	176K5A0209	KOPPISETTI DEVISRI
28	176K5A0210	MARRI JYOTHI KUMAR
29	176K5A0211	MEDISETTI REVANTH
30	176K5A0212	MURAPAKA. PRABHATH SURYA BHAGAVAN
31	176K5A0213	P GANGA RAJU
32	176K5A0214	PEPAKAYALA SIVACHAKRA PHANI
33	176K5A0215	PERUMALLA JOY
34	176K5A0217	PONNADA KRANTHIBABU
35	176K5A0218	PUNYAMANTHULA SAI MANIKANTA
36	176K5A0219	SAMANASI SATISH
37	176K5A0220	SANDAKA VEERABABU
	176K5A0221	SHAIK ZIAUDDIN
38	176K5A0222	SHEIK BASHEER JOHNY
39	176K5A0223	VAKAPALLI ANIL KUMAR
40	176K5A0224	VARUPULA VEERA VENKATA SATYA NAVEEN
41	176K5A0225	VASAMSETTI CHANDRA KIRAN
42	176K5A0226	VUAYAROWTHU ANAND SAI KUMAR

Principal

IDEAL INSTITUTE OF TECHNOLOGY  
VEMURINAGAR KAKINADA

5  
4/2

S. NO.	ADMISSION NO.	NAME OF THE STUDENT
43	176K5A0227	VONTEDDU VENKATA NAGA VAMSI
44	176K5A0228	YANDRA MOHAN KUMAR
45	176K5A0229	CHALLAGALI VIJAYA RAMARAJU
46	176K5A0230	CHEKKA CHANDANA SAI SUSHMA
47	176K5A0231	GOLLAPALLI SIMHADRIESWARARAO
48	176K5A0232	KOTHAPALLI NAVEEN
	176K5A0233	NALAM SIVA SATYANARAYANA
49	176K5A0234	KOTIPALLI PAVAN KUMAR
50	176K5A0235	KOTIPALLI VEERA VENKATA SATYANARAYANA
51	176K5A0237	DHALI RAMA MADHAVI (SPOT)
52	176K5A0238	ISUKAPATLA SIVARAM KISHORE (SPOT)
53	176K5A0239	P AJAY KUMAR (SPOT)
54	176K5A0240	RANGALA MUKESH KUMAR (SPOT)



**ONE DAY WORKSHOP  
ON  
“POWER ELECTRONICS & APPLICATIONS”**

**DATE: 11-03-2019**

**RESOURCE PERSON NAME:**

**Dr. K.SATYA NARAYANA**

Professor & HoD- EEE Dept,

Pragati Engineering College,

Surampalem.

*TESK*  
Principal  
IDEAL INSTITUTE OF TECHNOLOGY  
VIDYUTNAGAR KAKINADA