

2016-17

from the jacks to exert a compressive force in the concrete. Once the concrete has reached the desired strength, the tensioned wires are released from the jacks. A typical concrete strength of 28 N/mm² can be achieved by 24-hour steam curing, as well as using additives. To create shorter members, dividing plates can be placed at any point along the member which, when removed, permit the cutting of the wires.

Post-tensioning:

This follows the reverse method to pre-tensioning, whereby the concrete member is cast and the prestressing occurs after the concrete is hardened. This method is often used where stressing is to be carried out on site after casting an insitu component or where a series of precast concrete units are to be joined together to form the required member. The wires, cables or bars may be positioned in the unit before concreting, but bonding to the concrete is prevented by using a flexible duct or rubber sheath which is deflated and removed when the concrete has hardened. Stressing is carried out after the concrete has been cured by means of hydraulic jacks operating from one or both ends of the member. Due to the high local stresses at the anchorage positions it is common for a helical (spiral) reinforcement to be included in the design. When the required stress has been reached, the wire or cables are anchored to maintain the prestress. The ends of the unit are sealed with cement mortar to prevent corrosion due to any entrapped moisture and to assist in stress distribution. Anchorages used in post-tensioning depend on whether the tendons are to be stressed individually or as a group. Most systems use a form of split cone wedges or jaws which act against a form of bearing or pressure plate

DAY 1

Estimation of Prestress force distribution in the multi strand system of pre stressed concrete structures

Program Committee

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Dr. P. Krishna Rao
Chairman

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Dr. P. Chiranjeevini Kumari
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HoD, CIVIL

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Kum K. Lakshmi Satya, Asst. Prof.
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Kum R. Gayathri Devi, Asst. Prof.



ONE DAY WORKSHOP ON
"PRESTRESSED CONCRETE"

08 July 2016

Organized by
Department of CIVIL

About the College

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Introduction to Prestressed Concrete

Prestressed concrete is a structural material that allows for predetermined, engineering stresses to be placed in members to counteract the stresses that occur when they are subject to loading. It combines the high strength compressive properties of concrete with the high tensile strength of steel.

In ordinary reinforced concrete, stresses are carried by the steel reinforcement, whereas prestressed concrete supports the load by induced stresses throughout the entire structural element. This makes it more resistant to shock and vibration than ordinary concrete, and able to form long, thin structures with much smaller sectional areas to support equivalent loads. Prestressed concrete was patented by San Franciscan engineer P.H Jackson in 1886, although it did not emerge as an accepted building material until 50 years later when a shortage of steel, coupled with technological advancements, made prestressed concrete the building material of choice during European post-war reconstruction. It is now commonly used for floor beams, piles and railways sleepers, as well as structures such as bridges, water tanks, roofs and runways. Generally, prestressed concrete is not necessary for columns and walls, however, it can be used economically for tall columns and high retaining walls with high bending stresses.

Topics

Pre-tensioning

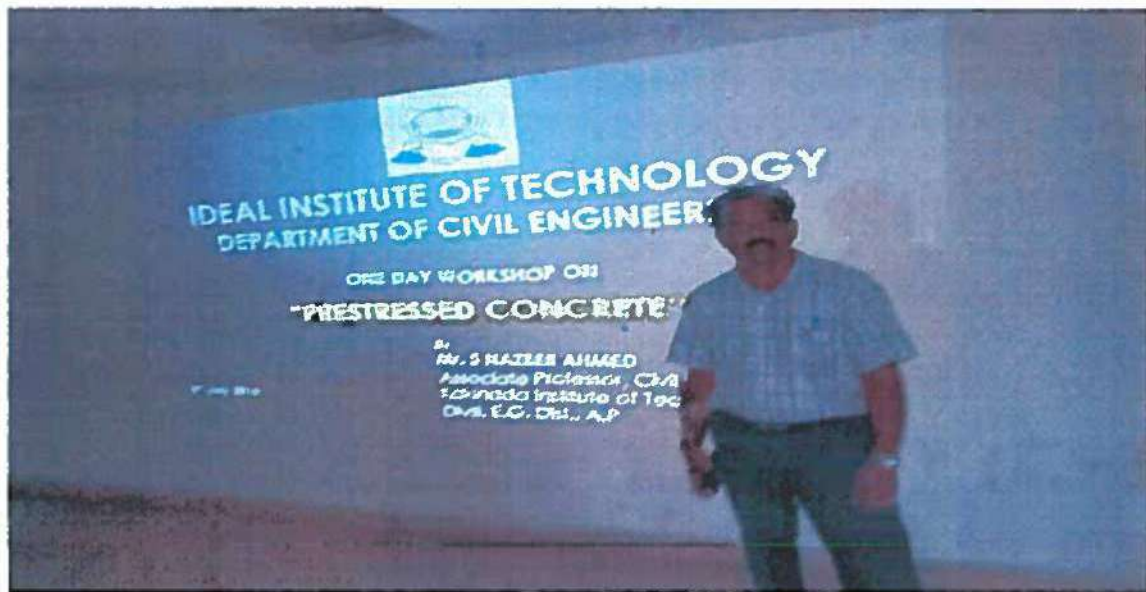
This process involves the stressing of wires or cables by anchoring them at the end of a metal form, which may be up to 120 m in length. Hydraulic jacks stress the wire as required, often adding 10% to accommodate creep and other pre-stress losses that may be incurred. Side moulds are then fixed and the concrete placed around the tensioned wires. The concrete hardens and shrinks, gripping the steel along its length, transferring the tension



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(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. S Nazeer ahmed is giving lecture on Prestressed Concrete.

P. Reddy
IQAC

T. S. H.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

About Workshop

Department of Computer Science Engineering organized workshop on "C Programming". The main focus is to train the student. This workshop was based on C Programming concepts. The workshop was specifically meant for the students, with a view to make the students gain in-depth knowledge in C programming. The aim of workshop was to develop programming skills among students.

Learning Goals

Learning C programming has lot of benefits, but the foremost thing in which it helps is to understand the underlying architecture of how things works?

It is said that 'C' is a god's programming language. One can say, C is a base for the programming. If you know 'C,' you can easily grasp the knowledge of the other programming languages that uses the concept of 'C'

To provide complete knowledge of C language. Students will be able to develop logics which will help them to create programs, applications in C. Also by learning the basic programming constructs they can easily switch over to any other language in future. After the completion of this workshop, the students will be able to develop applications.

DAY 1	Biscis & Features of C Language
DAY 2	Functions,,Pointers. Files

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Convenor:

Mr. M.S.R.S.Prasad, Assoc. Prof.
HoD, CSE

Coordinators:

Smt. S. Surya Sri, Asst. Prof.
Smt. R.V.V.G.Lakshmi, Asst. Prof.
Smt. S. Indira Spandana, Asst. Prof.
Ms. N.D.Deepthi Priya, Asst. Prof.



TWO DAY WORKSHOP ON " C PROGRAMMING"

18th Aug 2016 to 19th Aug 2016

Organized by
Department of CSE

About the College

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Introduction to C Programming

The C language is one of the most popular programming language of all time, due to its simplicity and easy to understand features and functions it is used and utilized on the global platforms.

The C language which is used in developing system application and software that can be used in both high level and low level language. The importance of C Programming language grew over a span of time as it was capable of developing system software and operating system.

The C language is a computer programming language used to create software, desktop applications, operating systems, etc.



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Vidyut Nagar, KAKINADA-533 003. (A.P.)



Mr A. Radhakrishna is giving lecture on C-Programming

A. Radhakrishna
IQAC

T. S. H.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

changes in physical phenomena, such as sound, light, temperature, position, or pressure. The physical variable is converted to an analog signal by a transducer. For example, in sound recording, fluctuations in air pressure (that is to say, sound) strike the diaphragm of a microphone which induces corresponding fluctuations in the current produced by a coil in an electromagnetic microphone, or the voltage produced by a condenser microphone. The voltage or the current is said to be an "analog" of the sound.

An analog signal has a theoretically infinite resolution. In practice an analog signal is subject to electronic noise and distortion introduced by communication channels and signal processing operations, which can progressively degrade the signal-to-noise ratio (SNR). In contrast, digital signals have a finite resolution. In analog systems, it is difficult to detect when such degradation occurs. However, in digital systems, degradation can not only be detected but corrected as well.

Fundamentals of modulation, deals with signal modulation where the message information is encoded in the amplitude of a series of signal pulse. Pulse amplitude modulation scheme in which the amplitudes of a train of carrier pulses are varied according to the sample value of the message signal. Demodulation is performed by detecting the amplitude level of the carrier at every single period.

There are two types of pulse amplitude modulation:

1. **Single polarity PAM:** In this a suitable fixed DC bias is added to the signal to ensure that all the pulses are positive.
2. **Double polarity PAM:** In this the pulses are both positive and negative.

Pulse-amplitude modulation is widely used in modulating signal transmission of digital data, with non-baseband applications having been largely replaced by pulse-

code modulation, and, more recently, by pulse-position modulation. In particular, all telephone modems faster than 300 bit/s use quadrature amplitude modulation (QAM). (QAM uses a two-dimensional constellation).The number of possible pulse amplitudes in analog PAM is theoretically infinite. Digital PAM reduces the number of pulse amplitudes to some power of two. For example, in 4-level PAM there are possible discrete pulse amplitudes; in 8-level PAM there are possible discrete pulse amplitudes; and in 16-level PAM there are possible discrete pulse amplitudes.

Topic	Fundamentals of Modulation
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Program Comittee

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Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

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Dr. T. Srikanth

Convenor:

Mr. Bh.V.S.R.K.Ayyappa Kumar, Assoc. Prof., HoD, ECE

Coordinator:

Smt. P. V. Ratnam, Asst. Prof.

Resource Person:

Mr. N.Rajesh Babu, Assoc. Prof. Dept. of ECE,
Aditya College of Engineering and Technology



**WORKSHOP ON
ANALOG COMMUNICATION**

9th March 2017

**Organized by
Department of ECE**

About the College

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Introduction to Analog Communication

An analog signal is any continuous signal for which the time varying feature (variable) of the signal is a representation of some other time varying quantity, i.e., *analogous* to another time varying signal. For example, in an analog audio signal, the instantaneous voltage of the signal varies continuously with the pressure of the sound waves. It differs from a digital signal, in which the continuous quantity is a representation of a sequence of discrete values which can only take on one of a finite number of values. The term analog signal usually refers to electrical signals; however, mechanical, pneumatic, hydraulic, human speech, and other systems may also convey or be considered analog signals.

An analog signal uses some property of the medium to convey the signal's information. For example, an aneroid barometer uses rotary position as the signal to convey pressure information. In an electrical signal, the voltage, current, or frequency of the signal may be varied to represent the information.

Any information may be conveyed by an analog signal, often such a signal is a measured response to



IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. N Rajesh Babu is giving lecture on Analog Communications

P. Radhika
IQAC

T. S. R.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

2017-18

About Workshop

The motivation behind this workshop is to give hands on learning experience to the students. Learners will gain the working knowledge on how to secure a database and pl/sql concepts

Learning Goals

Create database-level applications using Oracle PL/SQL. Participants will be exposed to various advanced PL/SQL techniques. Exercises and lab sessions reinforce the learning objectives and provide participants the opportunity to gain practical hands-on experience.

Database security refers to dedicated means used to protect data stored in databases or database management software from hacker attacks and insider-driven threats. It includes a multitude of processes, tools and strategies that ensure security of a database environment.

The goal of database security is the protection of data against threats such as accidental or intentional loss, destruction or misuse. These threats pose problems to the database integrity and access.

Students will learn and practice the skills needed to implement these components in real-world automated data collection scenarios. The course is delivered in a workshop format that enables students to learn at their own pace.

DAY 1	Purpose & Security in DBMS
DAY 2	PL/SQL Concepts

Program Committee

Chief Patrons:

Dr. P. Krishna Rao
Chairman

Patrons:

Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

Principal:

Dr. T. Srikanth

Convenor:

Mr. M.S.R.S.Prasad, Assoc. Prof.
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Smt. S. Indira Spandana, Asst. Prof.
Smt. S. Jyothirmayee, Asst. Prof.
Ms. N.D.Deepti Priya, Asst. Prof.
Mr. T.Srinivasulu, Asst. Prof.
Ms. N.Veeramani, Asst. Prof.
Mr. R.S.V.V.Prasad Rao, Asst. Prof.



TWO DAY WORKSHOP ON "DATABASE MANAGEMENT SYSTEM"

20th Jun 2017 to 21st Jun 2017

**Organized by
Department of CSE**

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Introduction to Data Base Management System

The database management systems are highly essential for organizations in managing different databases and retrieving relevant information. This management system is used in various organizations as an interface that helps users in forming a connection with the databases. Additionally, it helps in organizing data in a way that it can be accessed easily.

The data obtained and stored by the organizations play a crucial role in targeting their goals and help in forming their business strategy. A database management system can store, organize, and monitor big or small information, just by using one software application. The use and implementation of a data system can help businesses in performing well and allows them to save overall costs.



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. A. Radhakrishna is giving lecture on Data base Management System

P. Radhika
IQAC


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IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

(that is to say, sound) strike the diaphragm of a microphone which induces corresponding fluctuations in the current produced by a coil in an electromagnetic microphone, or the voltage produced by a condenser microphone. The voltage or the current is said to be an "analog" of the sound.

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Systems:

A system is defined by the type of input and output it deals with. Since we are dealing with signals, so in our case, our system would be a mathematical model, a piece of code/software, or a physical device, or a black box whose input is a signal and it performs some processing on that signal, and the output is a signal. The input is known as excitation and the output is known as response.

Conversion of analog to digital signals

Since there are lot of concepts related to this analog to digital conversion and vice-versa. We will only discuss those which are related to digital image processing. There are two main concepts that are involved in the conversion.

- ♦ Sampling
- ♦ Quantization

Topics Covered:

- Basic Signals
- Analog and Digital Signals
- Sampling
- Quantization
- Systems
- Continuous Systems
- Discrete Systems

Topic	Signals and Systems
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Program Committee

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Mr. Bh.V.S.R.K.Ayyappa Kumar, Assoc. Prof., HoD, ECE

Coordinator:

Smt. VS Krishnan, Asst. Prof.

Resource Person:

Dr. M.Radhakamani, HOD
Dept. of CSE,
Pragati Engineering College



**WORKSHOP ON
SIGNALS AND SYSTEMS**

8th September 2017

**Organized by
Department of ECE**

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Introduction to Signals and Systems

In electronics and communication engineering, the fundamental quantity of representing some information is called a signal. It does not matter what the information is i.e. Analog or digital information. In mathematics, a signal is a function that conveys some information. In fact any quantity measurable through time over space or any higher dimension can be taken as a signal. A signal could be of any dimension and could be of any form.

An analog signal uses some property of the medium to convey the signal's information. For example, an aneroid barometer uses rotary position as the signal to convey pressure information. In an electrical signal, the voltage, current, or frequency of the signal may be varied to represent the information.

Any information may be conveyed by an analog signal, often such a signal is a measured response to changes in physical phenomena, such as sound, light, temperature, position, or pressure. The physical variable is converted to an analog signal by a transducer. For example, in sound recording, fluctuations in air pressure



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Dr. M Radhika Mani is giving lecture on Signals and Systems

Radhika
IQAC

T. S. R.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

you may develop an understanding that switching of power circuits is not only a logical operation, but also energy conversion. Every commutation in a power circuit will be accompanied with complex voltage and current transients.

1) Spurred on by technological progress and a growing concern about the efficient use of depleting energy resources, static power electronic converters employing advanced semi-conductor devices are currently used in an increasing number of applications. Such applications range from low power converters for miliwatt applications, to converters for high-voltage dc transmission systems, handling hundreds of mega-watts.

2) Other classical applications include adjustable-speed ac and dc drives for use in industry, switch-mode power supplies, audio amplifiers, uninterruptible power supplies and welding machines. In the last decade, power electronics and electrical drives have deeply penetrated the renewable energy sector.

3) Today, power electronic converters provide vital functionality in a diversity of new technologies, including large wind turbine systems, fuel cells systems and photovoltaic power generation. Further, motor drives comprising power electronic converters and advanced electric machines play a key role in the transportation sector, where new technologies are exploited in order to electrify many kinds of vehicle.

4) This specialisation combines state-of-the-art technologies with conventional technologies, for example power semiconductor devices, electronics, electromagnetics, digital signal processors, control theory, EMC and energy technology.

5) The themes for the study in Power Electronics and Drives provide you with in-depth understanding of the technologies and scientific disciplines involved in electric energy conversion by means of power electronic converters and electric machines.

DAY 1	Industrial Starters of 3-phase Induction Motors
-------	---

Program Committee

Chief Patrons:

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Chairman

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Secretary & Correspondent

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Mr. A. Ramesh, Asst. Prof.

Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.



WORKSHOP ON POWER ELECTRONICS & DRIVES

16 September 2017

Organized by
Department of EEE

About the College

Ideal Institute of Technology was established under the aegis of Governing Body of Ideal college of Arts and Sciences during academic year 2009-2010. The educational Society of Ideal Institutions has been rendering yeoman service in the glorious field of education for the past 40 years.

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employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

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The institute is always abuzz with new ideas and is an exciting place to be, if you happen to be a dreamer.

The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

Introduction to Industrial Starters of Induction Motors and Drives

Power electronics and electric drives are two fields of technology, very closely connected. Power electronics devices are the most advanced and effective switches of high power electric circuits. Frequently, they are used as switches for the commutation of windings of electric machines. The system which includes an electric machine, a power converter, based on semiconductor switches, and several control devices is known as an electric drive. Today about 60 % of the electric energy produced will be converted and used by electric drives of technological machines.

The starting and flexible control of speed and torque of electric machines is possible due to modern microprocessor control and advanced power electronics devices. Optimal motion control of technological machines is a very urgent problem, the solution which will lead to higher productivity of machines and essential energy saving.

This seminar is intended for students of universities of applied sciences and remote engineering colleges who study the courses of electronics. It is also useful for

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THEME OF THE LECTURE

This lecture will provide a platform for academicians, Researchers, Scientists and industry personnel from industry to come together and share the latest developments in broad areas of Thermodynamics in Mechanical Engineering.

The concept of heat arises from that particular sensation of warmth and coldness which is immediately experienced on touching a body. This direct sensation, however, furnishes no quantitative scientific measure of a body's state with regard to heat; it yields only qualitative results, which vary according to external circumstances.

Although the freezing of liquids and melting of crystals are fundamental for many areas of the sciences, even simple properties like the temperature -pressure relation along the melting line cannot be predicted today. However we can present a theory in which properties of the coexisting crystal and liquid phases at a single thermodynamic state point provide basis for calculating the pressure, density and entropy.

RESOURCE PERSONS

1. **Dr. P.KUMAR BABU**
Professor, Dept. of ME
Pragati Engineering College, Surampalem

GUEST LECTURE ON RECENT TRENDS IN THERMODYNAMICS

16th September 2017



Organized by

Department of Mechanical Engineering

IDEAL INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi;

Affiliated to JNTUK, Kakinada,

&

Vidyut Nagar, Kakinada, E.G.Dt.,
Andhra Pradesh, India- 533 003

Ph:

Fax: 08852-252232

Website: www.idealtech.edu.in

Email: principal@idealtech.edu.in

**GUEST LECTURE
ON
RECENT TRENDS IN MECHANICAL
ENGINEERING**

16th September, 2017

REGISTRATION FORM

(Make copies if desired)

Name : _____

Date of Birth : _____

Designation : _____

Department : _____

Organization : _____

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Phone : _____

E-mail : _____

DD No : _____

Date : _____

Signature of the
Applicant

ORGANISING COMMITTEE

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Principal & Professor, Dept. of ME

Co- Coordinator

Mrs. K.Aruna
Head, Dept. of ME

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Mr. M.S.R.S. Prasad

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Mr.M.Dedeeppya
Mr. N. Raja
Mrs. R. Vijaya Lakshmi
Ms. SK.Admsha
Mr. B.Dharma Kumar
Mr. K.S.Rambabu

ATTENDEES

This seminar is intended for Academicians,
Researchers, Industry Personnel and PG
Students

REGISTRATION FEE: NIL

IMPORTANT DATES:

Submission of Application : 01-09-17
Intimation of Selection : 03-09-17
Confirmation by Participant : 05-09-17

MODE OF PAYMENT

Participants are requested to send their filled
registration form along with Demand Draft drawn in
favor of "Ideal Institute of Technology," payable at
Kakinada. Spot payment is allowed, but registration
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MAILING ADDRESS

The Coordinator,
Department of ME
Vidyut Nagar, Kakinada, East Godavari District,
Andhra Pradesh, India-533003
principal@idealttech.edu.in

*For any further clarifications please feel free to
contact*

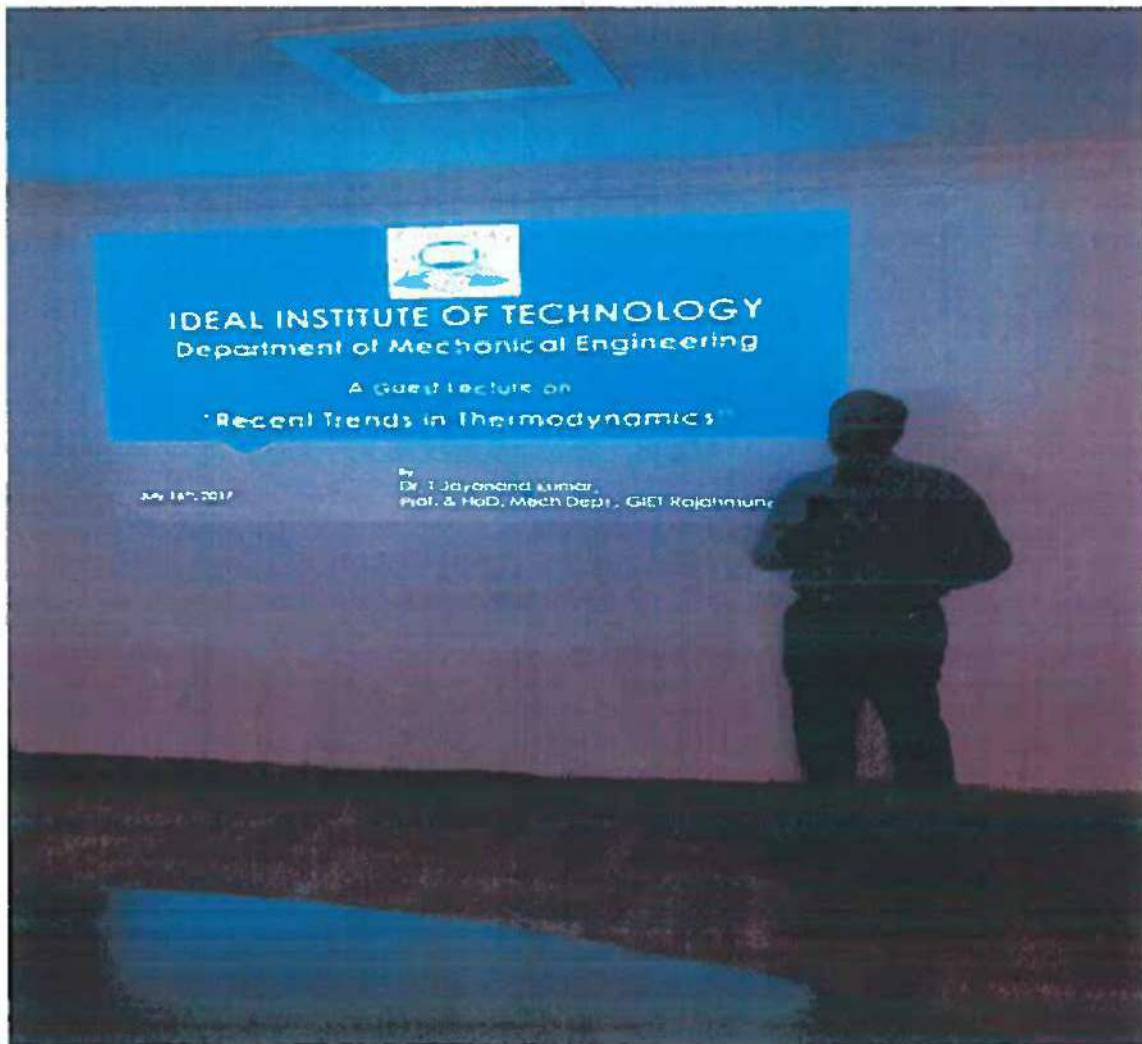
Mrs. K. Aruna
Head of the Department
Department of ME
Coordinator - +91 9949305868



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. T. Jayanand Kumar is giving lecture on Recent Trends on Thermodynamics

P. Radhika
IQAC

T-82

PRINCIPAL

Principal

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VIDYUTNAGAR, KAKINADA

Use in Construction:

Many different types of structures and components of structures can be built using reinforced concrete including slabs, walls, beams, columns, foundations, frames and more. Reinforced concrete can be classified as precast or cast-in-place concrete. Designing and implementing the most efficient floor system is key to creating optimal building structures. Small changes in the design of a floor system can have significant impact on material costs, construction schedule, and ultimate strength, operating costs, occupancy levels and end use of a building. Without reinforcement, constructing modern structures with concrete material would not be possible.

Properties of RCC

1. It should have sufficient compressive strength.
2. It should have Adequate tensile strength.
3. High Fire and weather resistance.
4. should be a Durability material.
5. Economy to molded any shape.
- 6 .Low maintenance cost.
7. The economy as a construction material.
8. Less deflection

DAY 1	Design & its Applications
DAY 2	Drawings

Program Committee

Chief Patrons:

Dr. P. Krishna Rao
Chairman

Patrons:

Dr. P. Chiranjeevi Kumari
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HoD, CIVIL

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Kum B.N. Satya Devi, Asst. Prof.
Kum K.Sumavalli Kalahsati, Asst. Prof.
Sri B. leela Krishna, Asst. Prof.
Sri M.Venkata Rao, Asst. Prof.



TWO DAY WORKSHOP ON
"DESIGN AND DRAWING OF REINFORCED
CONCRETE STRUCTURES"

18th Dec 2017 to 19th Dec 2017

Organized by
Department of CIVIL

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Introduction to Design and Drawing of Reinforced Concrete Structures

In the design and analysis of reinforced concrete members, you are presented with a problem. Unfamiliar to most of you: "The mechanics of members consisting of two materials." To Compound this problem, one of the materials (concrete) behaves differently in tension than in

Compression, and may be considered to be either elastic or inelastic, if it is not neglected entirely.

Although we will encounter some peculiar aspects of behavior of concrete members, we will Usually be close to a solution for most problems if we can apply the following three basic ideas:

- Geometry of deformation of sections will be consistent under given types of loading; i.e., moment will always cause strain to vary linearly with distance from neutral axis,
- Mechanics of materials will allow us to relate stresses to strains.
- Sections will be in equilibrium: external moments will be resisted by internal

Moment, external axial load will be equal to the sum of internal axial forces. (Many New engineers overly impressed speed and apparent accuracy of modern structural Analysis computational procedures think less about equilibrium and details).

We will use some or all of these ideas in solving most of the analysis problems we will have in Design of members and structures of reinforced concrete is a problem distinct from but closely Related to analysis. Strictly speaking, it is almost impossible to exactly analyze a concrete

Structure, and to design exactly is no less difficult. Fortunately, we can make a few fundamental assumptions which make the design of reinforced concrete quite simple, if not easy.



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. M, Anjan kumar is giving lecture on Design and Drawing of Reinforced Concrete Structure

P. Madhuk
IQAC

T. S.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

2018-19

JAYARAM HERO



11/07/2018

TO,

THE PRINCIPAL,

KAKINADA.

RESPECTED SIR

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SO WE REQUEST YOU TO GIVE US PERMISSION TO CONDUCT * SAFETY AWARENESS PROGRAME * IN YOUR COLLEGE FOR YOUR STUDENTS

THANKING YOU

JAYARAM HERO

K. Sundar Rao

KAKINADA

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ABOUT MECHANICAL DEPARTMENT

The department is strengthened with one Doctorate, nine Post Graduates as Out of which two staff members have industrial experience in reputed organizations. Machine Design, Piping & Inspection. Not only have the teaching staff, the technicians also had tremendous technical experience. The department is equipped with ISO certified laboratory equipment and sophisticated teaching aids like day light projector, e-Class room etc.

THEME OF THE LECTURE

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RESOURCE PERSON

1. Sri V Siva Prasad,
B.Tech, M.Tech (IIT MADRAS)
Faculty ACE coaching center, Hyderabad

Mobile No-

AN OVER VIEW ON THERMODYNAMICS

2, 3rd August 2018



Organized by

Department of Mechanical Engineering

IDEAL INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi;

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&

Vidyut Nagar, Kakinada, E.G.Dt.,
Andhra Pradesh, India- 533 003

Ph:

Fax: 08852-252232

Website: www.idealtech.edu.in

Email: principal@idealtech.edu.in

**NATIONAL SEMINAR
ON
RECENT TRENDS IN MECHANICAL
ENGINEERING**

2, 3rd August 2018

REGISTRATION FORM

(Make copies if desired)

Name : _____

Date of Birth : _____

Designation : _____

Department : _____

Organization : _____

Address for Communication: _____

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DD No : _____

Date: _____ Signature of the
Applicant

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Sri P.Kiran
Treasurer

Coordinator'

Dr. T. Srikanth
Principal & Professor, Dept. of ME

Co-Coordinator

Mrs.K.Aruna
Head, Dept. of ME

Advisory Committee

Dr.G.Krishnam Raju, *Director*
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Dr. Srihari Varma
Mr. Bh.Ayyappa Kumar
Mr. Ch.Vinod Kumar
Mr. M.S.R.S. Prasad

Organizing Committee

Mr.M.Dedeepya
Mr. N. Raja
Mrs. R. Vijaya Lakshmi
Mrs.J.L.Prathyusha
Mr.B.Dharma Kumar
Mr.K.S.Rambabu
Mr.N.Krishnakanth

ATTENDEES

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Students

REGISTRATION FEE: NIL

IMPORTANT DATES:

Submission of Application : 27-07-18
Intimation of Selection : 28-07-18
Confirmation by Participant : 30-07-18

MODE OF PAYMENT

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MAILING ADDRESS

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Department of ME
Vidyut Nagar, Kakinada, East Godavari District,
Andhra Pradesh, India-533003
principal@idealtch.edu.in

*For any further clarifications please feel free to
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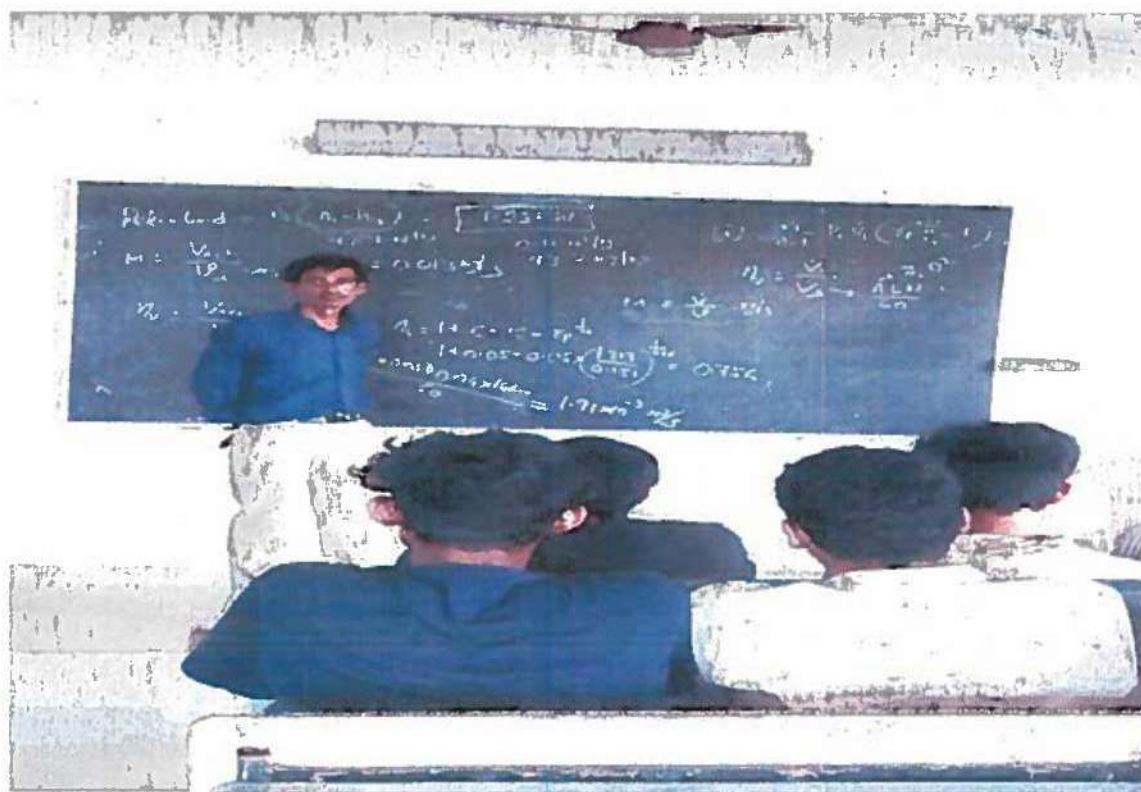
Mrs.K. Aruna
Head of the Department
Department of ME
Coordinator - +91 9949305868



IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. V. Siva Prasad is giving a Lecture on Overview of Thermodynamics.

P. Reddy
IQAC

T. S. R.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

Learning Goals

1. To Learn that Memory System for computers uses a hierarchy of Memories appropriately organized to Provide applications cost effective overall memory with high capacity and fast access.

2. To Identify that RAM uses DRAMs which are low cost, high capacity solid state memory. Their Access time is around 50ns. To get a Faster Access it is combined with a small SRAM memory called cache memory. Cache is smaller but faster than DRAM main memory. The Management of main memory is critical to the computer system.

3. To Analyze how Programs usually follow a pattern of Memory access in which Instructions stored in a small range of addresses are frequently referenced. Instruction accesses and Data Accesses are clustered in two distinct range of addresses. This is called Locality of Reference. This fact is used to keep frequently referenced instructions and data in small fast memories called caches. Normally there is a Data cache and an Instruction Cache.

4. To Learn a commonly used method in the design of memory systems called Virtual Memory. A virtual memory provides a user with a large logical memory address space to write programs even though the available physical memory (DRAM) may be small. This is achieved by Combining DRAM and a magnetic memory in such a way that the memory cycle time of the combined system is closer to DRAM cycle time while a large storage space provided by a magnetic disk.

5. To learn how computers use both cache and virtual memory effectively for memory management. Hardware and Operating System cooperate to achieve this.

6. To learn Various methods of connecting I/O Device to the CPU-memory system of a computer and also various challenges in I/O organization arises due to the fact that there is a large variety of I/O devices which vary in their speeds and data formatting.

7. To learn various methods used to transfer data to/from I/O Devices to the main memory. They are program controlled data transfer, Interrupt Controlled data transfer and DMA based data transfer.

DAY 1 Memory and Input/Output Organization

Program Committee

Chief Patrons:

Dr. P. Krishna Rao
Chairman

Patrons:

Dr. P. Chiranjeevi Kumari
Secretary & Correspondent

Principal:

Dr. T. Srikanth

Convener:

Mr. M.S.R.S. Prasad, Assoc. Prof.
HoD, CSE

Coordinators:

Smt. S. Surya Sri, Asst. Prof.
Smt. R. V.V.G. Lakshmi, Asst. Prof.
Smt. S. Indira Spandana, Asst. Prof.
Mr. K.S. V.V. Prasad Rao, Asst. Prof.
Mr. N.D. Deepthi Priya, Asst. Prof.
Mr. M. Kiran Kumar, Asst. Prof.
Smt. M. Jayadevi, Asst. Prof.
Smt. S. Jyothirmayee, Asst. Prof.
Ms. P. Sai Sowjanya, Asst. Prof.



WORKSHOP ON COMPUTER ORGANIZATION

07th December 2018

Organized by
Department of CSE

About the College

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A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

Introduction to Computer Organization

Computer Organization refers to the operational units and their interconnections that realize the Architectural Specifications. Organizational Attributes include those hardware details transparent to the programmer, such as control signals, interfaces between the computer and peripherals, Types and Capacity of Memory, Register Structure and Instruction set. It Answers the Question "How Does a Computer Work?" basically from Programmer's Point of View. Thus with the knowledge of Computer Organization, a Computer Designer refines these Component Specifications and Implements them using Hardware, Software and Firmware elements.

About Workshop

The motivation behind this workshop is to give Hands on Learning Experience to the students in learning basic parameters in Designing Memory systems for the Computer system and to learn several Techniques that are used in Practical Architectures to enhance the I/O system Performance.



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Dr.M. Radhika Mani is giving a Lecture on Computer Organization.

Radhika
IQAC

T. S. R.
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Principal
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VIDYUTNAGAR, KAKINADA

programmers, as managers, etc. Therefore most of the issues in Software Engineering are concerned with interactions among people, rather than with the production of software.

About Workshop

The main motivation of the workshop is to give hands on experience to the faculty on software engineering and its data models. How to make decisions about systems and testing techniques.

Learning Goals

- An ability to apply knowledge of mathematics, science, and engineering.
- An ability to design and conduct experiments, as well as to analyze and interpret data.
- An ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability.
- An ability to function on multi-disciplinary teams.
- An ability to identify, formulate, and solve engineering problems.
- An understanding of professional and ethical responsibility.
- An ability to communicate effectively.
- The broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context.
- A recognition of the need for, and an ability to engage in, life-long learning.
- A knowledge of contemporary issues.
- An ability to use the techniques, skills, and modern engineering tools necessary for engineering practice.

DAY 1	Software engineering goals
	Objectives Models and uses of all system models
DAY 2	White box testing Black box testing
	Levels of testing and all other testing techniques.

Program Committee

Chief Patrons:

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Smt. S. Indira Spandana, Asst. Prof.
Mr. R.S.V.V.Prasad Rao Asst. Prof.
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Smt. M. Jayasree, Asst. Prof.
Smt. S.Jyothirmayee, Asst. Prof.
Mr. P.Sai Sowjanya, Asst. Prof.



WORKSHOP ON SOFTWARE ENGINEERING

11th Dec 2018 & 12th Dec 2018

Organized by
Department of CSE

About the College

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Introduction to Software Engineering

Software Engineering is the sub discipline of Computer Science that attempts to apply engineering principles to the creation, operation, modification and maintenance of the software components of various systems. As with much of Computer Science, the subject of Software Engineering is at an very early stage in its development. It is much more of an art than a science, and at present has little in common with classical engineering.

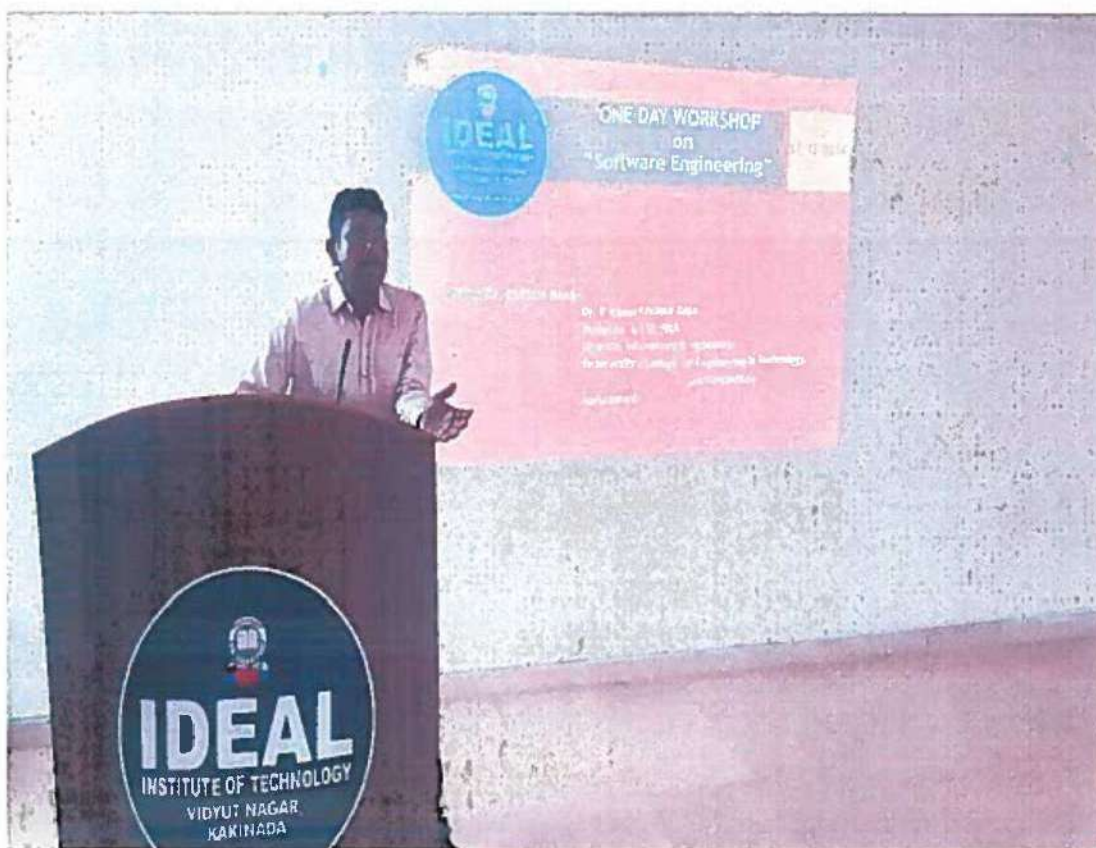
Software Engineering may well be concerned with the application of well-understood scientific methods to the construction, operation, modification and maintenance of software. Today, however, Software Engineering is concerned with finding ways in which to produce working software for predictable costs in predictable time. When the problems involved are very simple or when only one person is involved, implementing software to meet their own needs, there isn't much to be said, and we are a long way from having any scientific principles for the production of software. Therefore, the major focus of software engineering today is on well-tested heuristics for the production of software to solve complex problems when many people are



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Dr. P.Vamsi Krishna Raja is giving lecture on Software Engineering

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build and maintain homes to the utilities needed to power them, we take a lot from the earth in order to live day to day.

The use of energy-saving technologies in green buildings should run through the entire process of building design, construction and use, enabling green energy-saving technologies to maximize their effectiveness in construction. Realize the sustainable development of green building, reduce energy consumption, reduce people's interference with the natural environment, suitable for people living in "green" building.

While sustainable buildings are undeniably still at a nascent stage in the country, India ranks second, just after the US, in terms of the number of green technology projects and built-up area.

In the age of global warming and the resulting climate change problems, man-made intervention has caused a tremendous impact on the environment and the ecosystem, leading to a major resource crisis across the planet. Understandably, the construction industry is important to support the growing populations' living aspirations, but certainly not at the expense of over-exploiting Mother Nature.

It may be news to the uninformed that the construction industry, together with the materials industries that support it, is one of the major global consumers of natural resources, both physical and biological

Today Civil Engineering has extended its applications both in a diversified and integrated way. By adopting the basic concepts and latest technology

many of the complications in an engineering application are considerably reduced. One of such is sustainable green construction and assessment. The main aim of this Seminar is to share the Knowledge between the experts, Faculty members and Students.

DAY 1	Green Construction
-------	--------------------

Program Committee

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Ms. K Sumavalli. Prof.

Ms. R Gayathri Devi, Asst. Prof.

Ms. K Manjusha, Asst. Prof.

Ms. K Sulekya, Asst. Prof.



SUSTAINABLE GREEN CONSTRUCTION AND ASSESSMENT IN CIVIL ENGINEERING

12th December 2018

Organized by
Department of CIVIL

About the College

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ABOUT THE WORKSHOP:

Buildings across the globe are adopting sustainable operational and constructional strategies. A green building is aimed at minimizing the use of water, optimized energy efficiency, low waste generation, conservation of natural resources and thereby offering the occupants with a healthy environment to live and work in

As our planet is faced with more and more problems related to finite resource depletion, pollution, and climate change, we're forced to examine everything in our daily lives that makes an impact on the earth.

The buildings we live and work in are a huge part of this — both the most basic means of shelter for our survival as well as the source for our greatest consumption of resources. From the materials used to



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. A. Srinivas is giving a Lecture on Sustainable Green Construction and Assesment in Civil Engineering.

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transportation, industrial equipment, medicine, electronics, communications, and defense/space.

Following the discussion of applications is a section on the lessons for HTS that can be gleaned from nearly 80 years' experience with LTS. The seminar concludes with a discussion of the significance of a higher critical transition temperature (TC) in the context of the broader requirements that must be met by any viable commercial technology.

Superconductors exhibit unique features other than their ability to perfectly conduct current. For example, many expel magnetic fields during the transition to the superconducting state. This is due to the Meissner effect by which superconducting materials set up electric currents near their surface at T_c , therefore canceling the fields within the material itself. A stationary magnet on a superconductor demonstrates this effect: as the superconductor cools through its critical temperature, the expulsion of magnetic flux from the conductor causes the magnet to levitate above the material.

Below a certain "critical" temperature, materials undergo transition into the superconducting state, characterized by two basic properties: firstly, they offer no resistance to the passage of electrical current. When resistance falls to zero, a current can circulate inside the material without any dissipation of energy. Secondly, provided they are sufficiently weak, external magnetic fields will not penetrate the superconductor, but remain at its surface. This field expulsion phenomenon is known as the Meissner effect, after the physicist who first observed it in 1933.

The discovery of these "High Temperature Superconductors" sparked vast interest, and an entire industry dedicated to the research and commercial development of these materials and their applications has emerged. Today, an enormous range of devices are

under development for both low and high temperature superconductors.

International competition is strong in these materials, and current efforts involve many facets of the electronics, communications, power, medical technology, transportation, military, and materials processing industries.

DAY I	Super Conductivity and Engineering Applications
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Program Committee

Chief Patrons:

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Chairman

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Secretary & Correspondent

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Dr. T. Srikanth

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Mr. K. Venkateswara Rao, Asst. Prof.

Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.



WORKSHOP ON SUPER CONDUCTIVITY & ENGINEERING APPLICATIONS

14th December 2018

Organized by
Department of EEE

About the College

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Introduction to Super Conductivity and Engineering Applications

A short account is given of the characteristic properties of superconductors and the reasons for the importance of superconductivity in science and technology. The occurrence of the phenomenon in metals, alloys and chemical compounds is briefly discussed, with particular reference to those materials having the highest critical temperatures. The major part of the seminar is concerned with the applications of superconductivity in engineering, and attention is particularly devoted to materials for magnet construction, the various uses of superconducting magnets, and the other applications of superconductors such as power transmission cables, and transformers. Some forecasts are made regarding the probable developments in superconductivity in the next few years..

The purpose of this seminar is to assess the significance of high-temperature superconductors (HTS) and to forecast the timing of potential markets. Accordingly, it examines the major present and potential applications of superconductors in seven different sectors: high energy physics, electric power,



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. S. Srikanth is giving lecture on Super Conductivity and Engineering Applications

P. Reddy
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Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA.

ABOUT THE COLLEGE

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ABOUT CIVIL DEPARTMENT

The department is strengthened with one Doctorate, nine Post Graduates as Out of which two staff members have industrial experience in reputed organizations. Structural Design, Geotechnical analysis, Surveying and many other. Not only have the teaching staff, the technicians also had tremendous technical experience. The department is equipped with ISO certified laboratory equipment and sophisticated teaching aids like day light projector, e-Class room etc.

THEME OF THE LECTURE

This lecture will provide a platform for academicians, Researchers, Scientists and industry personnel from industry to come together and share the latest developments in broad areas of Total Station.

A total station (TS) or total station theodolite (TST) is an electronic/optical instrument used for surveying and building construction. It is an electronic transit theodolite integrated with electronic distance measurement (EDM) to measure both vertical and horizontal angles and the slope distance from the instrument to a particular point, and an on-board computer to collect data and perform triangulation calculations.

This eliminates the need for an assistant staff member as the operator holds the retro reflector and controls the total station from the observed point. These motorized total stations can also be used in automated setups known as Automated Motorized Total Station (AMTS). In this workshop hands on sessions also included for real time experience.

RESOURCE PERSON

I.Mr.V.V.SATYANARAYANAMURTHY.
Engineering Professionals,
Rajahmundry

TWO DAY WORKSHOP ON TOTAL STATION 5th & 6th DECEMBER 2019



Organized by

Department of CIVIL Engineering

IDEAL INSTITUTE OF TECHNOLOGY

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Affiliated to JNTUK, Kakinada,

&
Vidyut Nagar, Kakinada, E.G.Dt.,
Andhra Pradesh, India- 533 003
Ph:

Fax: 08852-252232

Website: www.idealtech.edu.in

Email: principal@idealtech.edu.in

**TWO DAY WORKSHOP ON
ON
TOTAL STATION
5th & 6th DECEMBER 2019
REGISTRATION FORM**

(Make copies if desired)

Name : _____

Date of Birth : _____

Designation : _____

Department : _____

Organization : _____

Address for Communication: _____

Phone : _____

E-mail : _____

DD No : _____

Date: _____

Signature of the
Applicant

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Principal & Professor, Dept. of ME

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Head, Dept. of CE

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Mr. Ch.Vinod Kumar
Mr. M.S.R.S. Prasad

Organizing Committee

Mr.S.Nazeer Ahmed
Mr.R.Kirankumar
Mr.Y.Sriram
Ms.B.Monica
Ms.M.Geeta sri

ATTENDEES

This seminar is intended for Academicians,
Researchers, Industry Personnel and PG
Students

REGISTRATION FEE: *NIL*

IMPORTANT DATES:

Submission of Application : 10-11-19
Intimation of Selection : 03-12-19
Confirmation by Participant : 04-12-19

MAILING ADDRESS

The Coordinator,
Department of CE
Vidyut Nagar, Kakinada, East Godavari District,
Andhra Pradesh, India-533003
principal@idealttech.edu.in

*For any further clarifications please feel free to
contact*

Ms.K. Sumavalli
Head of the Department
Department of CE
Coordinator - +91 7995539549

IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. D. Venkateshwarlu is giving a lecture on Total Station Theodolite

P. Radhika
IQAC

T-8
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VIDYUTNAGAR, KAKINADA

disturbances or faulty flow of power. Having understood these terminologies related to power system stability let us now look into the different types of stability.

The synchronous stability of a power system can be of several types depending upon the nature of disturbance, and for the purpose of successful analysis it can be classified into the following 3 types as shown below:

1. Steady state stability.
2. Transient stability.
3. Dynamic stability.

In recent years, power demand has increased substantially while the expansion of power generation and transmission has been severely limited due to limited resources and environmental restrictions. As a consequence, some transmission lines are heavily loaded and the system stability becomes a power transfer-limiting factor. Flexible AC transmission systems (FACTS) controllers have been mainly used for solving various power system steady state control problems. However, recent studies reveal that FACTS controllers could be employed to enhance power system stability in addition to their main function of power flow control.

The seminar explains about an increasing interest in this subject for the last two decades, where the enhancement of system stability using FACTS controllers has been extensively investigated. This seminar presents a comprehensive review on the research and developments in the power system stability enhancement using FACTS damping controllers. Several technical issues related to FACTS installations have been

highlighted and performance comparison of different FACTS controllers has been discussed. In addition, some of the utility experience, real-world installations, and semiconductor technology development will be reviewed and summarized. Applications of FACTS to other power system studies will be discussed.

DAY 1	Power Systems Stability & FACTS to improve it
-------	---

Program Committee

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Dr. P. Krishna Rao
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Patrons:

Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

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Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.



WORKSHOP ON POWER SYSTEMS STABILITY & FACTS TO IMPROVE IT

06th January 2019

Organized by
Department of EEE

About the College

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Introduction to Power Systems Stability & FACTS to improve it

Power system stability is the ability of an electric power system, for a given initial operating condition, to regain a state of operating equilibrium after being subjected to a physical disturbance, with most system variables bounded so that practically the entire system remains intact.

For stable operation it is important for the bus to be well synchronized with the generators over the entire duration of transmission, and for this reason the power system stability is also referred to as synchronous stability and is defined as the ability of the system to return to synchronism after having undergone some disturbance due to switching on and off of load or due to line transience.

To understand stability well another factor that is to be taken into consideration is the stability limit of the system. The stability limit defines the maximum power permissible to flow through a particular point or a part of the system during which it is subjected to line

Analog Circuits

A circuit diagram representing an analog circuit, in this case a simple amplifier

Analog electronic circuits are those in which current or voltage may vary continuously with time to correspond to the information being represented. Analog circuitry is constructed from two fundamental building blocks: series and parallel circuits.

In a series circuit, the same current passes through a series of components. A string of Christmas lights is a good example of a series circuit: if one goes out, they all do.

In a parallel circuit, all the components are connected to the same voltage, and the current divides between the various components according to their resistance.

Digital Circuits

In digital electronic circuits, electric signals take on discrete values, to represent logical and numeric values. These values represent the information that is being processed. In the vast majority of cases, binary encoding is used: one voltage (typically the more positive value) represents a binary '1' and another voltage (usually a value near the ground potential, 0 V) represents a binary '0'. Digital circuits make extensive use of transistors, interconnected to create logic gates that provide the functions of Boolean logic: AND, NAND, OR, NOR, XOR and combinations thereof. Transistors interconnected so as to provide positive feedback are used as latches and flip flops, circuits that have two or more metastable states, and remain in one of these states until changed by an external input. Digital circuits therefore can provide logic and memory, enabling them to perform arbitrary computational functions. (Memory based on flip-flops is known as static random-access memory (SRAM). Memory based on the storage of

charge in a capacitor, dynamic random-access memory (DRAM) is also widely used.)

The design process for digital circuits is fundamentally different from the process for analog circuits. Each logic gate regenerates the binary signal, so the designer need not account for distortion, gain control, offset voltages, and other concerns faced in an analog design. As a consequence, extremely complex digital circuits, with billions of logic elements integrated on a single silicon chip, can be fabricated at low cost.

DAY 1	Analog Circuits
DAY 2	Digital Circuits

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WORKSHOP ON ELECTRONIC CIRCUIT ANALYSIS

07th & 08th January 2019

Organized by
Department of ECE

About the College

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Introduction to Electronic Circuit Analysis

An electronic circuit is composed of individual electronic components, such as resistors, transistors, capacitors, inductors and diodes, connected by conductive wires or traces through which electric current can flow. To be referred to as electronic, rather than electrical, generally at least one active component must be present. The combination of components and wires allows various simple and complex operations to be performed: signals can be amplified, computations can be performed, and data can be moved from one place to another.

Circuits can be constructed of discrete components connected by individual pieces of wire, but today it is much more common to create interconnections by photolithographic techniques on a laminated substrate (a printed circuit board or PCB) and solder the components to these interconnections to create a finished circuit. In an integrated circuit or IC, the components and interconnections are formed on the same substrate, typically a semiconductor such as silicon or (less commonly) gallium arsenide.

About Workshop

The main motivation of the workshop is to give hands on experience to the students on distributed systems and its concepts. What is a distributed system? It's one of those things that's hard to define without first defining many other things. Here is a "cascading" definition of a distributed system

Learning Goals

To be truly reliable, a distributed system must have the following characteristics:

- **Fault-Tolerant:** It can recover from component failures without performing incorrect actions.
- **Highly Available:** It can restore operations, permitting it to resume providing services even when some components have failed.
- **Recoverable:** Failed components can restart themselves and rejoin the system, after the cause of failure has been repaired.
- **Consistent:** The system can coordinate actions by multiple components often in the presence of concurrency and failure. This underlies the ability of a distributed system to act like a non-distributed system.
- **Scalable:** It can operate correctly even as some aspect of the system is scaled to a larger size. For example, we might increase the size of the network on which the system is running. This increases the frequency of network outages and could degrade a "non-scalable" system. Similarly, we might increase the number of users or servers, or overall load on the system. In a scalable system, this should not have a significant effect.
- **Predictable Performance:** The ability to provide desired responsiveness in a timely manner.
- **Secure:** The system authenticates access to data and services

DAY 1	Concepts of distributed systems Layers of systems and introduction of all the systems
DAY 2	Operating systems Java concepts and techniques.

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WORKSHOP ON DISTRIBUTED SYSTEMS

24th Jan 2019 & 25th Jan 2019

**Organized by
Department of CSE**

About the College

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Introduction to Distributed Systems

A distributed system is an application that executes a collection of protocols to coordinate the actions of multiple processes on a network, such that all components cooperate together to perform a single or small set of related tasks.

Why build a distributed system? There are lots of advantages including the ability to connect remote users with remote resources in an open and scalable way. When we say *open*, we mean each component is continually open to interaction with other components. When we say *scalable*, we mean the system can easily be altered to accommodate changes in the number of users, resources and computing entities.

Thus, a distributed system can be much larger and more powerful given the combined capabilities of the distributed components, than combinations of stand-alone systems. But it's not easy - for a distributed system to be useful, it must be reliable. This is a difficult goal to achieve because of the complexity of the interactions between simultaneously running components.



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. P. Vamsi Krishna Raj is giving lecture on Distributed Systems

P. Vamsi Krishna Raj
IQAC

T. S. R.
PRINCIPAL
Principal

IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

Components of Data Communication

1. **Message :** It is the information to be delivered.
2. **Sender :** Sender is the person who is sending the message.
3. **Receiver :** Receiver is the person to whom the message is to be delivered.
4. **Medium :** It is the medium through which message is to be sent for example modem.
5. **Protocol :** These are some set of rules which govern data communication.

About Workshop

The main motivation of the workshop is to give hands on experience to the students on computer networks. It is useful computer network security projects and also for simplest routing algorithms.

Learning Goals

The main goal of networking is "Resource sharing", and it is to make all programs, data and equipment available to anyone on the network without the regard to the physical location of the resource and the user.

A second goal is to provide high reliability by having alternative sources of supply. For example, all files could be replicated on two or three machines, so if one of them is unavailable, the other copies could be available.

Another closely related goal is to increase the systems performance as the work load increases by just adding more processors. With central mainframes, when the system is full, it must be replaced by a larger one, usually at great expense and with even greater disruption to the users.

Computer networks provide a powerful communication medium. A file that was updated or modified on a network can be seen by the other users on the network immediately.

DAY 1	CSMA and related problems
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Ms. P.Sai Sowjanya, Asst. Prof.



WORKSHOP ON COMPUTER NETWORKS

22nd January 2019

**Organized by
Department of CSE**

About the College

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Introduction to Computer Networks

Today the world scenario is changing. Data Communication and network have changed the way business and other daily affair works. Now, they rely on computer networks and internetwork. A set of devices often mentioned as nodes connected by media link is called a Network. A node can be a device which is capable of sending or receiving data generated by other nodes on the network like a computer, printer etc. These links connecting the devices are called Communication channels.

Computer network is a telecommunication channel through which we can share our data. It is also called data network. The best example of computer network is Internet. Computer network does not mean a system with control unit and other systems as its slave. It is called a distributed system.

A network must be able to meet certain criteria, these are mentioned below:

1. Performance
2. Reliability
3. Scalability



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr.M. Radhika Mani is giving a Lecture on Computer Networks.

P. Redhika
IQAC

T. S. R.
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VIDYUT NAGAR, KAKINADA

changes in physical phenomena, such as sound, light, temperature, position, or pressure. The physical variable is converted to an analog signal by a transducer. For example, in sound recording, fluctuations in air pressure (that is to say, sound) strike the diaphragm of a microphone which induces corresponding fluctuations in the current produced by a coil in an electromagnetic microphone, or the voltage produced by a condenser microphone. The voltage or the current is said to be an "analog" of the sound.

An analog signal has a theoretically infinite resolution. In practice an analog signal is subject to electronic noise and distortion introduced by communication channels and signal processing operations, which can progressively degrade the signal-to-noise ratio (SNR). In contrast, digital signals have a finite resolution. In analog systems, it is difficult to detect when such degradation occurs. However, in digital systems, degradation can not only be detected but corrected as well.

Pulse-amplitude modulation (PAM), is a form of signal modulation where the message information is encoded in the amplitude of a series of signal pulse. It is an analog pulse modulation scheme in which the amplitudes of a train of carrier pulses are varied according to the sample value of the message signal. Demodulation is performed by detecting the amplitude level of the carrier at every single period.

There are two types of pulse amplitude modulation:

1. Single polarity PAM: In this a suitable fixed DC bias is added to the signal to ensure that all the pulses are positive.
2. Double polarity PAM: In this the pulses are both positive and negative.

Pulse-amplitude modulation is widely used in modulating signal transmission of digital data, with non-

baseband applications having been largely replaced by pulse code modulation, and, more recently, by pulse-position modulation. In particular, all telephone modems faster than 300 bit/s use quadrature amplitude modulation (QAM). (QAM uses a two-dimensional constellation). The number of possible pulse amplitudes in analog PAM is theoretically infinite. Digital PAM reduces the number of pulse amplitudes to some power of two. For example, in 4-level PAM there are possible discrete pulse amplitudes; in 8-level PAM there are possible discrete pulse amplitudes; and in 16-level PAM there are possible discrete pulse amplitudes.

DAY 1 Pulse Amplitude Modulation

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WORKSHOP ON ANALOG COMMUNICATION

18th February 2019

Organized by
Department of ECE

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Introduction to Analog Communication

An analog signal is any continuous signal for which the time varying feature (variable) of the signal is a representation of some other time varying quantity, i.e., *analogous* to another time varying signal. For example, in an analog audio signal, the instantaneous voltage of the signal varies continuously with the pressure of the sound waves. It differs from a digital signal, in which the continuous quantity is a representation of a sequence of discrete values which can only take on one of a finite number of values. The term analog signal usually refers to electrical signals; however, mechanical, pneumatic, hydraulic, human speech, and other systems may also convey or be considered analog signals.

An analog signal uses some property of the medium to convey the signal's information. For example, an aneroid barometer uses rotary position as the signal to convey pressure information. In an electrical signal, the voltage, current, or frequency of the signal may be varied to represent the information.

Any information may be conveyed by an analog signal, often such a signal is a measured response to



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. G. Rama Krishna is giving a Lecture on Analog Communication.

P. Krishna
IQAC

T. S.
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VIDYUTNAGAR, KAKINADA

you may develop an understanding that switching of power circuits is not only a logical operation, but also energy conversion. Every commutation in a power circuit will be accompanied with complex voltage and current transients.

1) Spurred on by technological progress and a growing concern about the efficient use of depleting energy resources, static power electronic converters employing advanced semi-conductor devices are currently used in an increasing number of applications. Such applications range from low power converters for miliwatt applications, to converters for high-voltage dc transmission systems, handling hundreds of mega-watts.

2) Other classical applications include adjustable-speed ac and dc drives for use in industry, switch-mode power supplies, audio amplifiers, uninterruptible power supplies and welding machines. In the last decade, power electronics and electrical drives have deeply penetrated the renewable energy sector.

3) Today, power electronic converters provide vital functionality in a diversity of new technologies, including large wind turbine systems, fuel cells systems and photovoltaic power generation. Further, motor drives comprising power electronic converters and advanced electric machines play a key role in the transportation sector, where new technologies are exploited in order to electrify many kinds of vehicle.

4) This specialisation combines state-of-the-art technologies with conventional technologies, for example power semiconductor devices, electronics, electromagnetics, digital signal processors, control theory, EMC and energy technology.

5) The themes for the study in Power Electronics and Drives provide you with in-depth understanding of the technologies and scientific disciplines involved in electric energy conversion by means of power electronic converters and electric machines.

DAY 1	Power Electronics & Drives
-------	----------------------------

Program Committee

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Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.



WORKSHOP ON POWER ELECTRONICS & DRIVES

11th March 2019

**Organized by
Department of EEE**

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Introduction to Power Electronics & Drives

Power electronics and electric drives are two fields of technology, very closely connected. Power electronics devices are the most advanced and effective switches of high power electric circuits. Frequently, they are used as switches for the commutation of windings of electric machines. The system which includes an electric machine, a power converter, based on semiconductor switches, and several control devices is known as an electric drive. Today about 60 % of the electric energy produced will be converted and used by electric drives of technological machines.

The flexible control of speed and torque of electric machines is possible due to modern microprocessor control and advanced power electronics devices. Optimal motion control of technological machines is a very urgent problem, the solution which will lead to higher productivity of machines and essential energy saving.

This seminar is intended for students of universities of applied sciences and remote engineering colleges who study the courses of electronics. It is also useful for

2019-20

ABOUT THE COLLEGE

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ABOUT CIVIL DEPARTMENT

The department is strengthened with one Doctorate, nine Post Graduates as Out of which two staff members have industrial experience in reputed organizations. Structural Design, Geotechnical analysis, Surveying and many other. Not only have the teaching staff, the technicians also had tremendous technical experience. The department is equipped with ISO certified laboratory equipment and sophisticated teaching aids like day light projector, e-Class room etc.

THEME OF THE LECTURE

This lecture will provide a platform for academicians, Researchers, Scientists and industry personnel from industry to come together and share the latest developments in broad areas of Total Station.

The role of a Quantity Surveyor. Known in the industry as a Construction Cost Consultant or Commercial Manager, their role is to keep a close eye on project finances and contractual relationships. They make sure that the financial position of construction projects is accurately reported and controlled effectively.

Conducting feasibility studies to estimate materials, time and labour costs. Preparing, negotiating and analysing costs for tenders and contracts. In this workshop hands on sessions also included for real time experience.

RESOURCE PERSON

1. Mr. V.V. SATYANARAYANAMURTHY.
Engineering Professionals,
Rajahmundry

TWO DAY WORKSHOP ON QUANTITY SURVEYING & CONSTRUCTION MANAGEMENT 5th & 6th JULY 2019



Organized by

Department of CIVIL Engineering

IDEAL INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi;
Affiliated to JNTUK, Kakinada,

&

Vidyut Nagar, Kakinada, E.O.Dt.,
Andhra Pradesh, India- 533 003
Ph:

Fax: 08852-252232

Website: www.idealtech.edu.in

Email: principal@idealtech.edu.in

TWO DAY WORKSHOP ON
ON
QUANTITY SURVEYING &
CONSTRUCTION MANAGEMENT

5th & 6th JULY 2019

REGISTRATION FORM

(Make copies if desired)

Name : _____

Date of Birth : _____

Designation : _____

Department : _____

Organization : _____

Address for Communication: _____

Phone : _____

E-mail : _____

DD No : _____

Date: _____

Signature of the
Applicant

ORGANISING COMMITTEE

Chief Patron

Dr. Paruchuri Krishna Rao
Chairman

Patrons

Dr.P. Chirajeevini Kumari
Secretary and Correspondent
Sri P.Kiran
Treasurer

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Dr. T. Srikanth
Principal & Professor, Dept. of ME

Co-Coordinator

Mr.M.Venkat Rao
Head, Dept. of CE

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Dr.Ch.V.V.Satyanarayana Murthy
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Mr. Bh.Ayyappa Kumar
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Mr.S.Nazeer Ahmed
Mr.R.Kirankumar
Ms.K.Sumavalli
Mr.Y.Sriram
Ms.B.Monica
Ms.M.Geeta sri

ATTENDEES

This seminar is intended for Academicians,
Researchers, Industry Personnel and PG
Students

REGISTRATION FEE: NIL

IMPORTANT DATES:

Submission of Application : 10-06-19
Intimation of Selection : 03-06-19
Confirmation by Participant : 04-06-19

MAILING ADDRESS

The Coordinator,
Department of CE
Vidyut Nagar, Kakinada, East Godavari District,
Andhra Pradesh, India-533003
principal@idealttech.edu.in

*For any further clarifications please feel free to
contact*

Mr.M.Venkat Rao
Head of the Department
Department of CE
Coordinator - +91 7995539549



IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. M .Anjan kumar is giving lecture on Quantity Surveying and Construction Management

flask
IQAC

T. S. S.

PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

Learning Goals

Current Trends in AI:

Artificial Intelligence has made substantial progress over the past years. The GMAIL, E-mail response promoters, Alexa in your rooms, the transcription of your voicemails, automatic voice calls for your appointments, self driving cars etc...

MORE TRENDS:

Reinforcement Learning:

This is different from supervised and unsupervised learning techniques. It is a framework that was experience driven sequential decision making. Most gameplaying algorithms used reinforcement learning.

Ethics in AI

This involves ensuring ethical design of AI applications which will consider

Legal Accountability

Transparency

Policies

Governance Frameworks

Embedded values into AI Applications

Quantum Computing

It uses quantum physics to compute calculation faster than any supercomputers today, unlike regular computers, quantum computers use qubits to solve information.

AI Power Chips

Major chip manufactures including INTEL, NVIDEA, AMD and ARM aim to produce AI Powered chips to speed up the application that run on AI

Privacy & Policy

It is important to protect our privacy and ensure the organisations approach data privacy. Facebook uses over privacy policies that have been witnessed the previous years are just the tip of iceberg.

DAY 1	Recent Trends in Artificial Intelligence
-------	--

Program Committee

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Chairman

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Secretary & Correspondent

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Smt. R V V G Lakshmi, Asst. Prof.,
Smt. M Jayasree, Asst. Prof.,
Mr. M Kiran Kumar, Asst. Prof.,
Ms. R Sridivya, Asst. Prof.,



WORKSHOP ON ARTIFICIAL INTELLIGENCE

06th July 2019

Organized by
Department of CSE

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The objectives of AI research are reasoning, knowledge representation, planning, learning, natural language processing, realization, and ability to move and manipulate objects. There are long-term goals in the general intelligence sector.

About Workshop

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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. P. Vamsi Krishna Raja is giving lecture on Artificial Intelligence

P. Reddy K
IQAC

1-8
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
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Smt. D. Vijaya Lakshmi, Asst. Prof.,
Mr. Ch.V Prakash, Asst. Prof.,
Mr. V Siva Krishnan, Asst. Prof.,
Mr. P.S.J.Sriram, Asst. Prof.,



WORKSHOP ON ARTIFICIAL INTELLIGENCE

06th July 2019

Organized by
Department of ECE

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Dr.M. Siva kumar is giving lecture on Artificial Intelligence

P. Ladhika
IQAC

T. S. S.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

To get aware about the use and further scope of MATLAB.

TOPICS TO BE DISCUSSED

A brief introduction about MATLAB.

What is MATLAB?

Data types

Matrix, string, cell and structure

Creating, accessing elements and manipulating of data of different types.

In MATLAB Workshop all students will learn to:

Gain the knowledge of MATLAB tool and needed to successfully solving numerical computation, visualization and programming.

Participants was developed a basic understanding of MATLAB.

Outlines of the workshop:

DAY (Saturday):

20th July 2019, 10.00 A.M TO 4.00P.M

A brief introduction about MATLAB tool.

Basis syntax

Variable

Data type

Operator

Function

Loops

Commands

Feedback from Students: Students experienced a special hands on practice learning session through which they got a better understanding of MATLAB. Students understood the importance of MATLAB. The content

delivery was as per the needs of every student along with the huge interaction..

Workshop Output: Every student developed some programs using MATLAB and feels more comfortable in MATLAB tool.

DAY I	Fundamental concepts & use and further scope of MATLAB.
-------	---

Program Committee

Chief Patrons:

Dr. P. Krishna Rao

Chairman

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Dr. P. Chiranjeevini Kumari

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Dr. T. Srikanth

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Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.,



WORKSHOP ON MATLAB

20th July 2019

Organized by
Department of EEE

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ABOUT THE WORKSHOP

MATLAB (matrix laboratory) is a multi-paradigm numerical computing environment. A proprietary programming language developed by MathWorks, MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, C#, Java, Fortran and Python. Although MATLAB is intended primarily for numerical computing, an optional toolbox uses the MuPAD symbolic engine, allowing access to symbolic computing abilities. As of 2017, MATLAB has over 2 million users across industry and academia.

OBJECTIVE OF THE WORKSHOP

The objective of this lab is to introduce you to the basic. Practice each new command by completing the examples and exercises. Turn-in the answers for all the exercises problems as per given. When answering the problems, indicate the commands you entered and the output displayed.

This workshop introduces fundamental concepts of MATLAB.

IDEAL INSTITUTE OF TECHNOLOGY

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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. D. Ravi Kishore is giving a lecture on MATLAB

P. Radhika
IQAC


PRINCIPAL
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IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

Learning Goals

1. To Learn that Memory System for computers uses a hierarchy of Memories appropriately organized to Provide applications cost effective overall memory with high capacity and fast access.
2. To Identify that RAM uses DRAMs which are low cost, high capacity solid state memory. Their Access time is around 50ns. To get a Faster Access it is combined with a small SRAM memory called cache memory. Cache is smaller but faster than DRAM main memory. The Management of main memory is critical to the computer system.
3. To Analyze how Programs usually follow a pattern of Memory access in which Instructions stored in a small range of addresses are frequently referenced. Instruction accesses and Data Accesses are clustered in two distinct range of addresses. This is called Locality of Reference. This fact is used to keep frequently referenced instructions and data in small fast memories called caches. Normally there is a Data cache and an Instruction Cache.
4. To Learn a commonly used method in the design of memory systems called Virtual Memory. A virtual memory provides a user with a large logical memory address space to write programs even though the available physical memory (DRAM) may be small. This is achieved by Combining DRAM and a magnetic memory in such a way that the memory cycle time of the combined system is closer to DRAM cycle time while a large storage space provided by a magnetic disk.
5. To learn how computers use both cache and virtual memory effectively for memory management. Hardware and Operating System cooperate to achieve this.

6. To learn Various methods of connecting I/O Devices to the CPU-memory system of a computer and also various challenges in I/O organization arises due to the fact that there is a large variety of I/O devices which vary in their speeds and data formatting.

7. To learn various methods used to transfer data/ from I/O Devices to the main memory. They are program controlled data transfer, Interrupt Controlled data transfer and DMA based data transfer.

DAY 1

Memory and Input/Output Organization

Program Committee

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Dr. P. Krishna Rao

Chairman

Patrons:

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Smt. R V V G Lakshmi, Asst. Prof.,
Smt. M Jayasree, Asst. Prof.,
Mr. M Kiran Kumar, Asst. Prof.,
Ms. R Sridivya, Asst. Prof.,



WORKSHOP ON COMPUTER ORGANIZATION

01st August 2019

Organized by
Department of CSE

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A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

Introduction to Computer Organization

Computer Organization refers to the operational units and their Interconnections that realize the Architectural Specifications. Organizational Attributes include those hardware details transparent to the programmer, such as control signals, Interfaces between the computer and peripherals, Types and Capacity of Memory, Register Structure and Instruction set. It Answers the Question "How Does a Computer Work?" basically from Programmer's Point of View. Thus with the knowledge of Computer Organization, a Computer Designer refines these Component Specifications and Implements them using Hardware, Software and Firmware elements.

About Workshop

The motivation behind this workshop is to give Hands on Learning Experience to the students in learning basic parameters in Designing Memory systems for the Computer system and to learn several Techniques that are used in Practical Architectures to enhance the I/O system Performance.



IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. M. Radhika Mani is giving lecture on Computer Organization.

Radhika
IQAC

T. S. R.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

Learning Goals

1. To Learn that Memory System for computers uses a hierarchy of Memories appropriately organized to Provide applications cost effective overall memory with high capacity and fast access.
2. To Identify that RAM uses DRAMs which are low cost, high capacity solid state memory. Their Access time is around 50ns. To get a Faster Access it is combined with a small SRAM memory called cache memory. Cache is smaller but faster than DRAM main memory. The Management of main memory is critical to the computer system.
3. To Analyze how Programs usually follow a pattern of Memory access in which Instructions stored in a small range of addresses are frequently referenced. Instruction accesses and Data Accesses are clustered in two distinct range of addresses. This is called Locality of Reference. This fact is used to keep frequently referenced instructions and data in small fast memories called caches. Normally there is a Data cache and an Instruction Cache.
4. To Learn a commonly used method in the design of memory systems called Virtual Memory. A virtual memory provides a user with a large logical memory address space to write programs even though the available physical memory (DRAM) may be small. This is achieved by Combining DRAM and a magnetic memory in such a way that the memory cycle time of the combined system is closer to DRAM cycle time while a large storage space provided by a magnetic disk.
5. To learn how computers use both cache and virtual memory effectively for memory management. Hardware and Operating System cooperate to achieve this.

6. To learn Various methods of connecting I/O Devices to the CPU-memory system of a computer and also various challenges in I/O organization arises due to the fact that there is a large variety of I/O devices which vary in their speeds and data formatting.

7. To learn various methods used to transfer data to/from I/O Devices to the main memory. They are program controlled data transfer, Interrupt Controlled data transfer and DMA based data transfer.

DAY 1	Memory and Input/Output Organization
-------	--------------------------------------

Program Committee

Chief Patrons:

Dr. P. Krishna Rao
Chairman

Patrons:

Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

Principal:

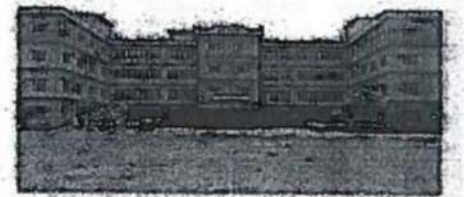
Dr. T. Srikanth

Convener:

Mr. Bh. V.S.R.K. Ayyappa Kumar,
Assoc. Prof., HoD, ECE

Coordinators:

Mr. B.V.Kalyan Ram, Asst. Prof.,
Smt. D. Vijaya Lakshmi, Asst. Prof.,
Mr. Ch.V Prakash, Asst. Prof.,
Mr. V Siva Krishnan, Asst. Prof.,
Mr. P.S.J.Sriram, Asst. Prof.,



WORKSHOP ON COMPUTER ORGANIZATION

01st August 2019

Organized by
Department of ECE

About the College

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IQAC

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Principal
IDEAL INSTITUTE OF TECHNOLOGY
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A real-time monitoring system that provides accurate and extensive measurements is a prerequisite to advanced operation and control systems for tackling the new challenges in power systems. Especially, the use of PMUs, providing synchronized phasor measurements in real-time, is considered as key for state estimation, stability analysis, wide-area monitoring and control for future power systems.

OBJECTIVE OF THE WORKSHOP

The objective of the Workshop is to do real-time experiments on power systems. These experiments will make the participants to understand the basic concepts of the power systems towards its operation and control. It will also help them to handle real-time projects on power system instead of restricting themselves to simulation. By the end of the programme, they will be well versed in handling the real time experiments and would build their intellectuality under research in bringing up projects. Through this workshop, the engineers of any discipline can understand the real power system concept and will be motivated to contribute.

TOPICS TO BE DISCUSSED

Performance Analysis of transmission line

- Ferranti Effect
- Surge Impedance Loading
- Voltage Control – Shunt Compensation
- Voltage Control – Series Compensation
- Real Power Control – Passive Series Compensation
- Real Power Control – Active Series Compensation

- Enhancement of Power System Stability
- Power Flow Analysis on IEEE 5 Bus System
- Data Acquisition in Power Systems
- Development of Numerical Relay.

DAY 1	Real time operation and control of power systems
-------	--

Program Committee

Chief Patrons:

Dr. P. Krishna Rao
Chairman

Patrons:

Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

Principal:

Dr. T. Srikanth

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Mr. Ch. Vinod Kumar, Assoc. Prof.,
HoD, EEE

Coordinators:

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Mr. G. Ravi Kumar, Asst. Prof.

Mr. D. Madhu, Asst. Prof.

Mr. M. Jagadeesh, Asst. Prof.

Mr. A. Ramesh, Asst. Prof.,



WORKSHOP ON REAL TIME OPERATION AND CONTROL OF POWER SYSTEMS

17th August 2019

**Organized by
Department of EEE**

About the College

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ABOUT THE WORKSHOP

With the aim at further advancing power system monitoring applications based on synchrophasor measurements and transitioning from ideas to innovation in this field, we have built a real-time simulation platform which incorporates real measurement devices, especially phasor measurement units (PMUs). This set up can validate the performance of the measurement devices and monitoring applications in a near in-field condition.



IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Dr. S. Srikanth is giving lecture on Real Time Operations Control of Power Systems

f. Redhik
IQAC

T. S. R.
PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

ABOUT THE COLLEGE

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ABOUT MECHANICAL DEPARTMENT

The department is strengthened with one Doctorate, nine Post Graduates as Out of which two staff members have industrial experience in reputed organizations. Machine Design, Piping & Inspection. Not only have the teaching staff, the technicians also had tremendous technical experience. The department is equipped with ISO certified laboratory equipment and sophisticated teaching aids like day light projector, e-Class room etc.

THEME OF THE LECTURE

This lecture will provide a platform for academicians, Researchers, Scientists and industry personnel from industry to come together and share the latest developments in broad areas of Non Destructive Testing

NDT is a testing and analysis technique used by industry to evaluate the properties of a material, component, structure or system for characteristic differences or welding defects and discontinuities without causing damage to the original part. This is a passive NDT technique, which relies on detecting the short bursts of ultrasound emitted by active cracks under a load. Sensors dispersed over the surface the structure detect the AE. It is even possible to detect AE from plasticization in highly stressed areas before a crack forms. In this workshop hands on sessions also included for real time experience.

RESOURCE PERSON

1. Sri T Meher Pretham ,CEO
UIDIL NDT,
Vijayawada

ONE DAY WORKSHOP ON NON DESTRUCTIVE TESTING 3rd January 2020



Organized by

Department of Mechanical Engineering

IDEAL INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi;
Affiliated to JNTUK, Kakinada,

&

Vidyut Nagar, Kakinada, E.G.DL,
Andhra Pradesh, India- 533 003
Ph:

Fax: 08852-252232

Website: www.idealtech.edu.in

Email: principal@idealtech.edu.in

ONE DAY WORKSHOP ON
ON
NON DESRTUCTIVE TESTING
3rd January 2020

REGISTRATION FORM

(Make copies if desired)

Name : _____

Date of Birth : _____

Designation : _____

Department : _____

Organization : _____

Address for Communication:

Phone : _____

E-mail : _____

DD No : _____

Date: _____

Signature of the
Applicant

ORGANISING COMMITTEE

Chief Patron

Dr. Paruchuri Krishna Rao
Chairman

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Dr.P. Chirajeevini Kumari
Secretary and Correspondent
Sri P.Kiran
Treasurer

Coordinator

Dr. T. Srikanth
Principal & Professor, Dept. of ME

Co-Coordinator

Mrs.K.Aruna
Head, Dept. of ME

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Dr.Ch.V.V.Satyanarayana Murthy
Dr. Srihari Varma
Mr. Bh.Ayyappa Kumar
Mr. Ch.Vinod Kumar
Mr. M.S.R.S. Prasad

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Mr.B.Dharma Kumar
Mrs.J L Prathyusha
Mr.K.S.Rambabu
Mr.Ch.Raju
P.Durga prasad
Mr.N.Krishnakanth

ATTENDEES

This seminar is intended for Academicians,
Researchers, Industry Personnel and PG
Students

REGISTRATION FEE: *NIL*

IMPORTANT DATES:

Submission of Application : 31-12-19
Intimation of Selection : 01-01-20
Confirmation by Participant : 02-01-20

MAILING ADDRESS

The Coordinator,
Department of ME
Vidyut Nagar, Kakinada, East Godavari District,
Andhra Pradesh, India-533003
principal@idealtech.edu.in

*For any further clarifications please feel free to
contact*

Mrs.K. Aruna
Head of the Department
Department of ME
Coordinator - +91 9949305868

[4] decompose in alkaline condition of concrete and various additives and surface treatment of concrete. The High Speed 1 tunnel linings incorporated concrete containing 1 kg/m³ or more of polypropylene fibers, of diameter 18 & 32 μ m, giving the benefits noted below.[5] Adding fine diameter polypropylene fibers, not only provides reinforcement in tunnel lining, but also prevents "spalling" and damage of lining in case of fire due to accident.

Different Types of Fiber Reinforced Concrete

1. Steel Fiber Reinforced Concrete.
2. Polypropylene Fiber Reinforced (PFR) cement mortar & concrete.
3. GFRG Glass Fiber Reinforced Concrete.
4. Asbestos Fibers.
5. Carbon Fibers.
6. Organic Fibers.

Properties of RCC

1. Fibers reinforced concrete may be useful where high tensile strength and reduced cracking are desirable or when conventional reinforcement cannot be placed.
2. It improves the impact strength of concrete, limits the crack growth and leads to a greater strain capacity of the composite material.
3. For industrial projects, macro-synthetic fibers are used to improve concrete's durability. Made from synthetic materials, these fibers are long and thick in size and may be used as a replacement for bar or fabric reinforcement.
4. Adding fibers to the concrete will improve its freeze-thaw resistance and help keep the concrete strong and attractive for extended periods.
5. Improve mix cohesion, improving pumpability over long distances.
6. Increase resistance to plastic shrinkage during curing.
7. Minimizes steel reinforcement requirements.
8. Reduces segregation and bleed-water.

DAY 1	Durability of Fiber reinforced Concrete Structures
DAY 2	Design methods for fiber Reinforced concrete

Program Committee

Chief Patrons:

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Chairman

Patrons:

Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

Principal:

Dr. T. Srikanth

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HoD, CIVIL

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Kum B. Naga Tulasi, Asst. Prof.
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Kum K. Sumavalli Kalahsati, Asst. Prof.
Sri B. leela Krishna, Asst. Prof.
Sri M. Venkata Rao, Asst. Prof.
Kum B. Mounica, Asst. Prof.
Sri Y. Srinam, Asst. Prof.
Kum K. Sulekha, Asst. Prof.



TWO DAY WORKSHOP ON " FIBRE REINFORCED CONCRETE"

7th Jan 2020 to 8th Jan 2020

**Organized by
Department of CIVIL**

About the College

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Introduction to Design and Drawing of Reinforced Concrete Structures

The concept of using fibers as reinforcement is not new. Fibers have been used as reinforcement since ancient times. Historically, horsehair was used in mortar and straw in mudbricks. In the 1900s, asbestos fibers

were used in concrete. In the 1950s, the concept of composite materials came into being and fiber-reinforced concrete was one of the topics of interest. Once the health risks associated with asbestos were discovered, there was a need to find a replacement for the substance in concrete and other building materials. By the 1960s, steel, glass (GFRC), and synthetic (such as polypropylene) fibers were used in concrete. Research into new fiber-reinforced concretes continues today. [Fibers are usually used in concrete to control cracking due to plastic shrinkage and to drying shrinkage. They also reduce the permeability of concrete and thus reduce bleeding of water. Some types of fibers produce greater impact, abrasion, and shatter resistance in concrete. Larger steel or synthetic fibers can replace rebar or steel completely in certain situations. Fiber reinforced concrete has all but completely replaced bar in underground construction industry such as tunnel segments where almost all tunnel linings are fiber reinforced in lieu of using rebar. Indeed, some fibers actually reduce the compressive strength of concrete. The amount of fibers added to a concrete mix is expressed as a percentage of the total volume of the composite (concrete and fibers), termed "volume fraction" (Vf). Vf typically ranges from 0.1 to 3%. The aspect ratio (l/d) is calculated by dividing fiber length (l) by its diameter (d). Fibers with a non-circular cross section use an equivalent diameter for the calculation of aspect ratio. If the fiber's modulus of elasticity is higher than the matrix (concrete or mortar binder), they help to carry the load by increasing the tensile strength of the material. Increasing the aspect ratio of the fiber usually segments the flexural strength and toughness of the matrix. Longer length results in better matrix inside the concrete and finer diameter increases the count of fibers. To ensure that each fiber strand is effective, it is recommended to use fibers longer than maximum size of aggregate. Normal concrete contains 19 mm equivalent diameter aggregate which is 35-45% of concrete, fibers longer than 20mm are more effective. However, fibers that are too long and not properly treated at time of processing tend to "ball" in the mix and create workability problems. Fibers are added for long term durability of concrete. Glass [3] and polyester



IDEAL INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. M . Anjana Kumar is giving lecture on Fibre Reinforced Concrete

P. Ravi Kiran
IQAC

T. S. R.
PRINCIPAL
Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

About Workshop

This two-day workshop will introduce Python, the programming language designed to emphasize code readability. Learners will gain the working knowledge needed to start programming, debugging and using Python in everyday tasks.

Learning Goals

This main goal is to learn the core Python language and the standard library in detail. Learning various skills including text manipulation, modular programming, working with and retrieving data, interacting with files on your computer, and using some of the more popular third-party libraries (and getting them installed when and where we need them). The goal is to get participants up and running with Python in as short a time as possible.

Activities

Students will learn the basics of writing and running Python scripts. will cover topics for people completely new to programming along with comparisons and contrasts to other programming languages. Everything from "OMG white space?!!!" to ways to manipulate the language into a very terse format to cool tricks we can do with the simplest, most basic Python data-types.

DAY 1	Core Python Language
DAY 2	Standard Library Functions, Packages for Python

Program Committee

Chief Patrons:

Dr. P. Krishna Rao
Chairman

Patrons:

Dr. P. Chiranjeevini Kumari
Secretary & Correspondent

Principal:

Dr. T. Srikanth

Convenor:

Mr. M.S.R.S.Prasad, Assoc. Prof.
HoD, CSE

Coordinators:

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Smt. S.Jyothirmayee, Asst. Prof.
Smt. M. Jayasree, Asst. Prof.
Smt. P.Sai Sowjanya, Asst. Prof.
Smt. V.Satya Sri, Asst. Prof.
Smt. N.D.Deepti Priya, Asst. Prof.
Mr. R.S.V.V.Prasad Rao, Asst. Prof.
Ms. R.Sridivys, Asst. Prof.



TWO DAY WORKSHOP ON "PYTHON PROGRAMMING"

17th Mar 2020 to 18th Mar 2020

Organized by
Department of CSE

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The ideal junior college had been started to cater to the needs of student community. In due course the jr college flourished into Degree college and P.G courses like M.B.A, M.C.A, M.A, M.Sc and Engineering College.

Now the college is run under the expertise of Dr. P Krishna Rao, Chairman of Pragati Engineering College to improve the Capabilities and Job potentials of the students.

Ideal institute of technology is one and only institute which is located in the midst of Kakinada amid mango groves in the lap of Nature.

In this present era of knowledge and technology driven economy, globalization and liberalization, every employer and the industry house seeks employees to be productive and possess skills, competence and values. This is exactly what we at Ideal Institute endeavor to provide to our students. The spirit of innovation, modernization and student-centric teaching-learning are the corner stones of academics at Ideal Institute.

The faculty members strive to promote a sense of enquiry and questioning among their students. The culture at the institute is one of openness, mutual trust and faith most conducive to unconditional commitment to learning.

Our faculty colleagues thrive on a diet of commitment towards excellence. We believe that learning shall be a joy and teaching a pleasure

The Institute is committed to generate, disseminate and preserve knowledge to work with others to bring this knowledge to face the global challenges. We are committed to provide our students an education that combines the rigours of academics and the grind of application along with the excitement of discovery.

We seek to develop in each member of the Ideal community the ability and passion to work wisely, creatively and effectively for the betterment of mankind.

The Institute extends moral and placement support too. Value based education, mentoring and character building, job oriented short term courses, guest speakers from the industry, visits to industry houses, Entrepreneurship Development Programmes are our

main stay. Students live in a unique environment that promotes culture, sports, society contributions, and above all the self-governance.

The institute is always abuzz with new ideas and is an exciting place to be, if you happen to be a dreamer.

The Institution is having more than 1,50,000 sq. ft., built up area with well established laboratories in each department to cater to the needs of the students. Besides the Institution is providing transport facility to the needy and also have good sports and canteen facilities.

A team of well qualified and experienced faculty members mould the young students into future Engineers of world.

Introduction to C Programming

Python has become a staple in data science, allowing data analysts and other professionals to use the language to conduct complex statistical calculations, create data visualizations, build machine learning algorithms, manipulate and analyze data, and complete other data-related tasks.

Python can build a wide range of different data visualizations, like line and bar graphs, pie charts, histograms, and 3D plots. Python also has a number of libraries that enable coders to write programs for data analysis and machine learning more quickly and efficiently.



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Dr. B. Sujatha is giving lecture on Python Programming

Pradeep
IQAC

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PRINCIPAL

Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA

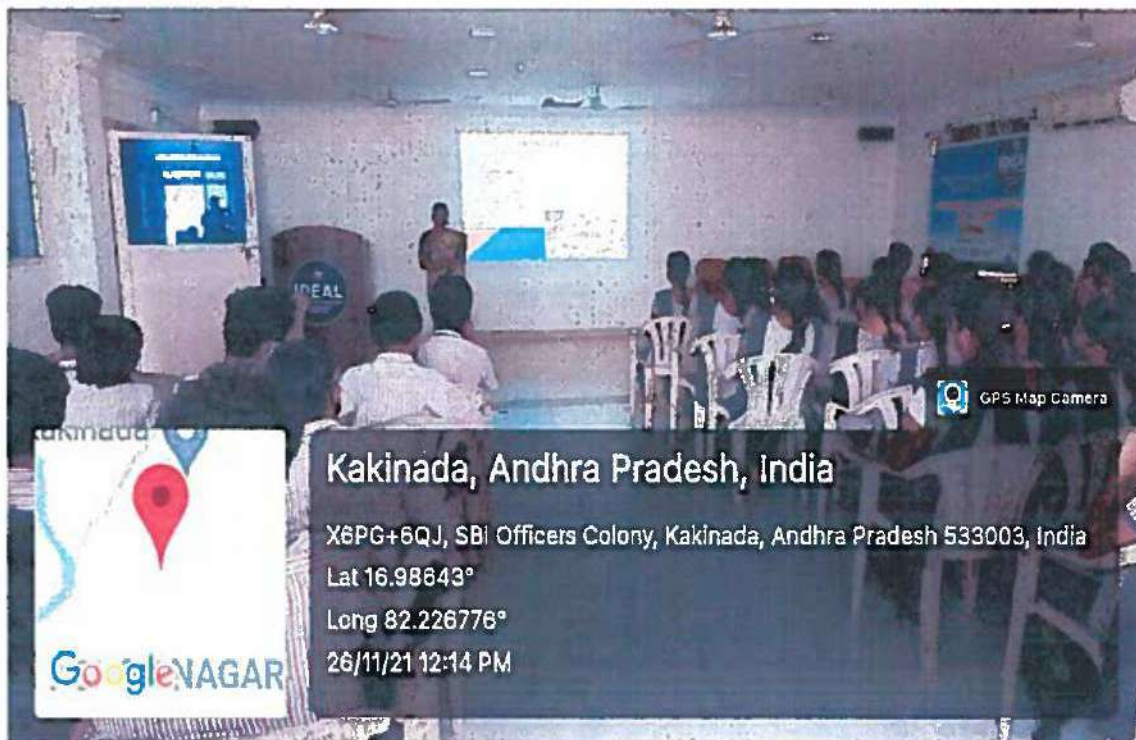
2020-21



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M. Sireesha is giving Seminar on Intellectual Property Rights

fledhik
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T. S. R.
PRINCIPAL
Principal
IDEAL INSTITUTE OF TECHNOLOGY
VIDYUTNAGAR, KAKINADA



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ONE DAY SEMINAR

ON

PROFESSIONAL ETHICS & HUMAN VALUES
18-12-2021

BY

Sri V.KAMARAJU- Assistant Professor
Ideal College Of Arts & Sciences-Kakinada.



Kakinada, Andhra Pradesh, India

X6PG+6JG, SBI Officers Colony, Kakinada, Andhra Pradesh

533003, India

Lat 16.98639°

Long 82.226725°

18/12/21 12:18 PM

Mr V. Kamalaju is giving
Seminar on professional Ethics &
Human values



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Vidyut Nagar, KAKINADA- 533 003. (A.P.)



Mr. V. Kamaraju is giving a Lecture on Professional Ethics & Human Values.

P. Reddy
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