

III Year - I Semester

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COMPUTER ARCHITECTURE AND ORGANIZATION

OBJECTIVES:

- Understand the architecture of a modern computer with its various processing units. Also the Performance measurement of the computer system.
- In addition to this the memory management system of computer.

UNIT -I:

Basic Structure Of Computers: Functional unit, Basic Operational concepts, Bus structures, System Software, Performance, The history of computer development.

UNIT -II:

Machine Instruction and Programs:

Instruction and Instruction Sequencing: Register Transfer Notation, Assembly Language Notation, Basic Instruction Types,

Addressing Modes, Basic Input/output Operations, The role of Stacks and Queues in computer programming equation. Component of Instructions: Logic Instructions, shift and Rotate Instructions

UNIT -III:

Type of Instructions: Arithmetic and Logic Instructions, Branch Instructions, Addressing Modes, Input/output Operations

UNIT -IV:

INPUT/OUTPUT ORGANIZATION: Accessing I/O Devices, Interrupts: Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Direct Memory Access,

Buses: Synchronous Bus, Asynchronous Bus, Interface Circuits, Standard I/O Interface: Peripheral Component Interconnect (PCI) Bus, Universal Serial Bus (USB)

UNIT -V:

The MEMORY SYSTEMS: Basic memory circuits, Memory System Consideration, Read-Only Memory: ROM, PROM, EPROM, EEPROM, Flash Memory,

Cache Memories: Mapping Functions, INTERLEAVING

Secondary Storage: Magnetic Hard Disks, Optical Disks,

UNIT -VI:

Processing Unit: Fundamental Concepts: Register Transfers, Performing An Arithmetic Or Logic Operation, Fetching A Word From Memory,

Execution of Complete Instruction, Hardwired Control,

Micro programmed Control: Microinstructions, Micro program Sequencing, Wide Branch Addressing Microinstructions with next –Address Field

OUTCOMES:

- Students can understand the architecture of modern computer.
- They can analyze the Performance of a computer using performance equation
- Understanding of different instruction types.
- 4. Students can calculate the effective address of an operand by addressing modes
- 5. They can understand how computer stores positive and negative numbers.
- 6. Understanding of how a computer performs arithmetic operation of positive and negative numbers.

TEXT BOOKS:

1. Computer Organization, Carl Hamacher, Zvonks Vranesic, Safea Zaky, 5th Edition, McGraw Hill.
2. Computer Architecture and Organization , John P. Hayes ,3rd Edition, McGraw Hill.

REFERENCE BOOKS:

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/Pearson
3. Fundamentals of Computer Organization and Design, - Sivaraama Dandamudi Springer Int. Edition.
4. “Computer Organization and Design: The Hardware/Software Interface” by David A. Patterson and John L. Hennessy.
5. J .P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998.

III Year - I Semester

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LINEAR IC APPLICATIONS

OBJECTIVES

- To understand the basic operation & performance parameters of differential amplifiers.
- To understand & learn the measuring techniques of performance parameters of OP-AMP
- To learn the linear and non-linear applications of operational amplifiers.
- To understand the analysis & design of different types of active filters using opamps
- To learn the internal structure, operation and applications of different analog ICs
- To Acquire skills required for designing and testing integrated circuits

UNIT I

INTEGRATED CIRCUITS: Differential Amplifier- DC and AC analysis of Dual input Balanced output Configuration, Properties of other differential amplifier configuration (Dual Input Unbalanced Output, Single Ended Input – Balanced/ Unbalanced Output), DC Coupling and Cascade Differential Amplifier Stages, Level translator.

UNIT II

Characteristics of OP-Amps, Integrated circuits-Types, Classification, Package Types and Temperature ranges, Power supplies, Op-amp Block Diagram, ideal and practical Op-amp Specifications, DC and AC characteristics, 741 op-amp & its features, Op-Amp parameters & Measurement, Input & Out put Off set voltages & currents, slew rate, CMRR, PSRR, drift, Frequency Compensation techniques.

UNIT III

LINEAR and NON-LINEAR APPLICATIONS OF OP-AMPS: Inverting and Non-inverting amplifier, Integrator and differentiator, Difference amplifier, Instrumentation amplifier, AC amplifier, V to I, I to V converters, Buffers. Non- Linear function generation, Comparators, Multivibrators, Triangular and Square wave generators, Log and Anti log Amplifiers, Precision rectifiers.

UNIT IV

ACTIVE FILTERS, ANALOG MULTIPLIERS AND MODULATORS: Design & Analysis of Butterworth active filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and all pass filters. Four Quadrant Multiplier, IC 1496, Sample & Hold circuits.

UNIT V

TIMERS & PHASE LOCKED LOOPS: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger; PLL - introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566).

UNIT VI

DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS: Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC. DAC and ADC Specifications, Specifications AD 574 (12 bit ADC).

TEXT BOOKS:

1. Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003.
2. Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1987.
3. Operational Amplifiers–C.G. Clayton, Butterworth & Company Publ. Ltd./Elsevier, 1971

REFERENCES :

1. Operational Amplifiers & Linear Integrated Circuits –Sanjay Sharma ;SK Kataria &Sons;2nd Edition,2010
2. Design with Operational Amplifiers & Analog Integrated Circuits – Sergio Franco, McGraw Hill, 1988.
3. OP AMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cenage Learning India Ltd.
4. Operational Amplifiers & Linear Integrated Circuits–R.F.Coughlin & Fredrick Driscoll, PHI, 6th Edition.
5. Operational Amplifiers & Linear ICs – David A Bell, Oxford Uni. Press, 3rd Edition

OUTCOMES

- Design circuits using operational amplifiers for various applications.
- Analyze and design amplifiers and active filters using Op-amp.
- Diagnose and trouble-shoot linear electronic circuits.
- Understand the gain-bandwidth concept and frequency response of the amplifier configurations.
- Understand thoroughly the operational amplifiers with linear integrated circuits.

III Year - I Semester

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DIGITAL IC APPLICATIONS

OBJECTIVES

The main objectives of this course are:

- Introduction of digital logic families and interfacing concepts for digital design is considered.
- VHDL fundamentals were discussed to modeling the digital system design blocks.
- VHDL compilers, simulators and synthesis tools are described, which are used to verify digital systems in a technology-independent fashion.
- Design and implementation of combinational and sequential digital logic circuits is explained.

Outcomes:

At the end of this course the student can able to:

- Understand the structure of commercially available digital integrated circuit families.
- Learn the IEEE Standard 1076 Hardware Description Language (VHDL).
- Model complex digital systems at several levels of abstractions, behavioral, structural, simulation, synthesis and rapid system prototyping.
- Analyze and design basic digital circuits with combinatorial and sequential logic circuits using VHDL.

Syllabus:

UNIT-I

Digital Logic Families and Interfacing: Introduction to logic families, CMOS logic, CMOS steady state and dynamic electrical behavior, CMOS logic families. Bipolar logic, transistor-transistor logic, TTL families, CMOS/TTL interfacing, low voltage CMOS logic and interfacing, Emitter coupled logic.

(Text book-1)

UNIT-II

Introduction to VHDL: Design flow, program structure, levels of abstraction, Elements of VHDL: Data types, data objects, operators and identifiers. Packages, Libraries and Bindings, Subprograms. VHDL Programming using structural and data flow modeling.

(Text book-2)

UNIT-III

Behavioral Modeling: Process statement, variable assignment statement, signal assignment statement, wait statement, if statement, case statement, null statement, loop statement, exit statement, next statement, assertion statement, more on signal assignment statement, Inertial Delay Model, Transport Delay Model, Creating Signal Waveforms, Signal Drivers, Other Sequential Statements, Multiple Processes. Logic Synthesis, Inside a logic Synthesizer.

(Text book-2)

UNIT-IV

Combinational Logic Design: Binary Adder-Subtractor, Ripple Adder, Look Ahead Carry Generator, ALU, Decoders, encoders, multiplexers and demultiplexers, parity circuits, comparators, Barrel Shifter, Simple Floating-Point Encoder, Dual Priority Encoder, Design considerations of the above combinational logic circuits with relevant Digital ICs, modeling of above ICs using VHDL.

(Text book-1)

UNIT-V

Sequential Logic Design: SSI Latches and flip flops, Ring Counter, Johnson Counter, Design of Modulus N Synchronous Counters, Shift Registers, Universal Shift Registers, Design considerations of the above sequential logic circuits with relevant Digital ICs, modeling of above ICs using VHDL.

(Text book-1)

UNIT-VI:

Synchronous and Asynchronous Sequential Circuits: Basic design steps: State diagram, state table, state assignment, choice of flip flops and derivation of next state and output expressions, timing diagram. State assignment problem: One hot encoding. Mealy and Moore type FSM for serial adder, VHDL code for the serial adder. Analysis of Asynchronous circuits, State Reduction, State Assignment. A complete design example: The vending machine controller.

(Reference text book- 1)

Text Books:

1. Digital Design Principles & Practices – John F. Wakerly, PHI/ Pearson Education Asia, 3rd Ed., 2005.
2. VHDL Primer – J. Bhasker, Pearson Education/ PHI, 3rd Edition.

References:

1. Fundamentals of Digital Logic with VHDL Design- Stephen Brown, Zvonko Vranesic, McGrawHill, 3rd Edition.

III Year - I Semester

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DIGITAL COMMUNICATIONS

UNIT I

PULSE DIGITAL MODULATION: Elements of digital communication systems, advantages of digital communication systems, Elements of PCM: Sampling, Quantization & Coding, Quantization error, Companding in PCM systems. Differential PCM systems (DPCM). Delta modulation, its draw backs, adaptive delta modulation, comparison of PCM and DM systems, noise in PCM and DM systems.

UNIT II

DIGITAL MODULATION TECHNIQUES: Introduction, ASK, FSK, PSK, DPSK, DEPSK, QPSK, M-ary PSK, ASK, FSK, similarity of BFSK and BPSK.

UNIT III

DATA TRANSMISSION : Base band signal receiver, probability of error, the optimum filter, matched filter, probability of error using matched filter, coherent reception, non-coherent detection of FSK, calculation of error probability of ASK, BPSK, BFSK, QPSK.

UNIT IV

INFORMATION THEORY: Discrete messages, concept of amount of information and its properties. Average information, Entropy and its properties. Information rate, Mutual information and its properties.

UNIT V

SOURCE CODING: Introductions, Advantages, Shannon's theorem, Shannon-Fano coding, Huffman coding, efficiency calculations, channel capacity of discrete and analog Channels, capacity of a Gaussian channel, bandwidth –S/N trade off.

UNIT VI

LINEAR BLOCK CODES: Introduction, Matrix description of Linear Block codes, Error detection and error correction capabilities of Linear block codes, Hamming codes, Binary cyclic codes, Algebraic structure, encoding, syndrome calculation, BCH Codes.

CONVOLUTION CODES: Introduction, encoding of convolution codes, time domain approach, transform domain approach. Graphical approach: state, tree and trellis diagram decoding using Viterbi algorithm.

TEXT BOOKS:

1. Digital communications - Simon Haykin, John Wiley, 2005
2. Principles of Communication Systems – H. Taub and D. Schilling, TMH, 2003

REFERENCES:

1. Digital and Analog Communication Systems - Sam Shanmugam, John Wiley, 2005.
2. Digital Communications – John Proakis, TMH, 1983. Communication Systems Analog & Digital – Singh & Sapre, TMH, 2004.
3. Modern Analog and Digital Communication – B.P.Lathi, Oxford reprint, 3rd edition, 2004.

Students undergoing this course are expected to:

Course Objectives:

1. Understand different pulse digital modulation techniques and their comparison
2. Familiarize various digital modulation techniques and calculation of their error probabilities
3. Understand the concept of entropy and different source coding techniques
4. Familiarize with block codes, cyclic codes and convolutional codes

Course Outcomes:

After undergoing the course students will be able to:

1. Determine the performance of different waveform coding techniques for the generation and digital representation of the signals.
2. Determine the probability of error for various digital modulation schemes
3. Analyze different source coding techniques
4. Compute and analyze different error control coding schemes for the reliable transmission of digital information over the channel.

III Year - I Semester

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ANTENNA AND WAVE PROPAGATION

OBJECTIVES

The student will be able to

- understand the applications of the electromagnetic waves in free space.
- introduce the working principles of various types of antennas
- discuss the major applications of antennas with an emphasis on how antennas are employed to meet electronic system requirements.
- understand the concepts of radio wave propagation in the atmosphere.

UNIT I

ANTENNA FUNDAMENTALS: Introduction, Radiation Mechanism – single wire, 2 wire, dipoles, Current Distribution on a thin wire antenna. Antenna Parameters - Radiation Patterns, Patterns in Principal Planes, Main Lobe and Side Lobes, Beamwidths, Polarization, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, illustrated Problems.

UNIT II

THIN LINEAR WIRE ANTENNAS: Retarded Potentials, Radiation from Small Electric Dipole, Quarter wave Monopole and Half wave Dipole – Current Distributions, Evaluation of Field Components, Power Radiated, Radiation Resistance, Beamwidths, Directivity, Effective Area and Effective Height. Natural current distributions, fields and patterns of Thin Linear Center-fed Antennas of different lengths, Radiation Resistance at a point which is not current maximum. Antenna Theorems – Applicability and Proofs for equivalence of directional characteristics, Loop Antennas: Small Loops - Field Components, Comparison of far fields of small loop and short dipole, Concept of short magnetic dipole, D and R_r relations for small loops.

UNIT III

ANTENNA ARRAYS : 2 element arrays – different cases, Principle of Pattern Multiplication, N element Uniform Linear Arrays – Broadside, End-fire Arrays, EFA with Increased Directivity, Derivation of their characteristics and comparison; Concept of Scanning Arrays. Directivity Relations (no derivations). Related Problems. Binomial Arrays, Effects of Uniform and Non-uniform Amplitude Distributions, Design Relations. Arrays with Parasitic Elements, Yagi-Uda Arrays, Folded Dipoles and their characteristics.

UNIT IV

NON-RESONANT RADIATORS : Introduction, Traveling wave radiators – basic concepts, Long wire antennas – field strength calculations and patterns, Microstrip Antennas-Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas –Geometry and Parameters, Impact of different parameters on characteristics. Broadband Antennas: Helical Antennas – Significance, Geometry, basic properties; Design considerations for monofilar helical antennas in Axial Mode and Normal Modes (Qualitative Treatment).

UNIT V

VHF, UHF AND MICROWAVE ANTENNAS : Reflector Antennas : Flat Sheet and Corner Reflectors. Paraboloidal Reflectors – Geometry, characteristics, types of feeds, F/D Ratio, Spill Over, Back Lobes, Aperture Blocking, Off-set Feeds, Cassegrain Feeds.

Horn Antennas – Types, Optimum Horns, Design Characteristics of Pyramidal Horns; Lens Antennas – Geometry, Features, Dielectric Lenses and Zoning, Applications, Antenna Measurements – Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and 3-Antenna Methods).

UNIT VI

WAVE PROPAGATION : Concepts of Propagation – frequency ranges and types of propagations. Ground Wave Propagation–Characteristics, Parameters, Wave Tilt, Flat and Spherical Earth Considerations. Sky Wave Propagation – Formation of Ionospheric Layers and their Characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF and Skip Distance – Calculations for flat and spherical earth cases, Optimum Frequency, LUHF, Virtual Height, Ionospheric Abnormalities, Ionospheric Absorption.

Fundamental Equation for Free-Space Propagation, Basic Transmission Loss Calculations. Space Wave Propagation – Mechanism, LOS and Radio Horizon. Tropospheric Wave Propagation – Radius of Curvature of path, Effective Earth's Radius, Effect of Earth's Curvature, Field Strength Calculations, M-curves and Duct Propagation, Tropospheric Scattering.

TEXT BOOKS

1. Antennas for All Applications – John D. Kraus and Ronald J. Marhefka, 3rd Edition, TMH, 2003.
2. Electromagnetic Waves and Radiating Systems – E.C. Jordan and K.G. Balmain, PHI, 2nd Edition, 2000.

REFERENCES

1. Antenna Theory - C.A. Balanis, John Wiley and Sons, 2nd Edition, 2001.
2. Antennas and Wave Propagation – K.D. Prasad, Satya Prakashan, Tech India Publications, New Delhi, 2001.
3. Transmission and Propagation – E.V.D. Glazier and H.R.L. Lamont, The Services Text Book of Radio, vol. 5, Standard Publishers Distributors, Delhi.
4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4th Edition, 1955.
5. Antennas – John D. Kraus, McGraw-Hill, 2nd Edition, 1988.

OUTCOMES

After going through this course the student will be able to

- Identify basic antenna parameters.
- Design and analyze wire antennas, loop antennas, reflector antennas, lens antennas, horn antennas and microstrip antennas
- Quantify the fields radiated by various types of antennas
- Design and analyze antenna arrays
- Analyze antenna measurements to assess antenna's performance
- Identify the characteristics of radio wave propagation

III Year - I Semester

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PULSE & DIGITAL CIRCUITS LAB

- 1. Linear wave shaping.**
- 2. Non Linear wave shaping – Clippers.**
- 3. Non Linear wave shaping – Clampers.**
- 4. Transistor as a switch.**
- 5. Study of Logic Gates & Some applications.**
- 6. Study of Flip-Flops & some applications.**
- 7. Sampling Gates.**
- 8. Astable Multivibrator.**
- 9. Monostable Multivibrator.**
- 10. Bistable Multivibrator.**
- 11. Schmitt Trigger.**
- 12. UJT Relaxation Oscillator.**
- 13. Bootstrap sweep circuit.**

Equipment required for Laboratory:

- 1. RPS - 0 – 30 V**
- 2. CRO - 0 – 20 M Hz.**
- 3. Function Generators - 0 – 1 M Hz**
- 4. Components**
- 5. Multi Meters**

III Year - I Semester

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L IC APPLICATIONS LAB

Minimum Twelve Experiments to be conducted :

1. Study of OP AMPs – IC 741, IC 555, IC 565, IC 566, IC 1496 – functioning, parameters and Specifications.
2. OP AMP Applications – Adder, Subtractor, Comparator Circuits.
3. Integrator and Differentiator Circuits using IC 741.
4. Active Filter Applications – LPF, HPF (first order)
5. Active Filter Applications – BPF, Band Reject (Wideband) and Notch Filters.
6. IC 741 Oscillator Circuits – Phase Shift and Wien Bridge Oscillators.
7. Function Generator using OP AMPs.
8. IC 555 Timer – Monostable Operation Circuit.
9. IC 555 Timer – Astable Operation Circuit.
10. Schmitt Trigger Circuits – using IC 741 and IC 555.
11. IC 565 – PLL Applications.
12. IC 566 – VCO Applications.
13. Voltage Regulator using IC 723.
14. Three Terminal Voltage Regulators – 7805, 7809, 7912.

Equipment required for Laboratories:

1. RPS
2. CRO
3. Function Generator
4. Multi Meters
5. IC Trainer Kits (Optional)
6. Bread Boards
7. Components:- IC741, IC555, IC565, IC1496, IC723, 7805, 7809, 7912 and other essential components.
8. Analog IC Tester

III Year - I Semester

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DICA LABORATORY

Note: The students are required to design and draw the internal logical structure of the following Digital Integrated Circuits and to develop VHDL/Verilog HDL Source code, perform simulation using relevant simulator and analyze the obtained simulation results using necessary synthesizer.

All the experiments are required to verify and implement the logical operations on the latest FPGA Hardware in the Laboratory.

List of Experiments :(Minimum of Ten Experiments has to be performed)

1. Realization of Logic Gates
2. Design of Full Adder using 3 modeling systems
3. 3 to 8 Decoder -74138
4. 8 to 3 Encoder (with and without parity)
5. 8 x 1 Multiplexer-74151 and 2x 4 De-multiplexer-74155
6. 4- Bit comparator-7485
7. D Flip-Flop-7474
8. Decade counter -7490
9. Shift registers-7495
10. 8-bit serial in-parallel out and parallel in-serial out
11. Fast In & Fast Out (FIFO)
12. MAC (Multiplier & Accumulator)
13. ALU Design.

Equipment/Software required:

1. Xilinx Vivado software / Equivalent Industry Standard Software
2. Xilinx Hardware / Equivalent hardware
3. Personal computer system with necessary software to run the programs and Implement.

III Year - I Semester

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PROFESSIONAL ETHICS AND HUMAN VALUES

Course Objectives:

***To give basic insights and inputs to the student to inculcate Human values to grow as a responsible human beings with proper personality.**

***Professional Ethics instills the student to maintain ethical conduct and discharge their professional duties.**

UNIT I: Human Values:

Morals, Values and Ethics – Integrity – Trustworthiness - Work Ethics – Service Learning – Civic Virtue – Respect for others – Living Peacefully – Caring – Sharing – Honesty – Courage – Value Time – Co-operation – Commitment – Empathy – Self-confidence – Spirituality- Character.

UNIT II: Principles for Harmony:

Truthfulness – Customs and Traditions - Value Education – Human Dignity – Human Rights – Fundamental Duties - Aspirations and Harmony (I, We & Nature) – Gender Bias - Emotional Intelligence – Salovey – Mayer Model – Emotional Competencies – Conscientiousness.

UNIT III: Engineering Ethics and Social Experimentation:

History of Ethics - Need of Engineering Ethics - Senses of Engineering Ethics- Profession and Professionalism — Self Interest - Moral Autonomy – Utilitarianism – Virtue Theory - Uses of Ethical Theories - Deontology- Types of Inquiry – Kohlberg's Theory - Gilligan's Argument – Heinz's Dilemma - Comparison with Standard Experiments — Learning from the Past – Engineers as Managers – Consultants and Leaders – Balanced Outlook on Law - Role of Codes – Codes and Experimental Nature of Engineering.

UNIT IV: Engineers' Responsibilities towards Safety and Risk:

Concept of Safety - Safety and Risk – Types of Risks – Voluntary v/s Involuntary Risk – Consequences - Risk Assessment – Accountability – Liability - Reversible Effects - Threshold Levels of Risk - Delayed v/s Immediate Risk - Safety and the Engineer – Designing for Safety – Risk-Benefit Analysis-Accidents.

UNIT V: Engineers' Duties and Rights:

Concept of Duty - Professional Duties – Collegiality - Techniques for Achieving Collegiality – Senses of Loyalty - Consensus and Controversy - Professional and Individual Rights – Confidential and Proprietary Information - Conflict of Interest-Ethical egoism - Collective Bargaining – Confidentiality - Gifts and Bribes - Problem solving- Occupational Crimes- Industrial Espionage- Price Fixing-Whistle Blowing.

UNIT VI: Global Issues:

Globalization and MNCs – Cross Culture Issues - Business Ethics – Media Ethics - Environmental Ethics – Endangering Lives - Bio Ethics - Computer Ethics - War Ethics – Research Ethics - Intellectual Property Rights.

- Related Cases Shall be dealt where ever necessary.

Outcome:

***It gives a comprehensive understanding of a variety issues that are encountered by every professional in discharging professional duties.**

***It provides the student the sensitivity and global outlook in the contemporary world to fulfill the professional obligations effectively.**

References:

1. Professional Ethics by R. Subramaniam – Oxford Publications, New Delhi.
2. Ethics in Engineering by Mike W. Martin and Roland Schinzinger - Tata McGraw-Hill – 2003.
3. Professional Ethics and Morals by Prof.A.R.Aryasri, DharanikotaSuyodhana - Maruthi Publications.
4. Engineering Ethics by Harris, Pritchard and Rabins, Cengage Learning, New Delhi.
5. Human Values & Professional Ethics by S. B. Gogate, Vikas Publishing House Pvt. Ltd., Noida.
6. Engineering Ethics & Human Values by M.Govindarajan, S.Natarajan and V.S.SenthilKumar-PHI Learning Pvt. Ltd – 2009.
7. Professional Ethics and Human Values by A. Alavudeen, R.Kalil Rahman and M. Jayakumaran – University Science Press.
8. Professional Ethics and Human Values by Prof.D.R.Kiran-Tata McGraw-Hill - 2013
9. Human Values And Professional Ethics by Jayshree Suresh and B. S. Raghavan, S.Chand Publications

III Year - II Semester

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MICROPROCESSORS AND MICROCONTROLLERS

UNIT-I:

8086 ARCHITECTURE: Main features, pin diagram/description, 8086 microprocessor family, 8086 internal architecture, bus interfacing unit, execution unit, interrupts and interrupt responses, 8086 system timing, minimum mode and maximum mode configuration.

UNIT-II:

8086 PROGRAMMING: Program development steps, instructions, addressing modes, assembler directives, writing simple programs with an assembler, assembly language program development tools.

UNIT-III:

8086 INTERFACING : Semiconductor memories interfacing (RAM,ROM), 8254 software programmable timer/counter, Intel 8259 programmable interrupt controller, software and hardware interrupt applications, Intel 8237a DMA controller, Intel 8255 programmable peripheral interface, keyboard interfacing, alphanumeric displays (LED,7-segment display, multiplexed 7-segment display, LCD), Intel 8279 programmable keyboard/display controller, stepper motor, A/D and D/A converters.

UNIT-IV:

80386 AND 80486 MICROPROCESSORS: Introduction, programming concepts, special purpose registers, memory organization, moving to protected mode, virtual mode, memory paging mechanism, architectural differences between 80386 and 80486 microprocessors.

UNIT-V:

Intel 8051 MICROCONTROLLER: Architecture, hardware concepts, input/output ports and circuits, external memory, counters/timers, serial data input/output, interrupts.

Assembly language programming: Instructions, addressing modes, simple programs.

Interfacing: keyboard, displays (LED, 7-segment display unit), A/D and D/A converters.

UNIT-VI:

PIC MICROCONTROLLER: Introduction, characteristics of PIC microcontroller, PIC microcontroller families, memory organization, parallel and serial input and output, timers, Interrupts, PIC 16F877 architecture, instruction set of the PIC 16F877.

Text Books:

1. Microprocessors and Interfacing – Programming and Hardware by Douglas V Hall, SSSP Rao, Tata McGraw Hill Education Private Limited, 3rd Edition.
2. The 8051 Microcontroller & Embedded Systems Using Assembly and C by Kenneth J.Ayala, Dhananjay V.Gadre,Cengage Learning , India Edition.

References:

1. The Intel Microprocessors-Architecture, Programming, and Interfacing by Barry B.Brey, Pearson, Eighth Edition-2012.
2. Microprocessors and Microcontrollers-Architecture, Programming and System Design by Krishna Kant, PHI Learning Private Limited, Second Edition, 2014.
3. Microprocessors and Microcontrollers by N.Senthil Kumar, M.Saravanan and S.Jeevananthan, Oxford University Press, Seventh Impression 2013

III Year - II Semester

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MICROWAVE ENGINEERING

OBJECTIVES

The student will

- Understand fundamental characteristics of waveguides and Microstrip lines through electromagnetic field analysis.
- Understand the basic properties of waveguide components and Ferrite materials composition
- Understand the function, design, and integration of the major microwave components oscillators, power amplifier.
- Understand a Microwave test bench setup for measurements.

UNIT I

MICROWAVE TRANSMISSION LINES: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations; Power Transmission and Power Losses in Rectangular Guide, Impossibility of TEM mode. Related Problems.

UNIT II

CIRCULAR WAVEGUIDES: Introduction, Nature of Fields, Characteristic Equation, Dominant and Degenerate Modes.

Cavity Resonators– Introduction, Rectangular and Cylindrical Cavities, Dominant Modes and Resonant Frequencies, Q factor and Coupling Coefficients, Excitation techniques- waveguides and cavities, Related Problems.

MICROSTRIP LINES– Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor.

UNIT III

MICROWAVE TUBES :Limitations and Losses of conventional tubes at microwave frequencies. Re-entrant Cavities, Microwave tubes – O type and M type classifications. O-type tubes :2 Cavity Klystrons – Structure, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory –Expressions for o/p Power and Efficiency, Applications, Reflex Klystrons – Structure, Applegate Diagram and Principle of working, Mathematical Theory of Bunching, Power Output, Efficiency, Electronic Admittance; Oscillating Modes and o/p Characteristics, Electronic and Mechanical Tuning, Applications, Related Problems.

UNIT - IV

HELIX TWTS: Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT and Suppression of Oscillations, Nature of the four Propagation Constants(Qualitative treatment).

M-type Tubes

Introduction, Cross-field effects, Magnetrons – Different Types, 8-Cavity Cylindrical Travelling Wave Magnetron – Hull Cut-off Condition, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics.

UNIT V

WAVEGUIDE COMPONENTS AND APPLICATIONS - I :Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide irises, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Resistive Card, Rotary Vane types; Waveguide Phase Shifters – Dielectric, Rotary Vane types. Scattering Matrix– Significance, Formulation and Properties. S-Matrix Calculations for – 2 port Junction, E-plane and H-plane Tees, Magic Tee, Hybrid Ring; Directional Couplers – 2Hole, Bethe Hole types, Ferrite Components– Faraday Rotation, S-Matrix Calculations for Gyrator, Isolator, Circulator, Related Problems.

UNIT VI

MICROWAVE SOLID STATE DEVICES: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diode – Principle, RWH Theory, Characteristics, Basic Modes of Operation, Oscillation Modes. Avalanche Transit Time Devices – Introduction, IMPATT and TRAPATT Diodes – Principle of Operation and Characteristics. **MICROWAVE MEASUREMENTS:** Description of Microwave Bench – Different Blocks and their Features, Precautions; Microwave Power Measurement – Bolometer Method. Measurement of Attenuation, Frequency, Q-factor, Phase shift, VSWR, Impedance Measurement.

TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, PHI, 3rd Edition, 1994.
2. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2nd Edition, 2002.

REFERENCES:

1. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004
2. Microwave Engineering- Annapurna Das and Sisir K.Das, Mc Graw Hill Education, 3rd Edition.
3. Microwave and Radar Engineering-M.Kulkarni, Umesh Publications, 3rd Edition.
4. Microwave Engineering – G S N Raju , I K International
5. Microwave and Radar Engineering – G Sasibhushana Rao Pearson

OUTCOMES : After going through this course the student will be able to

- Design different modes in waveguide structures
- Calculate S-matrix for various waveguide components and splitting the microwave energy in a desired direction
- Distinguish between Microwave tubes and Solid State Devices, calculation of efficiency of devices.
- Measure various microwave parameters using a Microwave test bench

III Year - II Semester

L	T	P	C
4	0	0	3

VLSI DESIGN

Objectives:

The main objectives of this course are:

- Basic characteristics of MOS transistor and examines various possibilities for configuring inverter circuits and aspects of latch-up are considered.
- Design processes are aided by simple concepts such as stick and symbolic diagrams but the key element is a set of design rules, which are explained clearly.
- Basic circuit concepts are introduced for MOS processes we can set out approximate circuit parameters which greatly ease the design process.

Outcomes:

At the end of this course the student can able to:

- Understand the properties of MOS active devices and simple circuits configured when using them and the reason for such encumbrances as ratio rules by which circuits can be interconnected in silicon.
- Know three sets of design rules with which nMOS and CMOS designs may be fabricated.
- Understand the scaling factors determining the characteristics and performance of MOS circuits in silicon.

Syllabus:

UNIT-I:

Introduction and Basic Electrical Properties of MOS Circuits: Introduction to IC technology, Fabrication process: nMOS, pMOS and CMOS. I_{ds} versus V_{ds} Relationships, Aspects of MOS transistor Threshold Voltage, MOS transistor Trans, Output Conductance and Figure of Merit. nMOS Inverter, Pull-up to Pull-down Ratio for nMOS inverter driven by another nMOS inverter, and through one or more pass transistors. Alternative forms of pull-up, The CMOS Inverter, Latch-up in CMOS circuits, Bi-CMOS Inverter, Comparison between CMOS and BiCMOS technology.

(Text Book-1)

UNIT-II:

MOS and Bi-CMOS Circuit Design Processes: MOS Layers, Stick Diagrams, Design Rules and Layout, General observations on the Design rules, 2 μ m Double Metal, Double Poly, CMOS/BiCMOS rules, 1.2 μ m Double Metal, Double Poly CMOS rules, Layout Diagrams of NAND and NOR gates and CMOS inverter, Symbolic Diagrams-Translation to Mask Form.

(Text Book-1)

UNIT-III:

Basic Circuit Concepts: Sheet Resistance, Sheet Resistance concept applied to MOS transistors and Inverters, Area Capacitance of Layers, Standard unit of capacitance, Some area Capacitance Calculations, The Delay Unit, Inverter Delays, Driving large capacitive loads, Propagation Delays, Wiring Capacitances, Choice of layers.

Scaling of MOS Circuits: Scaling models and scaling factors, Scaling factors for device parameters, Limitations of scaling, Limits due to sub threshold currents, Limits on logic levels and supply voltage due to noise and current density. Switch logic, Gate logic.

(Text Book-1)

UNIT-IV:

Chip Input and Output circuits: ESD Protection, Input Circuits, Output Circuits and $L(di/dt)$ Noise, On-Chip clock Generation and Distribution.

Design for Testability: Fault types and Models, Controllability and Observability, Ad Hoc Testable Design Techniques, Scan Based Techniques and Built-In Self Test techniques.

(Text Book-2)

UNIT-V:

FPGA Design: FPGA design flow, Basic FPGA architecture, FPGA Technologies, FPGA families- Altera Flex 8000FPGA, Altera Flex 10FPGA, Xilinx XC4000 series FPGA, Xilinx Spartan XL FPGA, Xilinx Spartan II FPGAs, Xilinx Vertex FPGA. Case studies: FPGA Implementation of Half adder and full adder.

Introduction to synthesis: Logic synthesis, RTL synthesis, High level Synthesis.

(Reference Text Book-1)

UNIT-VI:

Introduction to Low Power VLSI Design: Introduction to Deep submicron digital IC design, Low Power CMOS Logic Circuits: Over view of power consumption, Low –power design through voltage scaling, Estimation and optimisation of switching activity, Reduction of switching capacitance. Interconnect Design, Power Grid and Clock Design.

(Text Book-2)

Text Books:

1. Essentials of VLSI Circuits and Systems - Kamran Eshraghian, Douglas and A. Pucknell and Sholeh Eshraghian, Prentice-Hall of India Private Limited, 2005 Edition.
2. CMOS Digital Integrated Circuits Analysis and Design- Sung-Mo Kang, Yusuf Leblebici, Tata McGraw-Hill Education, 2003.

References:

1. Advanced Digital Design with the Verilog HDL, Michael D.Ciletti, Xilinx Design Series, Pearson Education
2. Analysis and Design of Digital Integrated Circuits in Deep submicron Technology, 3rd edition, David Hodges.

III Year - II Semester

L	T	P	C
4	0	0	3

DIGITAL SIGNAL PROCESSING

OBJECTIVES

The student will be able to

- Analyze the Discrete Time Signals and Systems
- Know the importance of FFT algorithm for computation of Discrete Fourier Transform
- Understand the various implementations of digital filter structures
- Learn the FIR and IIR Filter design procedures
- Know the need of Multirate Processing
- Learn the concepts of DSP Processors

UNIT I INTRODUCTION: Introduction to Digital Signal Processing: Discrete time signals & sequences, Classification of Discrete time systems, stability of LTI systems, Invertability, Response of LTI systems to arbitrary inputs. Solution of Linear constant coefficient difference equations. Frequency domain representation of discrete time signals and systems. Review of Z-transforms, solution of difference equations using Z-transforms, System function.

UNIT II DISCRETE FOURIER SERIES & FOURIER TRANSFORMS: Properties of discrete Fourier series, DFS representation of periodic sequences, Discrete Fourier transforms: Properties of DFT, linear filtering methods based on DFT, Fast Fourier transforms (FFT) - Radix-2 decimation in time and decimation in frequency FFT Algorithms, Inverse FFT.

UNIT III. DESIGN OF IIR DIGITAL FILTERS& REALIZATIONS: Analog filter approximations – Butter worth and Chebyshev, Design of IIR Digital filters from analog filters, Design Examples, Analog and Digital frequency transformations. Basic structures of IIR systems, Transposed forms.

UNIT IV DESIGN OF FIR DIGITAL FILTERS & REALIZATIONS:

Characteristics of FIR Digital Filters, frequency response. Design of FIR Digital Filters using Window Techniques and Frequency Sampling technique, Comparison of IIR & FIR filters.

Basic structures of FIR systems, *Lattice structures, Lattice-ladder structures*

UNIT V MULTIRATE DIGITAL SIGNAL PROCESSING: Introduction, Decimation, Interpolation Sampling rate conversion, Implementation of sampling rate converters, *Applications – Sub-band Coding of Speech Signals, Implementation of Digital Filter Banks, Trans-multiplexers.*

UNIT VI INTRODUCTION TO DSP PROCESSORS: Introduction to programmable DSPs: Multiplier and Multiplier Accumulator, Modified bus structures and memory access schemes in P-DSPs, Multiple Access Memory, Multiported memory, VLIW architecture, Pipelining, Special addressing modes, On-Chip Peripherals. Architecture of TMS320C5X: Introduction, Bus Structure, Central Arithmetic Logic Unit, Auxiliary Register ALU, Index Register, Block Move Address Register, Parallel Logic Unit, Memory mapped registers, program controller, some flags in the status registers, On-chip memory, On-chip peripherals.

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A.V. Oppenheim and R.W. Schaffer, PHI
3. Digital Signal Processors – Architecture, Programming and Applications,, B.Venkataramani, M.Bhaskar, TATA McGraw Hill, 2002
4. Digital Signal Processing – K Raja Rajeswari, I.K. International Publishing House

Reference Books:

1. Digital Signal Processing: Andreas Antoniou, TATA McGraw Hill , 2006
2. Digital Signal Processing: MH Hayes, Schaum's Outlines, TATA Mc-Graw Hill, 2007.
3. DSP Primer - C. Britton Rorabaugh, Tata McGraw Hill, 2005.
4. Fundamentals of Digital Signal Processing using Matlab – Robert J. Schilling, Sandra L. Harris, Thomson, 2007.
5. Digital Signal Processing – Alan V. Oppenheim, Ronald W. Schaffer, PHI Ed., 2006
6. Digital Signal Processing – Ramesh babu, Sci Tech publications

OUTCOMES

After going through this course the student will be able to

- Apply the difference equations concept in the analysis of Discrete time systems
- Use the FFT algorithm for solving the DFT of a given signal
- Design a Digital filter (FIR&IIR) from the given specifications
- Realize the FIR and IIR structures from the designed digital filter.
- Use the Multirate Processing concepts in various applications(eg: Design of phase shifters, Interfacing of digital systems...)
- Apply the signal processing concepts on DSP Processor.

III Year - II Semester

L	T	P	C
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**OOPS THROUGH JAVA
OPEN ELECTIVE**

OBJECTIVES:

- Understanding the OOP's concepts, classes and objects, threads, files, applets, swings and act.
- This course introduces computer programming using the JAVA programming language with object-oriented programming principles.
- Emphasis is placed on event-driven programming methods, including creating and manipulating objects, classes, and using Java for network level programming and middleware development

UNIT-I:

Introduction to OOP, procedural programming language and object oriented language, principles of OOP, applications of OOP, history of java, java features, JVM, program structure.

Variables, primitive data types, identifiers, literals, operators, expressions, precedence rules and associativity, primitive type conversion and casting, flow of control.

UNIT-II:

Classes and objects, class declaration, creating objects, methods, constructors and constructor overloading, garbage collector, importance of static keyword and examples, this keyword, arrays, command line arguments, nested classes.

UNIT-III:

Inheritance, types of inheritance, super keyword, final keyword, overriding and abstract class.

Interfaces, creating the packages, using packages, importance of CLASSPATH and java.lang package. Exception handling, importance of try, catch, throw, throws and finally block, user-defined exceptions, Assertions.

UNIT-IV:

Multithreading: introduction, thread life cycle, creation of threads, thread priorities, thread synchronization, communication between threads. Reading data from files and writing data to files, random access file,

UNIT-V:

Applet class, Applet structure, Applet life cycle, sample Applet programs. Event handling: event delegation model, sources of event, Event Listeners, adapter classes, inner classes.

UNIT-VI:

AWT: introduction, components and containers, Button, Label, Checkbox, Radio Buttons, List Boxes, Choice Boxes, Container class, Layouts, Menu and Scrollbar.

OUTCOMES:

- Understand Java programming concepts and utilize Java Graphical User Interface in Program writing.
- Write, compile, execute and troubleshoot Java programming for networking concepts.
- Build Java Application for distributed environment.
- Design and Develop multi-tier applications.

- Identify and Analyze Enterprise applications.

TEXT BOOKS:

1. The complete Reference Java, 8th edition, Herbert Schildt, TMH.
2. Programming in JAVA, Sachin Malhotra, Saurabh Choudary, Oxford.
3. Introduction to java programming, 7th edition by Y Daniel Liang, Pearson.

REFERENCE BOOKS:

1. Swing: Introduction, JFrame, JApplet, JPanel, Componets in Swings, Layout Managers in
2. Swings, JList and JScrollPane, Split Pane, JTabbedPane, JTree, JTable, Dialog Box.

DATA MINING OPEN ELECTIVE

OBJECTIVES:

- Students will be enabled to understand and implement classical models and algorithms in data warehousing and data mining.
- They will learn how to analyze the data, identify the problems, and choose the relevant models and algorithms to apply.
- They will further be able to assess the strengths and weaknesses of various methods and algorithms and to analyze their behavior.

UNIT –I

Introduction: Why Data Mining? What Is Data Mining? 1.3 What Kinds of Data Can Be Mined? 1.4 What Kinds of Patterns Can Be Mined? Which Technologies Are Used? Which Kinds of Applications Are Targeted? Major Issues in Data Mining. Data Objects and Attribute Types, Basic Statistical Descriptions of Data, Data Visualization, Measuring Data Similarity and Dissimilarity

UNIT –II

Data Pre-processing: Data Preprocessing: An Overview, Data Cleaning, Data Integration, Data Reduction, Data Transformation and Data Discretization

UNIT –III

Classification: Basic Concepts, General Approach to solving a classification problem, Decision Tree Induction: Working of Decision Tree, building a decision tree, methods for expressing an attribute test conditions, measures for selecting the best split, Algorithm for decision tree induction.

UNIT –IV

Classification: Alternative Techniques, Bayes' Theorem, Naïve Bayesian Classification, Bayesian Belief Networks

UNIT –V

Association Analysis: Basic Concepts and Algorithms: Problem Defecation, Frequent Item Set generation, Rule generation, compact representation of frequent item sets, FP-Growth Algorithm. **(Tan & Vipin)**

UNIT –VI

Cluster Analysis: Basic Concepts and Algorithms: Overview: What Is Cluster Analysis? Different Types of Clustering, Different Types of Clusters; K-means: The Basic K-means Algorithm, K-means Additional Issues, Bisecting K-means, Strengths and Weaknesses; Agglomerative Hierarchical Clustering: Basic Agglomerative Hierarchical Clustering Algorithm DBSCAN: Traditional Density Center-Based Approach, DBSCAN Algorithm, Strengths and Weaknesses. **(Tan & Vipin)**

OUTCOMES:

- Understand stages in building a Data Warehouse
- Understand the need and importance of preprocessing techniques
- Understand the need and importance of Similarity and dissimilarity techniques
- Analyze and evaluate performance of algorithms for Association Rules.
- Analyze Classification and Clustering algorithms

TEXT BOOKS:

1. Introduction to Data Mining: Pang-Ning Tan & Michael Steinbach, Vipin Kumar, Pearson.
2. Data Mining concepts and Techniques, 3/e, Jiawei Han, Michel Kamber, Elsevier.

REFERENCE BOOKS:

1. Data Mining Techniques and Applications: An Introduction, Hongbo Du, Cengage Learning.
2. Data Mining : Vikram Pudi and P. Radha Krishna, Oxford.
3. Data Mining and Analysis - Fundamental Concepts and Algorithms; Mohammed J. Zaki, Wagner Meira, Jr, Oxford
4. Data Warehousing Data Mining & OLAP, Alex Berson, Stephen Smith, TMH.

INDUSTRIAL ROBOTICS

OPEN ELECTIVE

Course Objectives:

1. To give students practice in applying their knowledge of mathematics, science, and Engineering and to expand this knowledge into the vast area of robotics.
2. The students will be exposed to the concepts of robot kinematics, Dynamics, Trajectory planning.
3. Mathematical approach to explain how the robotic arm motion can be described.
4. The students will understand the functioning of sensors and actuators.

UNIT-I

INTRODUCTION: Automation and Robotics, CAD/CAM and Robotics – An over view of Robotics – present and future applications – classification by coordinate system and control system.

UNIT – II

COMPONENTS OF THE INDUSTRIAL ROBOTICS: Function line diagram representation of robot arms, common types of arms. Components, Architecture, number of degrees of freedom – Requirements and challenges of end effectors, determination of the end effectors, comparison of Electric, Hydraulic and Pneumatic types of locomotion devices.

UNIT – III

MOTION ANALYSIS: Homogeneous transformations as applicable to rotation and translation – problems.

MANIPULATOR KINEMATICS: Specifications of matrices, D-H notation joint coordinates and world coordinates Forward and inverse kinematics – problems.

UNIT – IV

Differential transformation and manipulators, Jacobians – problems

Dynamics: Lagrange – Euler and Newton – Euler formulations – Problems.

UNIT V

General considerations in path description and generation. Trajectory planning and avoidance of obstacles, path planning, Skew motion, joint integrated motion –straight line motion – Robot programming, languages and software packages-description of paths with a robot programming language..

UNIT VI

ROBOT ACTUATORS AND FEED BACK COMPONENTS:

Actuators: Pneumatic, Hydraulic actuators, electric & stepper motors.

Feedback components: position sensors – potentiometers, resolvers, encoders – Velocity sensors.

ROBOT APPLICATIONS IN MANUFACTURING: Material Transfer - Material handling, loading and unloading- Processing - spot and continuous arc welding & spray painting - Assembly and Inspection.

TEXT BOOKS:

1. Industrial Robotics / Groover M P / Pearson Edu.
2. Robotics and Control / Mittal R K & Nagrath I J / TMH.

REFERENCES:

1. Robotics / Fu K S/ McGraw Hill.
2. Robotic Engineering / Richard D. Klafter, Prentice Hall
3. Robot Analysis and Control / H. Asada and J.J.E. Slotine / BSP Books Pvt.Ltd.
4. Introduction to Robotics / John J Craig / Pearson Edu.

Course outcomes:

Upon successful completion of this course you should be able to:

1. Identify various robot configuration and components,
2. Select appropriate actuators and sensors for a robot based on specific application
3. Carry out kinematic and dynamic analysis for simple serial kinematic chains
4. Perform trajectory planning for a manipulator by avoiding obstacles.

POWER ELECTRONICS

(Open Elective)

Preamble:

The usage of power electronics in day to day life has increased in recent years. It is important for student to understand the fundamental principles behind all these converters. This course covers characteristics of semiconductor devices, ac/dc, dc/dc, ac/ac and dc/ac converters. The importance of using pulse width modulated techniques to obtain high quality power supply (dc/ac converter) is also discussed in detail in this course.

Learning Objectives:

- To study the characteristics of various power semiconductor devices and to design firing circuits for SCR.
- To understand the operation of single phase half wave and full-wave converters
- To understand the operation of different types of DC-DC converters.
- To understand the operation of inverters and application of PWM techniques for voltage control and harmonic mitigation.
- To understand the operation of AC-AC converters and switch mode power supplies operation.

UNIT-I

Power Semi-Conductor Devices

Thyristors–Silicon controlled rectifiers (SCR's) – Characteristics of power MOSFET and power IGBT – Basic theory of operation of SCR–Static characteristics – Turn on and turn off methods–Dynamic characteristics of SCR – Snubber circuit design – Firing circuits for SCR

UNIT-II

AC-DC Single-Phase Converters

Single phase half wave controlled rectifiers – R load and RL load with and without freewheeling diode – Single Phase full wave controlled rectifiers – center tapped configuration and bridge configuration – R load and RL load with and without freewheeling diode – Effect of source inductance in 1-phase fully controlled bridge rectifier.

UNIT-III

DC-DC Converters

Buck Converter operation – Time ratio control and current limit control strategies–Voltage and current waveforms– Derivation of output voltage –Boost converter operation –Voltage and current waveforms–Derivation of output voltage – Buck-Boost converter operation –Voltage and current waveforms – Principle operation of forward and fly back converters

UNIT – IV

DC-AC Converters

Single phase inverters–Unipolar and bipolar switching – Single phase half bridge and full bridge inverters with R and RL loads – PWM techniques– Sine triangular PWM technique– amplitude and frequency modulation Indices – Harmonic analysis.

UNIT – V

AC – AC Single-Phase Converters

Static V-I characteristics of TRIAC and modes of operation – Single phase AC-AC regulator phase angle control and integrated cycle control with R and RL load – For continuous and discontinuous conduction – Principle of operation of Cyclo-Converters

UNIT – VI

Switch Mode Power Supplies

Overview of Switching Power Supplies – Linear Power Supplies – DC to DC converters with electrical isolation – Control of Switch Mode DC Supplies – PWM duty ratio control – Current mode control – Power Supply Protection

Learning Outcomes:

Student should be able to

- Explain the characteristics of various power semiconductor devices and analyse the static and dynamic characteristics of SCR's.
- Design firing circuits for SCR.
- Able to explain the operation of single phase half wave and full-wave converters
- Analyse the operation of different types of DC-DC converters.
- Explain the operation of inverters and application of PWM techniques for voltage control and harmonic mitigation.
- Analyse the operation of AC-AC converters.
- Able to explain switch mode power supplies operation and control

Text Books:

1. Power Electronics: Circuits, Devices and Applications – by M. H. Rashid, Prentice Hall of India, 2nd edition, 1998
2. Power Electronics: Essentials & Applications by L.Umanand, Wiley, Pvt. Limited, India, 2009

Reference Books:

1. Power Electronics: converters, applications & design -by Nedmohan, Tore M. Undeland, Robbins by Wiley India Pvt. Ltd.
2. Elements of Power Electronics–Philip T.Krein.oxford.
3. Power Electronics – by P.S.Bhimbra, Khanna Publishers.
4. Power Electronics handbook by Muhammad H.Rashid, Elsevier.
5. Power Converter Circuits -by William Shepherd, Li zhang, CRC Taylor & Francis Group.

**BIO-MEDICAL ENGINEERING
(OPEN ELECTIVE)**

UNIT-I:

INTRODUCTION TO BIOMEDICAL INSTRUMENTATION: Age of Biomedical Engineering, Development of Biomedical Instrumentation, Man Instrumentation System, Components of the Man-Instrument System, Physiological System of the Body, Problems Encountered in Measuring a Living System, Sources of Bioelectric Potentials, Muscle, Bioelectric Potentials, Sources of Bioelectric Potentials, Resting and Action Potentials, Propagation of Action Potential, Bioelectric Potentials-ECG, EEG and EMG, Evoked Responses.

UNIT-II:

ELECTRODES AND TRANSDUCERS: Introduction, Electrode Theory, Biopotential Electrodes, Examples of Electrodes, Basic Transducer Principles, Biochemical Transducers, The Transducer and Transduction Principles, Active Transducers, Passive Transducers, Transducers for Biomedical Applications, Pulse Sensors, Respiration Sensor, Transducers with Digital Output.

UNIT-III:

CARDIOVASCULAR SYSTEM AND MEASUREMENTS: The Heart and Cardiovascular System, Electro Cardiography, Blood Pressure Measurement, Measurement of Blood Flow and Cardiac Output, Measurement of Heart Sound, Plethysmography.

MEASUREMENTS IN THE RESPIRATORY SYSTEM: The Physiology of The Respiratory System, Tests and Instrumentation for The Mechanics of Breathing, Respiratory Therapy Equipment.

UNIT-IV:

PATIENT CARE AND MONITORING: Elements of Intensive-Care Monitoring, Patient Monitoring Displays, Diagnosis, Calibration and Repair ability of Patient-Monitoring Equipment, Other Instrumentation for Monitoring Patients, Organization of the Hospital for Patient-Care Monitoring, Pacemakers, Defibrillators, Radio Frequency Applications of Therapeutic use.

THERAPEUTIC AND PROSTHETIC DEVICES: Audiometers and Hearing Aids, Myoelectric Arm, Laparoscope, Ophthalmology Instruments, Anatomy of Vision, Electrophysiological Tests, Ophthalmoscope, Tonometer for Eye Pressure Measurement, Diathermy, Clinical Laboratory Instruments, Biomaterials, Stimulators.

UNIT-V:

DIAGNOSTIC TECHNIQUES AND BIO-TELEMETRY: Principles of Ultrasonic Measurement, Ultrasonic Imaging, Ultrasonic Applications of Therapeutic Uses, Ultrasonic Diagnosis, X-Ray and Radio-Isotope Instrumentations, CAT Scan, Emission Computerized Tomography, MRI, Introduction to Biotelemetry, Physiological Parameters Adaptable to Biotelemetry, The Components of Biotelemetry System, Implantable Units, Telemetry for ECG Measurements during Exercise, Telemetry for Emergency Patient Monitoring

UNIT-VI:

MONITORS, RECORDERS AND SHOCK HAZARDS: Biopotential Amplifiers, Monitors, Recorders, Shock Hazards and Prevention, Physiological Effects and Electrical Current, Shock Hazards from Electrical Equipment, Methods of Accident Prevention, Isolated Power Distribution System.

Text Books:

1. "Bio-Medical Electronics and Instrumentation", Onkar N. Pandey, Rakesh Kumar, Katson Books.
2. "Bio-Medical Instrumentation", Cromewell , Wiebell, Pfeiffer

References:

1. "Introduction to Bio-Medical Equipment Technology", 4th Edition, Joseph J. Carr, John M. Brown, Pearson Publications.
2. "Hand Book of Bio-Medical Instrumentation", Khandapur. McGrawHill

ARTIFICIAL NEURAL NETWORKS

OPEN ELECTIVE

Course Objectives:

1. To Introduce the concept of Artificial Neural Networks , Characteristics, Models of Neuron, Learning Rules, Learning Methods, Stability and Convergence
2. To study the basics of Pattern Recognition and Feed forward Neural Networks
3. To study the basics of Feedback neural networks and Boltzmann machine
4. To introduce the Analysis of Feedback layer for different output functions, Pattern Clustering and Mapping networks
5. To study the Stability, Plasticity, Neocognitron and Different applications of Neural Networks

UNIT-I : Basics of Artificial Neural Networks

Introduction: Biological Neural Networks, Characteristics of Neural Networks, Models of Neuron, Topology, Basic Learning Rules

Activation and Synaptic Dynamics: Activation Dynamic Models, Synaptic Dynamic Models, Learning Methods, Stability & Convergence, Recall in Neural Networks

UNIT-II: Functional Units of ANN for Pattern Recognition Tasks: Pattern Recognition problem Basic Fundamental Units, Pattern Recognition Tasks by the Functional Units

Feed forward Neural Networks: Analysis of Pattern Association Networks, Analysis of Pattern Classification Networks, Analysis of Pattern Mapping Networks

UNIT-III:

Feedback Neural Networks: Analysis of linear auto adaptive feed forward networks, Analysis of pattern storage Networks, Stochastic Networks & Stimulated Annealing, Boltzmann machine

UNIT-IV:

Competitive Learning Neural Networks: Components of a Competitive Learning Network, Analysis of Feedback layer for Different Output Functions, Analysis of Pattern Clustering Networks and Analysis of Feature Mapping Network

UNIT-V:

Architectures for Complex Pattern Recognition Tasks: Associative memory, Pattern mapping Stability – Plasticity dilemma: ART, temporal patterns, Pattern visibility: Neocognitron

UNIT-VI:

Applications of Neural Networks: Pattern classification, Associative memories, Optimization, Applications in Image Processing, Applications in decision making

Text Book

1. B.Yagnanarayana“Artificial Neural Networks”, PHI

Reference Book

1. Laurene Fausett ,“Fundamentals of Neural Networks”, Pearson Education
2. Simon Haykin , “Neural Networks”, Second Edition

Course Outcomes

1. This Course introduces Artificial Neural Networks and Learning Rules and Learning methods
2. Feed forward and Feedback Neural Networks are introduced
3. Applications of Neural Networks in different areas are introduced

III Year - II Semester

L	T	P	C
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MICROPROCESSORS AND MICROCONTROLLERS LAB

LIST OF EXPERIMENTS

PART- A: (Minimum of 5 Experiments has to be performed)

8086 Assembly Language Programming using Assembler Directives

15. Sorting.
16. Multibyte addition/subtraction
17. Sum of squares/cubes of a given n-numbers
18. Addition of n-BCD numbers
19. Factorial of given n-numbers
20. Multiplication and Division operations
21. Stack operations
22. BCD to Seven segment display codes

PART- B: (Minimum of 3 Experiments has to be performed)

8086 Interfacing

1. Hardware/Software Interrupt Application
2. A/D Interface through Intel 8255
3. D/A Interface through Intel 8255
4. Keyboard and Display Interface through Intel 8279
5. Generation of waveforms using Intel 8253/8254

PART- C: (Minimum of 3 Experiments has to be performed)

8051 Assembly Language Programs

1. Finding number of 1's and number of 0's in a given 8-bit number
2. Addition of even numbers from a given array
3. Ascending / Descending order
4. Average of n-numbers

PART-D: (Minimum of 3 Experiments has to be performed)

8051 Interfacing

1. Switches and LEDs
2. 7-Segment display (multiplexed)
3. Stepper Motor Interface
4. Traffic Light Controller

Equipment Required:

1. Regulated Power supplies
2. Analog/Digital Storage Oscilloscopes
3. 8086 Microprocessor kits
4. 8051 microcontroller kits
5. ADC module
6. DAC module
7. Stepper motor module
8. Keyboard module
9. LED, 7-Segment Units
10. Digital Multimeters
11. ROM/RAM Interface module
12. Bread Board etc.

III Year - II Semester

L	T	P	C
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VLSI LABORATORY

Note: The students are required to design the schematic diagrams using CMOS logic and to draw the layout diagrams to perform the following experiments using 130nm technology with the Industry standard EDA Tools.

List of Experiments:

- i. Design and Implementation of an Universal Gates
- ii. Design and Implementation of an Inverter
- iii. Design and Implementation of Full Adder
- iv. Design and Implementation of Full Subtractor
- v. Design and Implementation of Decoder
- vi. Design and Implementation of RS-Latch
- vii. Design and Implementation of D-Latch
- viii. Design and Implementation asynchronous counter
- ix. Design and Implementation of static RAM cell
- x. Design and Implementation of 8 bit DAC using R-2R ladder network

Software Required:

- i. Mentor Graphics Software / Equivalent Industry Standard Software.
- ii. Personal computer system with necessary software to run the programs and to implement.

III Year - II Semester

L	T	P	C
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DIGITAL COMMUNICATIONS LAB

1. Time division multiplexing.
2. Pulse code modulation.
3. Differential pulse code modulation.
4. Delta modulation.
5. Frequency shift keying.
6. Phase shift keying .
7. Differential phase shift keying.
8. Companding
9. Source Encoder and Decoder
10. Linear Block Code-Encoder and Decoder
11. Binary Cyclic Code – Encoder and Decoder
12. Convolution Code – Encoder and Decoder

Equipment required for Laboratories:

1. RPS – 0 – 30 V
2. CRO – 0 – 20 M Hz.
3. Function Generators – 0 – 1 M Hz
4. RF Generators – 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Digital Communication
7. Components

III Year - II Semester

L	T	P	C
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INTELLECTUAL PROPERTY RIGHTS AND PATENTS

UNIT I: Introduction to Intellectual Property Rights (IPR)

Concept of Property - Introduction to IPR – International Instruments and IPR - WIPO - TRIPS – WTO -Laws Relating to IPR - IPR Tool Kit - Protection and Regulation - Copyrights and Neighboring Rights – Industrial Property – Patents - Agencies for IPR Registration – Traditional Knowledge –Emerging Areas of IPR - Layout Designs and Integrated Circuits – Use and Misuse of Intellectual Property Rights.

UNIT II: Copyrights and Neighboring Rights

Introduction to Copyrights – Principles of Copyright Protection – Law Relating to Copyrights - Subject Matters of Copyright – Copyright Ownership – Transfer and Duration – Right to Prepare Derivative Works –Rights of Distribution – Rights of Performers – Copyright Registration – Limitations – Infringement of Copyright – Relief and Remedy – Case Law - Semiconductor Chip Protection Act.

UNIT III: Patents

Introduction to Patents - Laws Relating to Patents in India – Patent Requirements – Product Patent and Process Patent - Patent Search - Patent Registration and Granting of Patent - Exclusive Rights – Limitations - Ownership and Transfer — Revocation of Patent – Patent Appellate Board - Infringement of Patent – Double Patenting — Patent Cooperation Treaty – New developments in Patents – Software Protection and Computer related Innovations.

UNIT IV: Trademarks

Introduction to Trademarks – Laws Relating to Trademarks – Functions of Trademark – Distinction between Trademark and Property Mark – Marks Covered under Trademark Law - Trade Mark Registration – Trade Mark Maintenance – Transfer of rights - Deceptive Similarities - Likelihood of Confusion - Dilution of Ownership – Trademarks Claims and Infringement – Remedies – Passing Off Action.

UNIT V: Trade Secrets

Introduction to Trade Secrets – General Principles - Laws Relating to Trade Secrets - Maintaining Trade Secret – Physical Security – Employee Access Limitation – Employee Confidentiality Agreements – Breach of Contract – Law of Unfair Competition – Trade Secret Litigation – Applying State Law.

UNIT VI: Cyber Law and Cyber Crime

Introduction to Cyber Law – Information Technology Act 2000 - Protection of Online and Computer Transactions - E-commerce - Data Security – Authentication and Confidentiality - Privacy - Digital Signatures – Certifying Authorities - Cyber Crimes - Prevention and Punishment – Liability of Network Providers.

- Relevant Cases Shall be dealt where ever necessary.

References:

1. Intellectual Property Rights (Patents & Cyber Law), Dr. A. Srinivas. Oxford University Press, New Delhi.
2. Deborah E.Bouchoux: Intellectual Property, Cengage Learning, New Delhi.
3. PrabhuddhaGanguli: Intellectual Property Rights, Tata Mc-Graw –Hill, New Delhi
4. Richard Stim: Intellectual Property, Cengage Learning, New Delhi.
5. Kompal Bansal &Parishit Bansal Fundamentals of IPR for Engineers, B. S. Publications (Press).
6. Cyber Law - Texts & Cases, South-Western's Special Topics Collections.
7. R.Radha Krishnan, S.Balasubramanian: Intellectual Property Rights, Excel Books. New Delhi.
8. M.Ashok Kumar and MohdIqbal Ali: Intellectual Property Rights, Serials Pub.